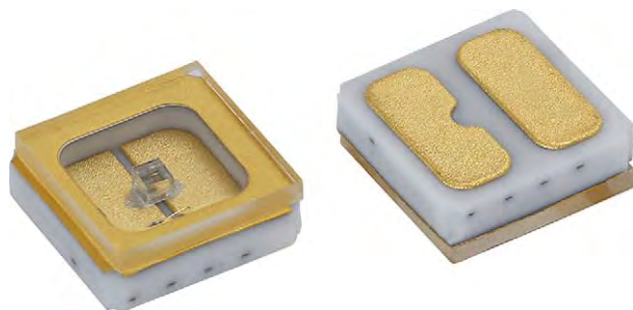


UVC Emitting Diode in SMD Package



DESCRIPTION

VLMU35CL..-280-120 is a ceramic based low power UVC LED with quartz window for long life time. The package size is 3.5 mm x 3.5 mm x 1.2 mm and the radiant power typically 3.2 mW at 20 mA in a wavelength range of 265 nm to 285 nm.

PRODUCT GROUP AND PACKAGE DATA

- Product group: LED
- Package: SMD ceramic
- Product series: standard power UV LED
- Angle of half intensity: $\pm 60^\circ$
- Lead-finishing: Au

FEATURES

- Ceramic SMT package with quartz window
- Dimension (L x W x H) in mm: 3.5 x 3.5 x 1.2
- Forward current: up to 70 mA
- Radiant power (typ.): 3.2 mW at 20 mA and 7.5 mW at 50 mA
- Leads / terminations finish: gold plated (Au)
- Reflow soldering method
- MSL 3 according to J-STD-020
- Material categorization: for definitions of compliance please see www.vishay.com/doc?99912



RoHS
COMPLIANT
HALOGEN
FREE
GREEN
(5-2008)

APPLICATIONS

- Sterilization
- Medical application
- Sensing of gases, germs, DNA, ...

SAFETY ADVICES

Depending on the mode of operation, these devices emit highly concentrated non visible ultraviolet light which can be hazardous to the human eye and skin. Products which incorporate these devices have to follow the safety precautions given in IEC 62471 "Photobiological Safety of Lamps and Lamp Systems".

PARTS TABLE

PART	COLOR	RADIANT POWER (mW)			at I_F (mA)	WAVELENGTH (nm)			at I_F (mA)	FORWARD VOLTAGE (V)			at I_F (mA)	TECHNOLOGY
		MIN.	TYP.	MAX.		MIN.	TYP.	MAX.		MIN.	TYP.	MAX.		
VLMU35CL00-280-120	Ultraviolet	1.8	3.2	-	20	265	278	285	20	4	5	6.5	20	AlGaIn

ABSOLUTE MAXIMUM RATINGS ($T_{amb} = 25^\circ\text{C}$, unless otherwise specified)

PARAMETER	TEST CONDITION	SYMBOL	VALUE	UNIT
DC forward current		I_F	70	mA
Power dissipation		P_V	0.45	W
Reverse voltage			Not designed for reverse operation	
Electrostatic discharge	HBM: MIL-STD-883 C 3B	ESD	2000	V
Junction temperature		T_j	+85	$^\circ\text{C}$
Operating temperature range		T_{amb}	-40 to +80	$^\circ\text{C}$
Storage temperature range		T_{stg}	-40 to +100	$^\circ\text{C}$
Solder temperature		T_{sol}	260	$^\circ\text{C}$

**OPTICAL AND ELECTRICAL CHARACTERISTICS** ($T_{amb} = 25\text{ }^{\circ}\text{C}$, unless otherwise specified)
VLMU35CL..-280-120, ULTRAVIOLET

PARAMETER	TEST CONDITION	SYMBOL	MIN.	TYP.	MAX.	UNIT
Forward voltage	$I_F = 20\text{ mA}$	V_F	4	5	6.5	V
Radiant power	$I_F = 20\text{ mA}$	ϕ_e	1.8	3.2	-	mW
	$I_F = 50\text{ mA}$		-	7.5	-	
Ratio of radiant intensity/radiant power	$I_F = 20\text{ mA}$	I_e/ϕ_e	-	0.25	-	sr^{-1}
Peak wavelength	$I_F = 20\text{ mA}$	λ_p	265	278	285	nm
Angle of half intensity	$I_F = 20\text{ mA}$	ϕ	-	± 60	-	$^{\circ}$
Thermal resistance junction to ambient	Soldered on $20 \times 20 \times 1.7$ (in mm) Al MCPCB	R_{thJA}	-	46	-	K/W

Note

- Tolerances: $\pm 11\%$ for ϕ_e , $\pm 0.1\text{ V}$ for V_F , $\pm 3\text{ nm}$ for λ_p

RADIANT POWER CLASSIFICATION ($I_F = 20\text{ mA}$)

GROUP	MIN.	MAX.	UNIT
X2	1.8	2.1	mW
X3	2.1	2.5	
X4	2.5	3.0	
X5	3.0	3.5	
X6	3.5	4.1	
X7	4.1	4.8	
X8	4.8	5.6	

PEAK WAVELENGTH CLASSIFICATION ($I_F = 20\text{ mA}$)

GROUP	MIN.	MAX.	UNIT
W1	265	285	nm

FORWARD VOLTAGE CLASSIFICATION ($I_F = 20\text{ mA}$)

GROUP	MIN.	MAX.	UNIT
V1	4.0	4.5	V
V2	4.5	5.0	
V3	5.0	5.5	
V4	5.5	6.0	
V5	6.0	6.5	

Note

- In order to ensure availability, single groups for radiant intensity, wavelength, and forward voltage will not be orderable. Only one group for radiant intensity, wavelength, and forward voltage will be shipped in any one reel

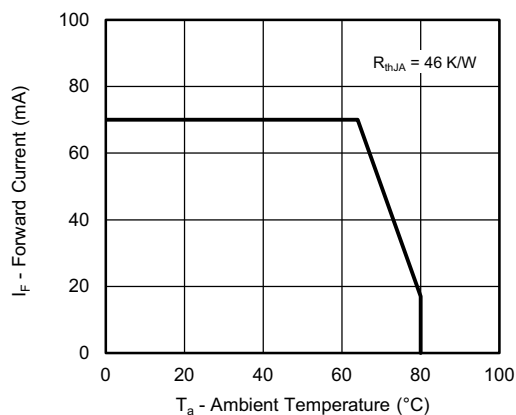
TYPICAL CHARACTERISTICS ($T_{amb} = 25\text{ }^{\circ}\text{C}$, unless otherwise specified)


Fig. 1 - Maximum Forward Current vs. Solder Point Temperature

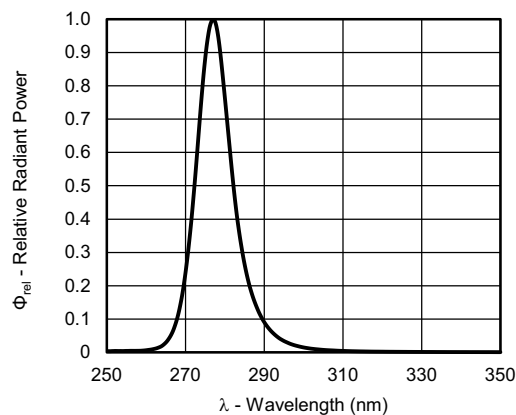


Fig. 4 - Relative Spectral Power vs. Wavelength

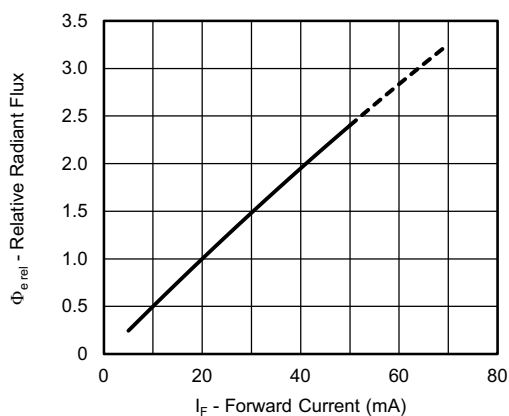


Fig. 2 - Relative Radiant Power vs. Forward Current

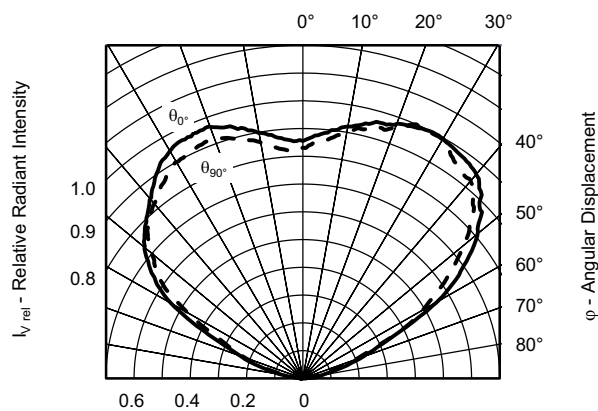


Fig. 5 - Relative Radiant Intensity vs. Angular Displacement

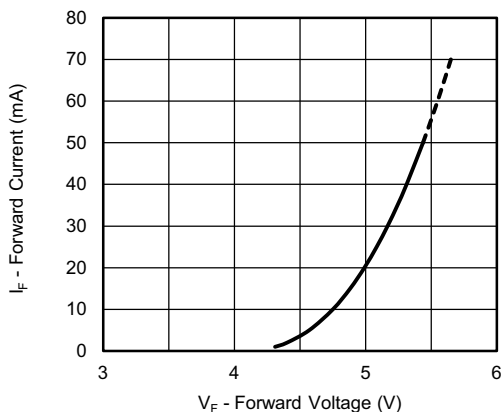


Fig. 3 - Forward Current vs. Forward Voltage

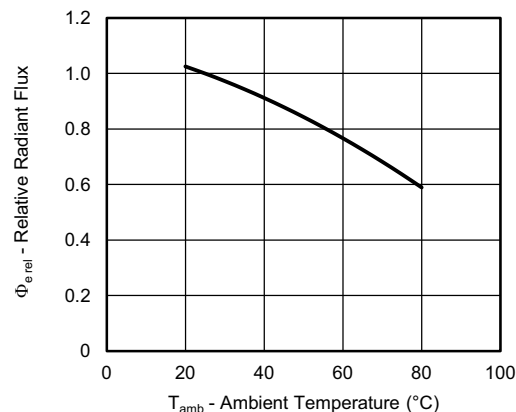
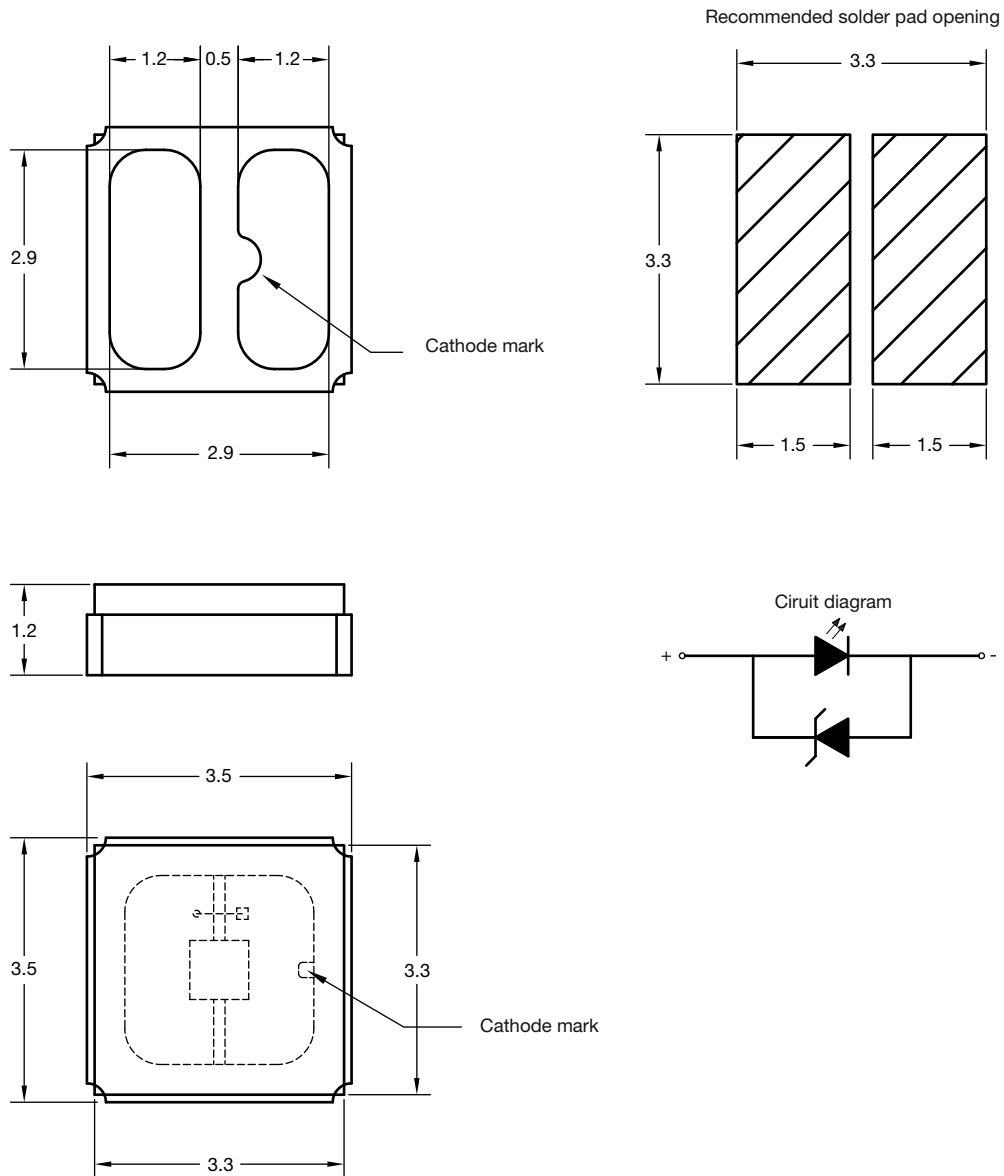
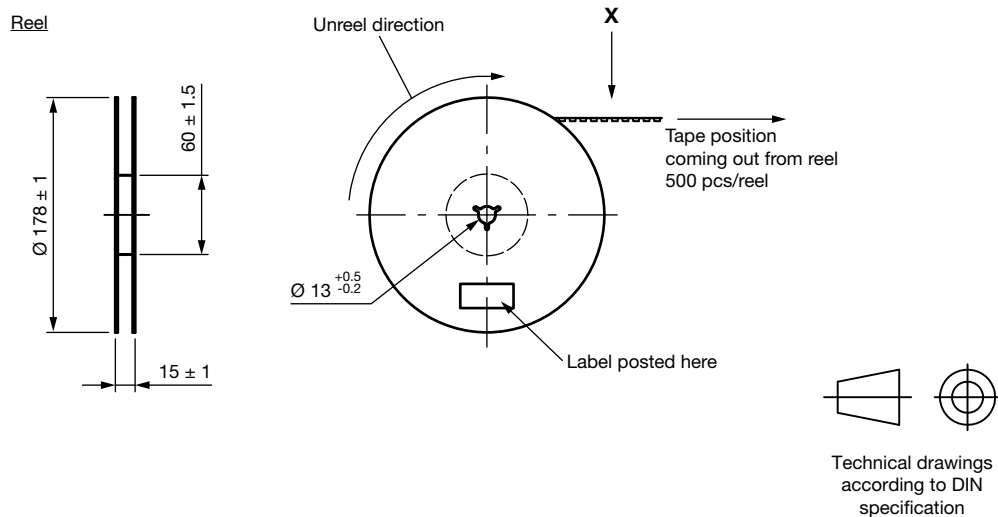
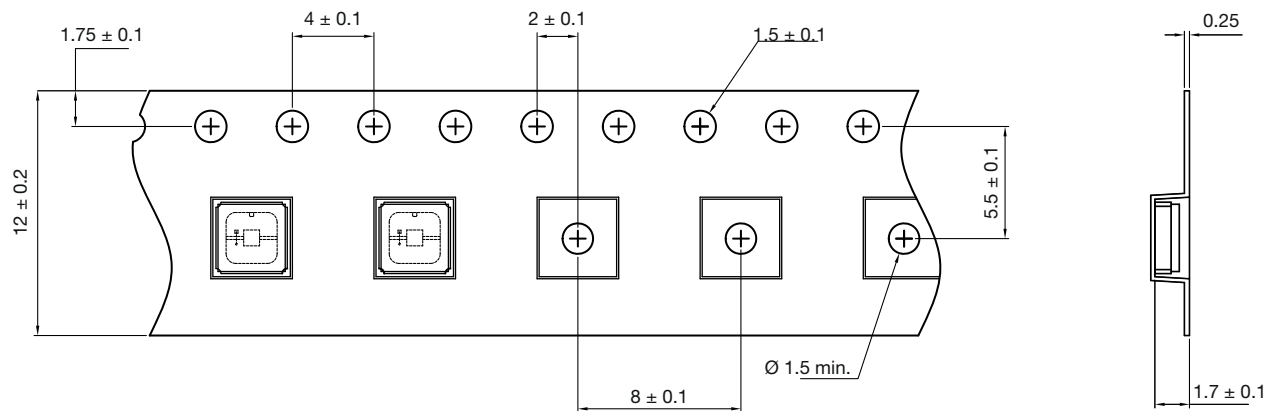
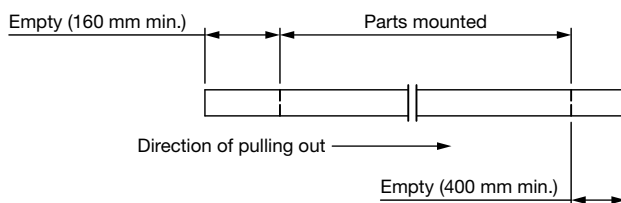


Fig. 6 - Relative Radiant Flux vs. Ambient Temperature



PACKAGE DIMENSIONS in millimeters



TAPE AND REEL DIMENSIONS in millimeters

Leader and trailer tape


SOLDERING PROFILE

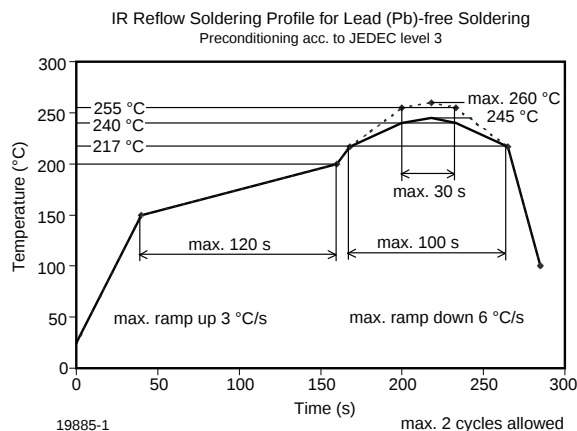


Fig. 7 - Vishay Lead (Pb)-free Reflow Soldering Profile
(according to J-STD-020C)

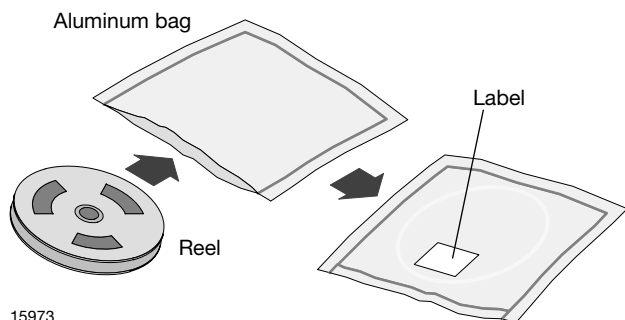
BAR CODE PRODUCT LABEL (example only)



- 2D barcode
- Vishay part number
- Quantity
- SEL = selection code (binning)
- Code of manufacturing plant
- Batch = date code: year / week / plant code
- Region code
- SL = sales location
- Terminations finishing
- Lead (Pb)-free symbol
- Halogen-free symbol
- RoHS symbol

DRY PACKING

The reel is packed in an anti-humidity bag to protect the devices from absorbing moisture during transportation and storage.



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FINAL PACKING

The sealed reel is packed into a cardboard box. A secondary cardboard box is used for shipping purposes.

RECOMMENDED METHOD OF STORAGE

Dry box storage is recommended as soon as the aluminum bag has been opened to prevent moisture absorption. The following conditions should be observed, if dry boxes are not available:

- Storage temperature 10 °C to 30 °C
- Storage humidity ≤ 60 % RH max.

After more than 168 h under these conditions moisture content will be too high for reflow soldering.

In case of moisture absorption, the devices will recover to the former condition by drying under the following condition:

192 h at 40 °C + 5 °C / - 0 °C and < 5 % RH (dry air / nitrogen) or

24 h at 60 °C + 5 °C and < 5 % RH for all device containers or

24 h at 100 °C + 5 °C not suitable for reel or tubes.

An EIA JEDEC® standard JESD22-A112 level 3 label is included on all dry bags.



17028-2

Example of JESD22-A112 level 3 label

ESD PRECAUTION

Proper storage and handling procedures should be followed to prevent ESD damage to the devices especially when they are removed from the antistatic shielding bag. Electrostatic sensitive devices warning labels are on the packaging.

VISHAY SEMICONDUCTORS STANDARD BAR CODE LABELS

The Vishay Semiconductors standard bar code labels are printed at final packing areas. The labels are on each packing unit and contain Vishay Semiconductors specific data.



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