

# LM6104 Quad Gray Scale Current Feedback Amplifier

## **General Description**

The LM6104 quad amplifier meets the requirements of battery operated liquid crystal displays by providing high speed while maintaining low power consumption.

Combining this high speed with high integration, the LM6104 conserves valuable board space in portable systems with a cost effective, surface mount quad package.

Built on National's advanced high speed VIPTM (Vertically Integrated PNP) process, the LM6104 current feedback architecture is easily compensated for speed and loading conditions. These features make the LM6104 ideal for buffering grey levels in liquid crystal displays.

## Features (Typical unless otherwise noted)

■ Low power |<sub>S</sub> = 8

 $I_S = 875 \,\mu\text{A/amplifier}$ 

■ Slew rate

100V/µs 30 **M**Hz

-3dB bandwidth (R<sub>F</sub> = 1 kΩ)
 High output drive

 $\pm\,\text{5V}$  into  $100\Omega$ 

Wide operating rangeHigh integration

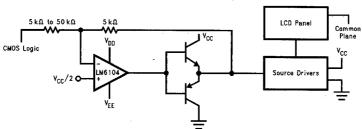
 $V_S = 5V \text{ to } \pm 12V$ Quad surface mount

## **Applications**

- Grey level buffer for liquid crystal displays
- Column buffer for portable LCDs
- Video distribution amplifiers, video line drivers
- Hand-held, high speed signal conditioning

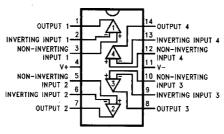
# **Typical Application**

#### **LCD Buffer Application for Grey Levels**



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# **Connection Diagram**



Order Number LM6104M See NS Package Number M14A TL/H/11979-2

## Absolute Maximum Ratings (Note 1)

if Military/Aerospace specified devices are required, please contact the National Semiconductor Sales Office/Distributors for availability and specifications.

Supply Voltage 24V
Differential Input Voltage ±6V

Input Voltage ± Supply Voltage
Inverting Input Current ± Supply Voltage

Soldering Information

Vapor Phase (60s) Infrared (15s) ±6V Operating Ratings

Supply Voltage Range

Storage Temperature Range

ESD Rating (Note 2)

Maximum Junction Temperature

Junction Temperature Range (Note 3)

 $-20^{\circ} \le T_{J} \le +80^{\circ}C$ 

 $-65^{\circ}C \le T_{J} \le +150^{\circ}C$ 

150°C

2000V

4.75V to 24V

## **Electrical Characteristics**

The following specifications apply for V+ = 8V, V- = -5V, R<sub>L</sub> = R<sub>F</sub> =  $2 \text{ k}\Omega$  and  $0^{\circ} \le T_{\text{J}} \le 60^{\circ}\text{C}$  unless otherwise noted.

215°C

220°C

| Symbol         | Parameter                        | Conditions   | LM6104M                |                    |           |
|----------------|----------------------------------|--|------------------------|--------------------|-----------|
|                |                                  |  | Typical<br>(Note 4)    | Limits<br>(Note 5) | Units     |
| Vos            | Input Offset Voltage             |  | 10                     | 30                 | mV max    |
| l <sub>B</sub> | Inverting Input Bias Current     |  | 5.0                    | 20                 | μA max    |
| _              | Non-Inverting Input Bias Current |  | 0.5                    | 2                  | μA max    |
| ls             | Supply Current                   | V <sub>O</sub> = 0V  | 3.5                    | 4.0                | mA max    |
| Isc            | Output Source Current            | $V_{O} = 0V$ $I_{IN(-)} = -100 \mu\text{A}$                                    | 60                     | 45                 | mA<br>min |
|                | Output Sink Current              | $V_{O} = 0V$ $I_{IN(-)} = 100 \mu A$   | 60                     | 45                 | mA<br>min |
| V <sub>O</sub> | Positive Output Swing            | I <sub>IN(-)</sub> = -100 μA   | 6.5                    | 6.1                | V min     |
| Ŭ              | Negative Output Swing            | i <sub>IN(-)</sub> = 100 μA  | -3.5                   | -3.1               | V max     |
| PSRR           | Power Supply Rejection Ratio     | $V_S = \pm 4 \text{ to } \pm 10 \text{V}$                                      | 70                     | 60                 | dB min    |
|                |                                  | 100 mV pp @ 100 kHz  | 40                     | 30                 | dB min    |
| R <sub>T</sub> | Transresistance                  |  | 10                     | 5                  | MΩ min    |
| SR             | Slew Rate                        | (Note 6)   | 100                    | 55                 | V/μs min  |
| BW             | Bandwidth                        | $A_V = -1$ $R_{IN} = R_F = 2 k\Omega$  | 7.5                    | 5.0                | MHz       |
|                | Ámp-to-Amp Isolation             | $R_L = 2 k\Omega$<br>F = 1 MHz   | 60                     |                    | dB        |
| CMVR           | Common Mode Voltage Range        |  | V+ - 1.4V<br>V- + 1.4V |                    | V         |
| CMRR           | Common Mode Rejection Ratio      |  | 60                     |                    | dB        |
| ts             | Settling Time                    | 0.05%, 5V Step, $A_V = -1$<br>$R_F = R_S = 2 \text{ k}\Omega$ , $V_S = \pm 5V$ | 240                    |                    | ns        |

Note 1: Absolute Maximum Ratings indicate limits beyond which damage to the device may occur. DC and AC electrical specifications do not apply when operating the device beyond its rated operating conditions. Operating ratings indicate conditions the device is intended to be functional, but device parameter specifications may not be guaranteed under the conditions.

Note 2: Human body model 1.5 k $\Omega$  and 100 pF. This is a class 2 device rating.

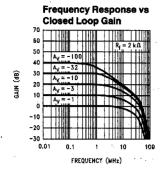
Note 3: Thermal resistance of the SO package is 98°C/W. When operating at TA = 80°C, maximum power dissipation is 700 mW.

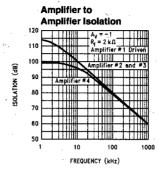
Note 4: Typical values represent the most likely parametric norm.

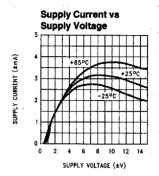
Note 5: All limits guaranteed at operating temperature extremes.

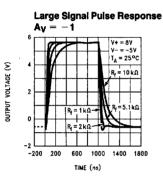
Note 6:  $A_V = -1$  with  $R_{IN} = R_F = 2 \, k\Omega$ . Slew rate is calculated from the 25% to the 75% point on both rising and falling edges. Output swing is -0.6V to +5.6V and 5.6V to 0.6V.

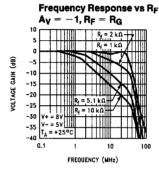
# **Typical Performance Characteristics**

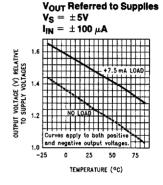


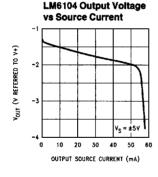


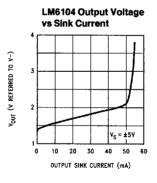












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## **Applications Information**

#### **CURRENT FEEDBACK TOPOLOGY**

The small-signal bandwidth of conventional voltage feed-back amplifiers is inversely proportional to the closed-loop gain based on the gain-bandwidth concept. In contrast, the current feedback amplifier topology, such as the LM6104, enables a signal bandwidth that is relatively independent of the amplifier's gain (see typical curve Frequency Response vs Closed Loop Gain).

#### FEEDBACK RESISTOR SELECTION: RF

Current feedback amplifier bandwidth and slew rate are controlled by R<sub>F</sub>. R<sub>F</sub> and the amplifier's internal compensation capacitor set the dominant pole in the frequency response. The amplifier, therefore, always requires a feedback resistor, even in unity gain.

Bandwidth and slew rate are inversely proportional to the value of  $R_F$  (see typical curve Frequency Response vs  $R_F$ ). This makes the amplifier especially easy to compensate for a desired pulse response (see typical curve Large Signal Pulse Response). Increased capacitive load driving capability is also achieved by increasing the value of  $R_F$ .

The LM6104 has guaranteed performance with a feedback resistor of 2 k $\Omega$ .

#### CAPACITIVE FEEDBACK

It is common to place a small lead capacitor in parallel with feedback resistance to compensate voltage feedback amplifiers. Do not place a capacitor across R<sub>F</sub> to limit the bandwidth of current feedback amplifiers. The dynamic impedance of capacitors in the feedback path of the LM6104, as with any current feedback amplifier, will cause instability.