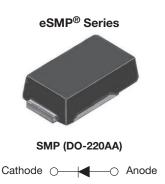


ROHS

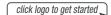
HALOGEN

FREE

Ultrafast Rectifier, 2 A FRED Pt®



DESIGN SUPPORT TOOLS





PRIMARY CHARACTERISTICS					
I _{F(AV)}	2 A				
V_{R}	100 V, 200 V				
V _F at I _F	0.79 V				
I _{FSM}	40 A				
t _{rr} (typ.)	23 ns				
T _J max.	175 °C				
Package	SMP (DO-220AA)				
Circuit configuration	Single				

FEATURES

- Very low profile typical height of 1.0 mm
- · Ideal for automated placement
- · Low forward voltage drop, low power losses
- · Low leakage current
- Meets MSL level 1, per J-STD-020, LF maximum peak of 260 °C
- For PFC, CRM snubber operation
- Material categorization: for definitions of compliance please see <u>www.vishay.com/doc?99912</u>

TYPICAL APPLICATION

For use in high frequency, freewheeling, DC/DC converters, PFC, and in snubber industrial and automotive applications.

MECHANICAL DATA

Case: SMP (DO-220AA)

Molding compound meets UL 94 V-0 flammability rating

Terminals: matte tin plated leads, solderable per

J-STD-002 and JESD 33-N102

Polarity: color band denotes cathode end

ABSOLUTE MAXIMUM RATINGS						
PARAMETER		SYMBOL	TEST CONDITIONS	VALUES	UNITS	
Peak repetitive reverse	VS-2ENH01-M3	V		100	W	
voltage	VS-2ENH02-M3	V_{RRM}		200	V	
Average rectified forward	current	I _{F(AV)}	T _C = 158 °C	2	^	
Non-repetitive peak surge current		I _{FSM}	T _J = 25 °C, 10 ms sine pulse	40	А	
Operating junction and st	torage temperatures	T _J , T _{Stg}		-55 to +175	°C	

ELECTRICAL SPECIFICATIONS (T _J = 25 °C unless otherwise specified)							
PARAMETER		SYMBOL	TEST CONDITIONS	MIN.	TYP.	MAX.	UNITS
Breakdown voltage,	VS-2ENH01-M3	V_{BR} ,	Ι _R = 100 μΑ	100	-	-	V
blocking voltage	VS-2ENH02-M3	V_{R}		200	-	-	
Forward voltage		V _F	I _F = 2 A	-	0.94	1.00	
			I _F = 2 A, T _J = 150 °C	-	0.79	0.84	
Reverse leakage current		I _R	$V_R = V_R$ rated	-	-	2	- μΑ
			T _J = 150 °C, V _R = V _R rated	-	-	20	
Junction capacitance		C _T	V _R = 200 V	-	8	-	pF



DYNAMIC RECOVERY CHARACTERISTICS (T _J = 25 °C unless otherwise specified)							
PARAMETER	SYMBOL	TEST CON	MIN.	TYP.	MAX.	UNITS	
		$I_F = 1.0 \text{ A}, dI_F/dt = 100 \text{ A/}\mu\text{s}, V_R = 30 \text{ V}$		-	23	-	
Reverse recovery time		$I_F = 0.5 \text{ A}, I_R = 1 \text{ A}, I_{rr} = 0.25 \text{ A}$		-	-	28	
	t _{rr}	T _J = 25 °C	$I_F = 2 \text{ A}$ $dI_F/dt = 200 \text{ A/}\mu\text{s}$ $V_R = 100 \text{ V}$	-	16	-	ns A nC
		T _J = 125 °C		-	25	-	
Peak recovery current	I _{RRM}	T _J = 25 °C		-	2.0	-	
		T _J = 125 °C		-	3.1	-	
Reverse recovery charge	Q _{rr}	T _J = 25 °C		-	15	-	
		T _J = 125 °C		-	37	-	

THERMAL - MECHANICAL SPECIFICATIONS							
PARAMETER		SYMBOL	SYMBOL TEST CONDITIONS		TYP.	MAX.	UNITS
Maximum junction temperature ran	•	T _J , T _{Stg}		-55	-	175	°C
Thermal resistar junction to mour	*	R _{thJM} ⁽¹⁾	Infinite heatsink	-	7	9	°C/W
Thermal resistar junction to ambi	*	R _{thJA}	PCB footprint 4.8 mm x 4.8 mm	-	107	-	C/VV
Marking device	VS-2ENH01-M3		Case style SMP (DO-220AA)	2H1			
iviai kii ig device	VS-2ENH02-M3		Case style Sivir (DO-220AA)	2H2			

Note

⁽¹⁾ Thermal resistance junction to mount follows JEDEC® 51-14 transient dual interface test method (TDIM)

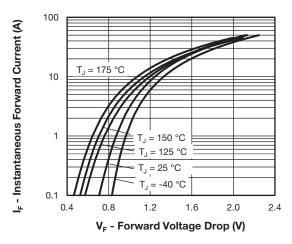


Fig. 1 - Typical Forward Voltage Drop Characteristics

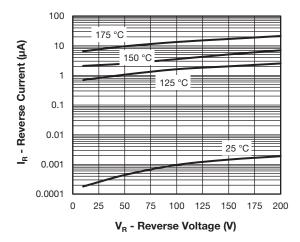


Fig. 2 - Typical Values of Reverse Current vs. Reverse Voltage

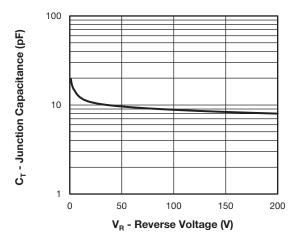


Fig. 3 - Typical Junction Capacitance vs. Reverse Voltage

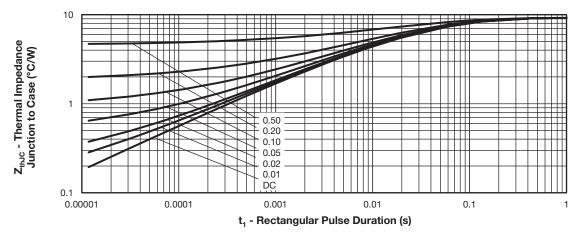


Fig. 4 - Transient Thermal Impedance, Junction to Case

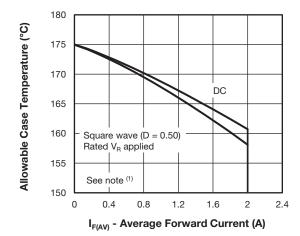


Fig. 5 - Maximum Allowable Case Temperature vs. Average Forward Current

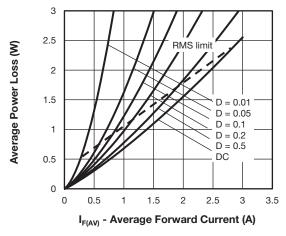


Fig. 6 - Forward Power Loss Characteristics

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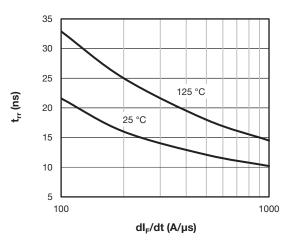


Fig. 7 - Typical Reverse Recovery Time vs. dl_F/dt

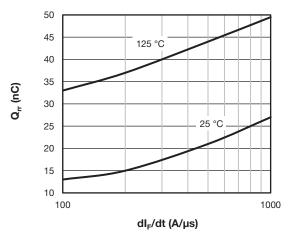
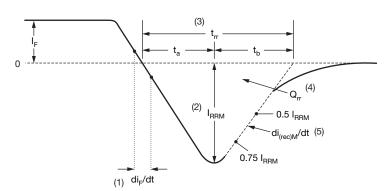


Fig. 8 - Typical Stored Charge vs. dl_F/dt

Note

 $^{(1)}$ Formula used: T_C = T_J - (Pd + Pd_{REV}) x R_{thJC}; Pd = forward power loss = I_{F(AV)} x V_{FM} at (I_{F(AV)}/D) (see fig. 5); Pd_{REV} = inverse power loss = V_{R1} x I_R (1 - D); I_R at V_{R1} = rated V_R



- (1) di_F/dt rate of change of current through zero crossing
- (2) I_{RRM} peak reverse recovery current
- (3) $\rm t_{rr}$ reverse recovery time measured from zero crossing point of negative going $\rm I_F$ to point where a line passing through 0.75 $\rm I_{RRM}$ and 0.50 $\rm I_{RRM}$ extrapolated to zero current.
- (4) \mathbf{Q}_{rr} area under curve defined by \mathbf{t}_{rr} and \mathbf{I}_{RRM}

$$Q_{rr} = \frac{t_{rr} \times I_{RRM}}{2}$$

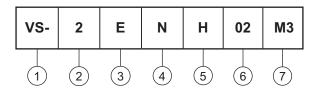
(5) $di_{(rec)M}/dt$ - peak rate of change of current during t_b portion of t_{rr}

Fig. 9 - Reverse Recovery Waveform and Definitions



ORDERING INFORMATION TABLE

Device code



1 - Vishay Semiconductors product

2 - Current rating (2 = 2 A)

3 - Circuit configuration:

E = single diode

4 - N = SMP package

5 - Process type,

H = ultrafast recovery

6 - Voltage code (02 = 200 V)

7 - M3 = halogen-free, RoHS-compliant, and terminations lead (Pb)-free

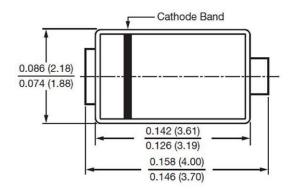
ORDERING INFORMATION (Example)						
PREFERRED P/N	PREFERRED PACKAGE CODE	MINIMUM ORDER QUANTITY	PACKAGING DESCRIPTION			
VS-2ENH01-M3/84A	84A	3000	7" diameter plastic tape and reel			
VS-2ENH01-M3/85A	85A	10 000	13" diameter plastic tape and reel			
VS-2ENH02-M3/84A	84A	3000	7" diameter plastic tape and reel			
VS-2ENH02-M3/85A	85A	10 000	13" diameter plastic tape and reel			

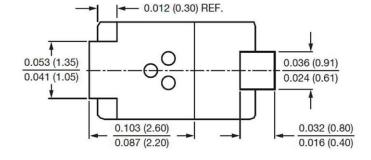
LINKS TO RELATED DOCUMENTS					
Dimensions <u>www.vishay.com/doc?96547</u>					
Part marking information	www.vishay.com/doc?96574				
Packaging information	www.vishay.com/doc?88869				
SPICE model	www.vishay.com/doc?96551				

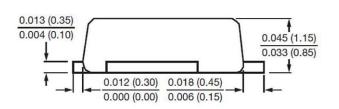


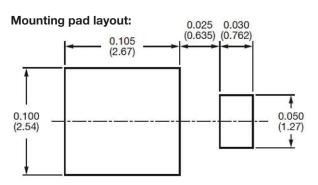
SMP (DO-220AA)

DIMENSIONS in inches (millimeters)











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