

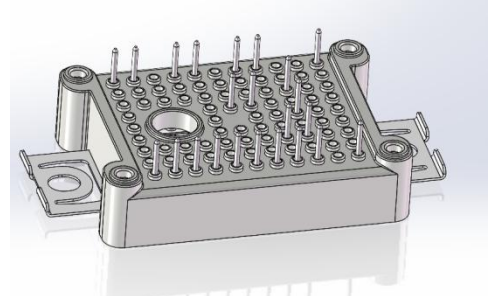
JLPI10B120RE1E7SN

LE1 PACK module with Trench/Fieldstop IGBT and Emitter Controlled diode and NTC



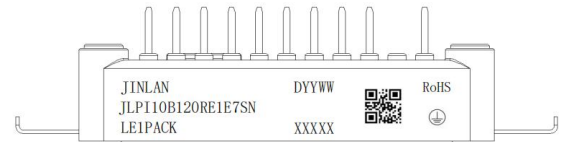
Features

- Low $V_{CE(sat)}$ Trench IGBT technology
- 10 μ s short circuit capability
- $V_{CE(sat)}$ with positive temperature coefficient
- Maximum junction temperature 175 $^{\circ}$ C
- Low inductance case
- Al₂O₃ substrate with low thermal resistance
- Solder Contact Technology
- Rugged mounting due to integrated mounting clamps



LE1 Pack

MARKING DIAGRAM

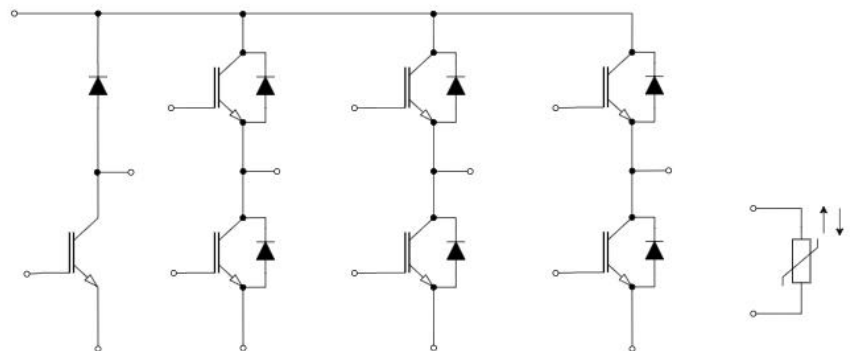
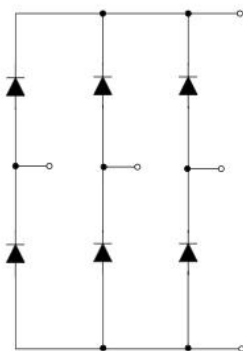


Typical Applications

- Inverter for motor drive
- AC and DC servo drive amplifier
- Uninterruptible power supply

JINLAN	= Company Name
JLPI10B120RE1E7SN	= Specific Device Code
YYWW	= Year and Work Week Code
XXXXX	= Serial Number
QR code	= Custom Assembly Information

Description





Package Insulation coordination

Parameter	Symbol	Note or test condition	Values	Unit
Isolation test voltage	V_{ISOL}	RMS, f=50Hz, t=60s	2.5	kV
Internal isolation		basic insulation(class 1, IEC 61140)	Al_2O_3	
Creepage distance	d_{creep}	terminal to heatsink	11.5	mm
Creepage distance	d_{creep}	terminal to terminal	6.3	mm
Clearance	d_{clear}	terminal to heatsink	10	mm
Clearance	d_{clear}	terminal to terminal	5	mm
Comparative tracking index (electrical)	CTI		>200	
RTI Elec.	RTI	housing	140	°C

Package Characteristic values

Parameter	Symbol	Note or test condition	Values			Unit
			Min.	Typ.	Max.	
Stray Inductance	LCE		--	30	--	nH
Storage Temperature Range	T_{STG}		-40		125	°C
Mounting force per clamp	F		20	--	50	N
Weight	G		--	24	--	g

**IGBT, Inverter****Absolute Maximum Ratings** ($T_c = 25^\circ\text{C}$ unless otherwise noted)

Symbol	Description	Value	Unit
V_{CES}	Collector-Emitter Voltage	1200	V
V_{GES}	Gate-Emitter Voltage	± 25	V
I_C	Collector Current @ $T_c=25^\circ\text{C}$	20	A
	Collector Current @ $T_c=100^\circ\text{C}$	10	A
I_{CM}	Pulsed Collector Current, t_p limited by $T_{vj\max}$	20	A

Characteristics ($T_c = 25^\circ\text{C}$ unless otherwise noted)

Symbol	Parameter	Test Condition	Min	Typ	Max	Unit
$V_{CE(sat)}$	Collector-Emitter Saturation Voltage	$V_{GE} = 15\text{ V}, I_C = 10\text{ A}, T_{vj} = 25^\circ\text{C}$	--	1.50	2.20	V
		$V_{GE} = 15\text{ V}, I_C = 10\text{ A}, T_{vj} = 125^\circ\text{C}$	--	1.70	--	
		$V_{GE} = 15\text{ V}, I_C = 10\text{ A}, T_{vj} = 150^\circ\text{C}$	--	1.75	--	
$V_{GE(TH)}$	Gate-Emitter Threshold Voltage	$V_{GE} = V_{CE}, I_C = 250\mu\text{A}, T_{vj} = 25^\circ\text{C}$	5.0	6.0	7.0	V
I_{CES}	Collector-Emitter Cutoff Current	$V_{GE} = 0\text{ V}, V_{CE} = V_{CES}, T_{vj} = 25^\circ\text{C}$	--	--	200	μA
I_{GES}	Gate-Emitter Leakage Current	$V_{GE} = V_{GES}, V_{CE} = 0\text{ V}, T_{vj} = 25^\circ\text{C}$	--	--	200	nA
R_{Gint}	Internal Gate Resistance		--	0.8	--	Ω
C_{ies}	Input Capacitance	$V_{CE}=25\text{V}, f=1\text{MHz}, V_{GE}=0\text{V}$	--	1610	--	pF
C_{oes}	Output capacitance		--	88.7	--	pF
C_{res}	Reverse Transfer		--	14.8	--	pF
Q_G	Gate Charge	$V_{GE}=15\text{V}, I_C=10\text{A}$	--	60.7	--	nC
$t_{d(on)}$	Turn-On Delay Time	$V_{CC} = 600\text{ V}, I_C = 10\text{ A}$ $R_{Gon} = 10\Omega, R_{Goff} = 25\Omega$ $V_{GE} = \pm 15\text{ V}$ Inductive Load $T_{vj} = 25^\circ\text{C}$	--	60	--	ns
t_r	Rise Time		--	20	--	
$t_{d(off)}$	Turn-off Delay Time		--	110	--	
t_f	Fall Time		--	47	--	
E_{on}	Turn-On Switching Loss per Pulse		--	0.495	--	mJ
E_{off}	Turn Off Switching Loss per Pulse		--	0.305	--	
E_{is}	Total Switching Loss		--	0.800	--	
$t_{d(on)}$	Turn-On Delay Time	$V_{CC} = 600\text{ V}, I_C = 10\text{ A}$ $R_{Gon} = 10\Omega, R_{Goff} = 25\Omega$ $V_{GE} = \pm 15\text{ V}$ Inductive Load $T_{vj} = 150^\circ\text{C}$	--	TBD	--	ns
t_r	Rise Time		--	TBD	--	
$t_{d(off)}$	Turn-off Delay Time		--	TBD	--	
t_f	Fall Time		--	TBD	--	
E_{on}	Turn-on Switching Loss per Pulse		--	0.770	--	mJ
E_{off}	Turn Off Switching Loss per Pulse		--	0.425	--	
E_{is}	Total Switching Loss		--	1.195	--	
I_{sc}	SC Data	$t_p \leq 10\mu\text{s}, V_{GE} \leq 15\text{ V}, T_{vj} = 150^\circ\text{C}, V_{CC} = 800\text{ V}$	--	35	--	A
R_{thJC}	Thermal resistance	Junction-to-Case (per IGBT)	--	TBD	--	K/W
$T_{vj\text{ op}}$		Temperature under switching conditions	-40		175 ¹⁾	$^\circ\text{C}$

¹⁾ $T_{vj\text{ op}} > 150^\circ\text{C}$ is only allowed for operation at overload conditions.



Diode, Inverter

Absolute Maximum Ratings (Tc = 25°C unless otherwise noted)

Symbol	Description	Value	Unit
V_{RRM}	Repetitive Peak Reverse Voltage	1200	V
I_F	Diode Continuous Forward Current	10	A
I_{FM}	Diode Maximum Forward Current $t_p=1ms$	20	A

Characteristics (Tc=25°C unless otherwise noted)

Symbol	Parameter	Test Condition	Min	Typ	Max	Unit
V_F	Diode Forward Voltage	$I_F = 10A, V_{GE}=0V, T_{vj} = 25^\circ C$	--	1.60	--	V
		$I_F = 10A, V_{GE}=0V, T_{vj} = 125^\circ C$	--	1.50	--	
		$I_F = 10A, V_{GE}=0V, T_{vj} = 150^\circ C$	--	1.70	--	
Q_r	Recovered Charge	$V_{CC} = 600V, I_C = 10A$ $R_{Gon} = 47\Omega, R_{Goff} = 47\Omega$ $V_{GE} = \pm 15V$ Inductive Load $T_{vj} = 25^\circ C$	--	0.214	--	μC
I_{RM}	Peak Reverse Recovery Current		--	5.22	--	A
T_{rr}	Reverse Recovery Time		--	27.5	--	ns
E_{rec}	Reverse Recovery Energy		--	0.090	--	mJ
Q_r	Recovered Charge	$V_{CC} = 600V, I_C = 10A$ $R_{Gon} = 47\Omega, R_{Goff} = 47\Omega$ $V_{GE} = \pm 15V$ Inductive Load $T_{vj} = 150^\circ C$	--	0.495	--	μC
I_{RM}	Peak Reverse Recovery Current		--	7.97	--	A
T_{rr}	Reverse Recovery Time		--	42.9	--	ns
E_{rec}	Reverse Recovery Energy		--	0.291	--	mJ
R_{thJC}	Thermal resistance	Junction-to-Case (per diode)	--	TBD	--	K/W
$T_{vj op}$		Temperature under switching conditions	-40		175 ²⁾	°C

²⁾ $T_{vj op} > 150^\circ C$ is only allowed for operation at overload conditions.

Diode, Rectifier

Absolute Maximum Ratings (Tc = 25°C unless otherwise noted)

Symbol	Description	Value	Unit
V_{RRM}	Repetitive Peak Reverse Voltage	1600	V
I_F	Diode Continuous Forward Current	10	A
I_{FM}	Diode Maximum Forward Current $t_p=1ms$	20	A

Characteristics (Tc=25°C unless otherwise noted)

Symbol	Parameter	Test Condition	Min	Typ	Max	Unit
V_F	Diode Forward Voltage	$I_F = 10A, T_j = 150^\circ C$	--	1.2	--	V
I_R	Reverse Current	$T_j = 150^\circ C, V_R = 1600V$	--	1	--	mA
R_{thJC}	Thermal resistance	Junction-to-Case (per diode)	--	TBD	--	K/W
$T_{vj op}$		Temperature under switching conditions	-40		175 ³⁾	°C

³⁾ $T_{vj op} > 150^\circ C$ is only allowed for operation at overload conditions.

**IGBT, Chopper****Absolute Maximum Ratings** (T_c = 25°C unless otherwise noted)

Symbol	Description	Value	Unit
V _{CES}	Collector-Emitter Voltage	1200	V
V _{GES}	Gate-Emitter Voltage	±30	V
I _C	Collector Current @ T _c =25°C	20	A
	Collector Current @ T _c =80°C	10	A
I _{CM}	Pulsed Collector Current, t _p =1S	20	A

Characteristics (T_c = 25°C unless otherwise noted)

Symbol	Parameter	Test Condition	Min	Typ	Max	Unit
V _{CE(sat)}	Collector-Emitter Saturation Voltage	V _{GE} = 15 V, I _C = 10 A, T _{vj} = 25°C	--	1.50	2.20	V
		V _{GE} = 15 V, I _C = 10 A, T _{vj} = 125°C	--	1.70	--	
		V _{GE} = 15 V, I _C = 10 A, T _{vj} = 150°C	--	1.75	--	
V _{GE(TH)}	Gate-Emitter Threshold Voltage	V _{GE} = V _{CE} , I _C = 250μA, T _{vj} = 25°C	5.0	6.0	7.0	V
I _{CES}	Collector-Emitter Cutoff Current	V _{GE} = 0 V, V _{CE} = V _{CES} , T _{vj} = 25°C	--	--	200	μA
I _{GES}	Gate-Emitter Leakage Current	V _{GE} = V _{GES} , V _{CE} = 0 V, T _{vj} = 25°C	--	--	200	nA
R _{Gint}	Internal Gate Resistance		--	0.8	--	Ω
C _{ies}	Input Capacitance	V _{CE} =25V, f=1MHz, V _{GE} =0V	--	1610	--	pF
C _{oes}	Output capacitance		--	88.7	--	pF
C _{res}	Reverse Transfer		--	14.8	--	pF
Q _G	Gate Charge	V _{GE} =15V, I _C =10A	--	60.7	--	nC
t _{d(on)}	Turn-On Delay Time	V _{CC} = 600 V, I _C = 10 A R _{Gon} = 10Ω, R _{Goff} = 25Ω V _{GE} = ±15V Inductive Load T _{vj} = 25°C	--	60	--	ns
t _r	Rise Time		--	20	--	
t _{d(off)}	Turn-off Delay Time		--	110	--	
t _f	Fall Time		--	47	--	
E _{on}	Turn-On Switching Loss per Pulse		--	0.495	--	mJ
E _{off}	Turn Off Switching Loss per Pulse		--	0.305	--	
E _{is}	Total Switching Loss		--	0.800	--	
t _{d(on)}	Turn-On Delay Time	V _{CC} = 600 V, I _C = 10 A R _{Gon} = 10Ω, R _{Goff} = 25Ω V _{GE} = ±15V Inductive Load T _{vj} = 150°C	--	TBD	--	ns
t _r	Rise Time		--	TBD	--	
t _{d(off)}	Turn-off Delay Time		--	TBD	--	
t _f	Fall Time		--	TBD	--	
E _{on}	Turn-on Switching Loss per Pulse		--	0.770	--	mJ
E _{off}	Turn Off Switching Loss per Pulse		--	0.425	--	
E _{is}	Total Switching Loss		--	1.195	--	
I _{SC}	SC Data	t _p ≤10μs, V _{GE} ≤15V, T _{vj} =150°C, V _{CC} =800V	--	35	--	A
R _{thJC}	Thermal resistance	Junction-to-Case (per IGBT)	--	TBD	--	K/W
T _{vj op}		Temperature under switching conditions	-40		175 ¹⁾	°C

⁴⁾T_{vj op} > 150°C is only allowed for operation at overload conditions.



Diode, Chopper

Absolute Maximum Ratings (T_C = 25°C unless otherwise noted)

Symbol	Description	Value	Unit
V _{RRM}	Repetitive Peak Reverse Voltage	1200	V
I _F	Diode Continuous Forward Current	10	A
I _{FM}	Diode Maximum Forward Current t _p =1ms	20	A

Characteristics (T_C=25°C unless otherwise noted)

V _F	Diode Forward Voltage	I _F = 10A, V _{GE} =0V, T _{vj} = 25°C	--	1.60	--	V
		I _F =10A, V _{GE} =0V, T _{vj} = 150°C	--	1.50	--	
Q _r	Recovered Charge	V _R =600V, I _F =10A, R _{Gon} = 10Ω, R _{Goff} = 25Ω V _{GE} =±15V T _{vj} =25°C	--	0.214	--	μC
I _{RM}	Peak Reverse Recovery Current		--	5.22	--	A
T _{rr}	Reverse Recovery Time		--	27.5	--	ns
E _{rec}	Reverse Recovery Energy		--	0.090	--	mJ
Q _r	Recovered Charge	V _R =600V, I _F =10A, R _{Gon} = 10Ω, R _{Goff} = 25Ω V _{GE} =±15V T _{vj} =150°C	--	0.495	--	μC
I _{RM}	Peak Reverse Recovery Current		--	7.97	--	A
T _{rr}	Reverse Recovery Time		--	42.9	--	ns
E _{rec}	Reverse Recovery Energy		--	0.291	--	mJ
R _{thJC}	Thermal resistance	Junction-to-Case (per diode)	--	TBD	--	K/W
T _{vj op}		Temperature under switching conditions	-40		175 ²⁾	°C

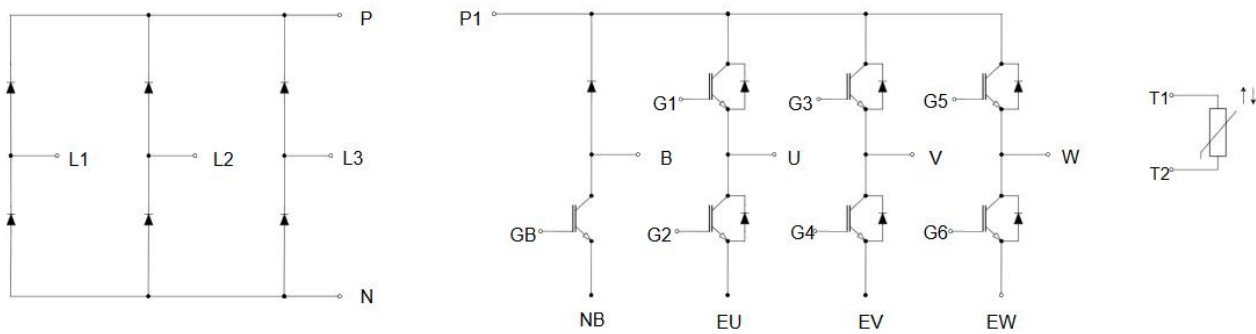
⁵⁾T_{vj op} > 150°C is only allowed for operation at overload conditions.

NTC Characteristics (T_C = 25°C unless otherwise noted)

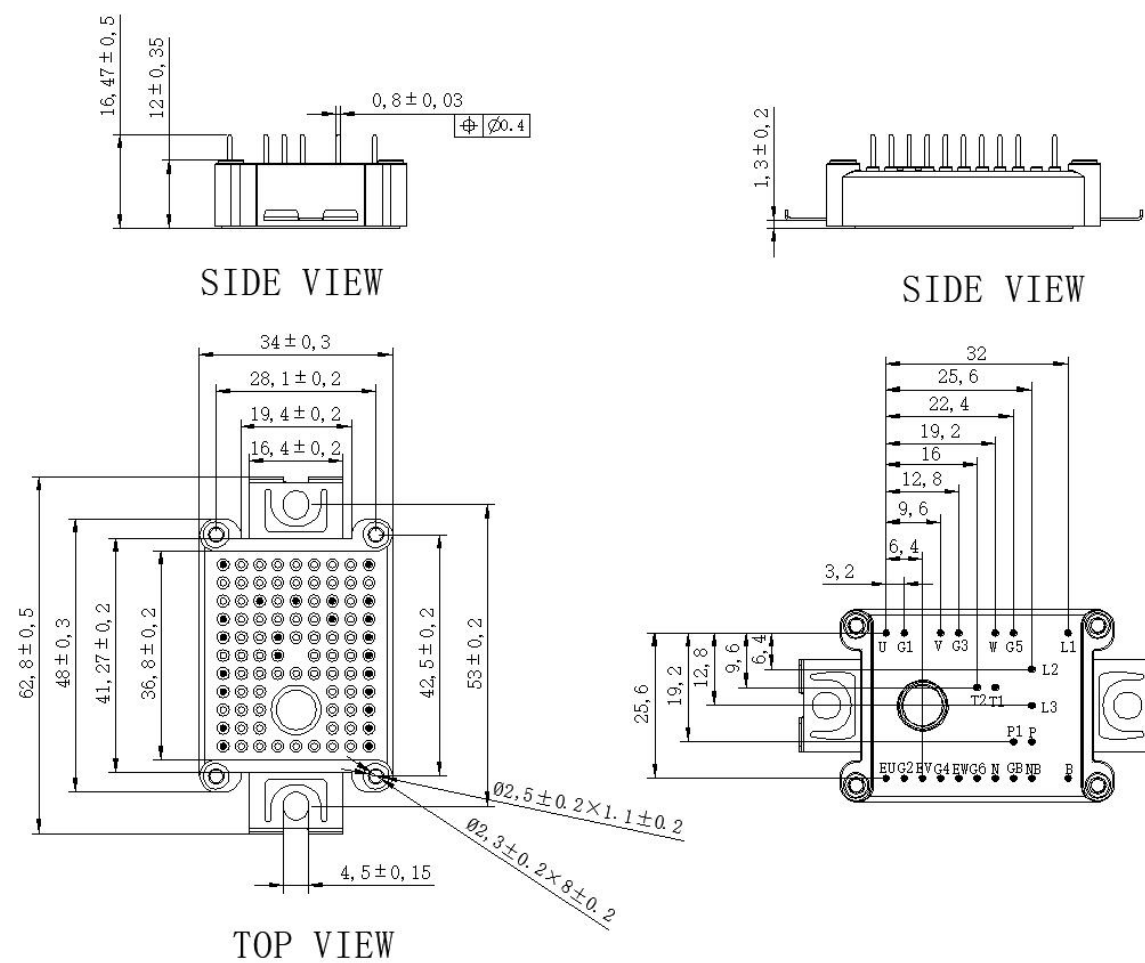
Symbol	Parameter	Test Condition	Min	Typ	Max	Unit
R ₂₅	Rated Resistance		--	5.0	--	kΩ
ΔR/R	Deviation of R ₁₀₀	T _C =100 °C, R ₁₀₀ =493.3 Ω	-5	--	5	%
P ₂₅	Power Dissipation		--	--	20.0	mW
B _{25/50}	B-value	R ₂ =R ₂₅ exp[B _{25/50} (1/T ₂ - 1/(298.15K))]	--	3375	--	K
B _{25/80}	B-value	R ₂ =R ₂₅ exp[B _{25/80} (1/T ₂ - 1/(298.15K))]	--	3411	--	K
B _{25/100}	B-value	R ₂ =R ₂₅ exp[B _{25/100} (1/T ₂ - 1/(298.15K))]	--	3433	--	K



CIRCUIT DIAGRAM



PACKAGE DIMENSION





REVISION HISTORY

Document version	Date of release	Description of changes
Rev.00	2025-05-30	Preview



ATTENTION

■ Any and all Jinlan power products described or contained herein do not have specifications that can handle applications that require extremely high levels of reliability, such as life-support systems, aircraft's control systems, or other applications whose failure can be reasonably expected to result in serious physical and/or material damage. Consult with your Jinlan Power Semiconductor representative nearest you before using any Jinlan power products described or contained herein in such applications.

■ Jinlan Power Semiconductor assumes no responsibility for equipment failures that result from using products at values that exceed, even momentarily, rated values (such as maximum ratings, operating condition ranges, or other parameters) listed in products specifications of any and all Jinlan power modules described or contained herein.

■ Specifications of any and all Jinlan power products described or contained herein stipulate the performance, characteristics, and functions of the described products in the independent state, and are not guarantees of the performance, characteristics, and functions of the described products as mounted in the customer's products or equipment. To verify symptoms and states that cannot be evaluated in an independent device, the customer should always evaluate and test devices mounted in the customer's products or equipment.

■ Jinlan Power Semiconductor (Wuxi).co.,LTD. strives to supply high-quality high-reliability products. However, any and all semiconductor products fail with some probability. It is possible that these probabilistic failures could give rise to accidents or events that could endanger human lives, that could give rise to smoke or fire, or that could cause damage to other property. When designing equipment, adopt safety measures so that these kinds of accidents or events cannot occur. Such measures include but are not limited to protective circuits and error prevention circuits for safe design, redundant design, and structural design.

■ In the event that any or all Jinlan power products(including technical data, services) described or contained herein are controlled under any of applicable local export control laws and regulations, such products must not be exported without obtaining the export license from the authorities concerned in accordance with the above law.

■ No part of this publication may be reproduced or transmitted in any form or by any means, electronic or mechanical, including photocopying and recording, or any information storage or retrieval system, or otherwise, without the prior written permission of Jinlan Power Semiconductor (Wuxi).co.,LTD.

■ Information (including circuit diagrams and circuit parameters) herein is for example only; it is not guaranteed for volume production. Jinlan Power Semiconductor believes information herein is accurate and reliable, but no guarantees are made or implied regarding its use or any infringements of intellectual property rights or other rights of third parties.

■ Any and all information described or contained herein are subject to change without notice due to product/technology improvement, etc. When designing equipment, refer to the "Delivery Specification" for the Jinlan power product that you intend to use.

■ This catalog provides information as of May.2025. Specifications and information herein are subject to change without notice.