

# MSKSEMI 美森科

SEMICONDUCTOR



ESD



TVS



TSS



MOV



GDT



PLED

## AO3401MI-MS

Product specification

## Features

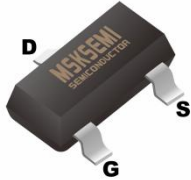
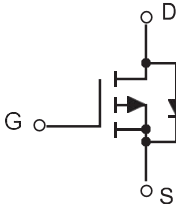

- High dense cell design for extremely low  $R_{DS(ON)}$
- Exceptional on-resistance and maximum DC current capability

## APPLICATION

- Load/Power Switching
- Interfacing Switching

$V_{(BR)DSS}$	$R_{DS(on)MAX}$	$I_D$
-30 V	65mΩ@-10V	-4.2A
	75mΩ@-4.5V	
	90mΩ@-2.5V	

## Reference News

PACKAGE OUTLINE	PIN Configuration	Marking
 SOT-23-3L		

## Maximum ratings ( $T_a=25^{\circ}\text{C}$ unless otherwise noted)

Parameter	Symbol	Value	Unit
Drain-Source Voltage	$V_{DS}$	-30	V
Gate-Source Voltage	$V_{GS}$	$\pm 12$	V
Continuous Drain Current	$I_D$	-4.2	A
Power Dissipation	$P_D$	350	mW
Thermal Resistance from Junction to Ambient ( $t < 5s$ )	$R_{\theta JA}$	357	$^{\circ}\text{C}/\text{W}$
Junction Temperature	$T_J$	150	$^{\circ}\text{C}$
Storage Temperature	$T_{STG}$	-55~+150	$^{\circ}\text{C}$

## MOSFET ELECTRICAL CHARACTERISTICS

**T<sub>a</sub>=25 °C unless otherwise specified**

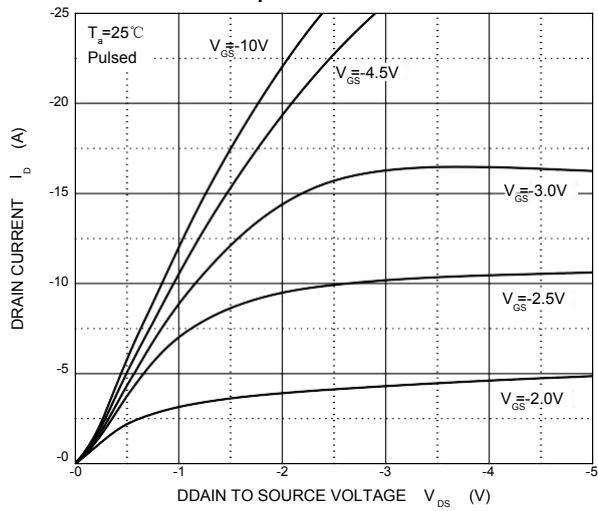
Parameter	Symbol	Test Condition	Min	Typ	Max	Unit
Off characteristics						
Drain-source breakdown voltage	V <sub>(BR)DSS</sub>	V <sub>GS</sub> = 0V, I <sub>D</sub> =-250μA	-30			V
Zero gate voltage drain current	I <sub>DSS</sub>	V <sub>DS</sub> =-24V, V <sub>GS</sub> = 0V			-1	μA
Gate-source leakage current	I <sub>GSS</sub>	V <sub>GS</sub> =±12V, V <sub>DS</sub> = 0V			±100	nA
On characteristics						
Drain-source on-resistance (note 1)	R <sub>DS(on)</sub>	V <sub>GS</sub> =-10V, I <sub>D</sub> =-4.2A		50	65	mΩ
		V <sub>GS</sub> =-4.5V, I <sub>D</sub> =-4A		60	75	mΩ
		V <sub>GS</sub> =-2.5V, I <sub>D</sub> =-1A		75	90	mΩ
Forward tranconductance (note 1)	g <sub>FS</sub>	V <sub>DS</sub> =-5V, I <sub>D</sub> =-5A	7			S
Gate threshold voltage	V <sub>GS(th)</sub>	V <sub>DS</sub> =V <sub>GS</sub> , I <sub>D</sub> =-250μA	-0.7	-0.9	-1.3	V
Dynamic characteristics (note 2)						
Input capacitance	C <sub>iss</sub>	V <sub>DS</sub> =-15V, V <sub>GS</sub> =0V, f = 1MHz		954		pF
Output capacitance	C <sub>oss</sub>			115		pF
Reverse transfer capacitance	C <sub>rss</sub>			77		pF
Switching characteristics (note 2)						
Turn-on delay time	t <sub>d(on)</sub>	V <sub>GS</sub> =-10V, V <sub>DS</sub> =-15V, R <sub>L</sub> =3.6Ω, R <sub>GEN</sub> =6Ω			6.3	ns
Turn-on rise time	t <sub>r</sub>				3.2	ns
Turn-off delay time	t <sub>d(off)</sub>				38.2	ns
Turn-off fall Time	t <sub>f</sub>				12	ns
Drain-source diode characteristics and maximum ratings						
Diode forward voltage (note 1)	V <sub>SD</sub>	I <sub>S</sub> =-1A, V <sub>GS</sub> =0V			-1	V

**Note :**

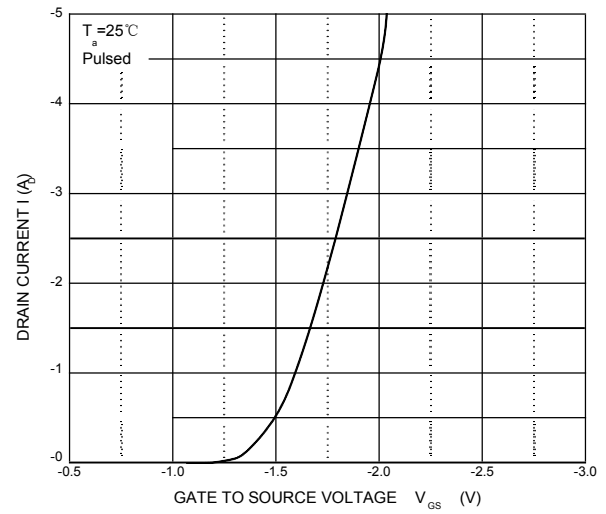
1. Pulse Test : Pulse width ≤ 300μs, duty cycle ≤ 2%.
2. These parameters have no way to verify.

## Typical Characteristics

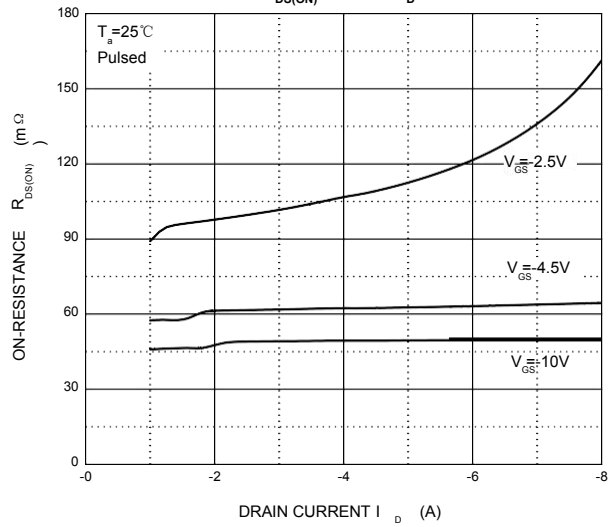
**Output Characteristics**



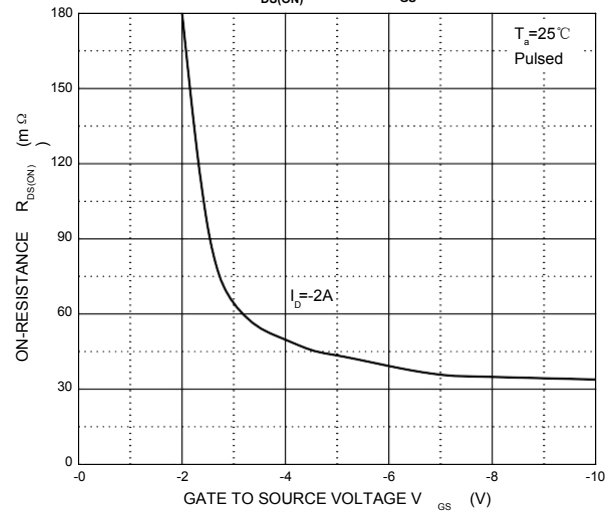
**Transfer Characteristics**



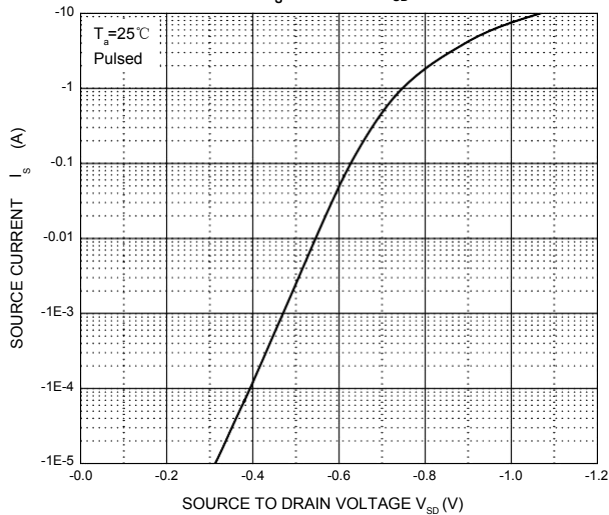
**$R_{DS(ON)}$  —  $I_D$**



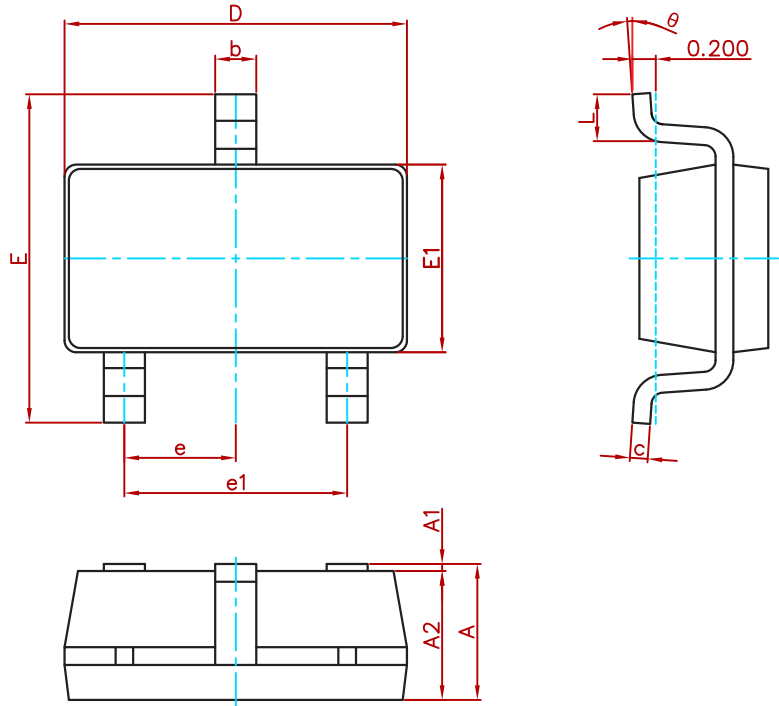
**$R_{DS(ON)}$  —  $V_{GS}$**



**$I_S$  —  $V_{SD}$**

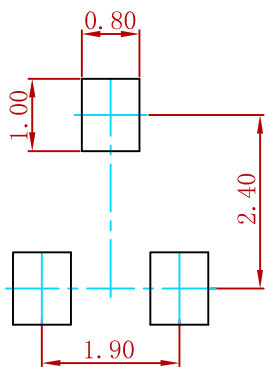


PACKAGE MECHANICAL DATA



Symbol	Dimensions In Millimeters		Dimensions In Inches	
	Min.	Max.	Min.	Max.
A	1.050	1.250	0.041	0.049
A1	0.000	0.100	0.000	0.004
A2	1.050	1.150	0.041	0.045
b	0.300	0.500	0.012	0.020
c	0.100	0.200	0.004	0.008
D	2.820	3.020	0.111	0.119
E1	1.500	1.700	0.059	0.067
E	2.650	2.950	0.104	0.116
e	0.950(BSC)		0.037(BSC)	
e1	1.800	2.000	0.071	0.079
L	0.300	0.600	0.012	0.024
θ	0°	8°	0°	8°

Suuggested Pad Layout



Note:  
1.Controlling dimension:in millimeters.  
2.General tolerance:± 0.05mm.  
3.The pad layout is for reference purposes only.

REELSPECIFICATION

P/N	PKG	QTY
AO3401MI-MS	SOT-23-3L	3000

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