

Features

- low insertion loss, 0.4 dB typ.
- high isolation, 23 dB typ.

Applications

- cellular
- instrumentation
- communications systems

JY-LRPS-2-4+



2 Way-0° 50Ω 10 to 1000 MHz

electrical schematic



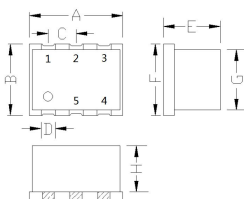
Transformer Electrical Specifications

Freq. range (MHz)	Isolation(dB)		Insertion Loss (dB) Above 3.0 dB.		Phase Unbalance (Degrees) Max.	Amplitude Unbalance (dB) Max.
	min	max	min	max		
10-1000	14	25	0.3	1.5	5.0	0.4

Typical Performance Data(TEST CONDITIONS: INPUT POWER = 0dBm @Temperature = +25°C)

Frequency. (MHz)	Total Loss (dB)		Amp. Unbal. (dB)	Isolation (dB)	PhaseUnbal. (deg.)	VSWRS		
	S-1	S-2				S	1	2
10.00	3.31	3.31	0.00	39.33	0.01	1.04	1.11	1.11
47.69	3.31	3.31	0.00	36.50	0.16	1.04	1.10	1.10
85.38	3.34	3.33	0.00	32.39	0.27	1.06	1.10	1.10
160.77	3.39	3.38	0.00	27.60	0.51	1.12	1.11	1.11
236.15	3.44	3.43	0.01	24.93	0.73	1.17	1.12	1.13
349.23	3.52	3.51	0.01	22.30	1.04	1.24	1.14	1.15
424.62	3.58	3.56	0.02	21.10	1.22	1.29	1.16	1.16
500.00	3.65	3.61	0.03	20.23	1.39	1.33	1.17	1.18
541.67	3.69	3.65	0.04	19.87	1.47	1.34	1.17	1.18
625.00	3.76	3.70	0.06	19.39	1.62	1.37	1.18	1.19
708.33	3.83	3.74	0.09	19.28	1.76	1.38	1.17	1.18
791.67	3.90	3.78	0.12	19.59	1.82	1.37	1.16	1.16
875.00	3.98	3.82	0.16	20.38	1.86	1.34	1.14	1.13
958.33	4.08	3.86	0.22	21.87	1.88	1.27	1.11	1.09
1000.00	4.14	3.89	0.25	22.95	1.88	1.23	1.10	1.07

Outline Drawing

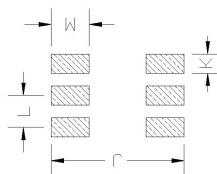


Suggested Layout,
Tolerance to be within ±0.2

Outline Dimensions: Unit (mm)

A	8.70	J	8.00
B	6.50	K	1.50
C	2.54	G	5.50
D	1.30	H	4.30
E	5.40	L	2.54
F	6.50	M	2.00
WT	3g		

PCB Land Pattern



Pin Connections

SUM PORT	6
PORT 1	4
PORT 2	3
GROUND	1
NOT USED	2,5

Maximum Ratings

Operating Temperature	-40°C to 85°C
Storage Temperature	-55°C to 100°C
Power Input (as a splitter)	1 W max.
Internal Dissipation	0.125W max.
Permanent damage may occur if any of these limits are exceeded.	

