

PRODUCT DATA SHEET



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Datasheet

esources

Please note: Please check the JINGAO Semiconductor website to verify the updated device numbers. The most current and up-to-date ordering information can be found at www.jg-semi.cn. Please email any questions regarding the system integration to JINGAO_questions@jgsemi.com.



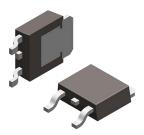
N-Ch 30V Fast Switching MOSFETs

Product Summary

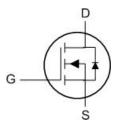
BVDSS	RDSON	ID
30V	5.2mΩ	80 A



- 100% EAS Guaranteed
- Green Device Available
- Excellent CdV/dt effect decline
- Advanced high cell density Trench technology



TO252



Absolute Maximum Ratings

		R			
Symbol	Parameter	10s	Steady State	Units	
V_{DS}	Drain-Source Voltage		V		
V_{GS}	Gate-Source Voltage	:	±20	V	
I _D @T _C =25°C	Continuous Drain Current, V _{GS} @ 10V¹		80		
I _D @T _C =100°C	Continuous Drain Current, V _{GS} @ 10V ¹		50		
I _{DM}	Pulsed Drain Current ²	Pulsed Drain Current ² 192			
EAS	Single Pulse Avalanche Energy³	06	mJ		
I _{AS}	Avalanche Current		Α		
P _D @T _C =25°C	Total Power Dissipation⁴		W		
T _{STG}	Storage Temperature Range	-55	°C		
TJ	Operating Junction Temperature Range	-55	to 125	°C	

Thermal Data

Symbol	Parameter	Тур.	Max.	Unit
R _{eJC}	Thermal Resistance Junction-Case ¹		0.56	°C/W



Thermal Characteristic

Thermal Resistance, Junction-to-Case ^(Note 2)	R ₀ JC	1.8	°C/W
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Electrical Characteristics (T_C=25°C unless otherwise noted)

Parameter Symbol		Condition	Min	Тур	Max	Unit	
Off Characteristics							
Drain-Source Breakdown Voltage	BV _{DSS}	V _{GS} =0V I _D =250µA	30	-	-	V	
Zero Gate Voltage Drain Current	I _{DSS}	V _{DS} =30V,V _{GS} =0V	-	-	1	μA	
Gate-Body Leakage Current	I _{GSS}	V _{GS} =±20V,V _{DS} =0V	-	-	±100	nA	
On Characteristics (Note 3)							
Gate Threshold Voltage	V _{GS(th)}	V _{DS} =V _{GS} ,I _D =250µA	1	1.6	3	V	
Dunin Course On Ctata Desistance	Б	V _{GS} =10V, I _D =30A	-	5.2	6.5	mΩ	
Drain-Source On-State Resistance	R _{DS(ON)}	V _{GS} =5V, I _D =24A	-	7.5	10		
Forward Transconductance	g Fs	V _{DS} =5V,I _D =24A	20	-	-	S	
Dynamic Characteristics (Note4)	,		'	'			
Input Capacitance	C _{lss}	\/ -45\/\/ -0\/	-	2016	-	PF	
Output Capacitance	C _{oss}	V_{DS} =15V, V_{GS} =0V, F=1.0MHz	-	251	-	PF	
Reverse Transfer Capacitance	C _{rss}	F-1.UIVIDZ	-	230	-	PF	
Switching Characteristics (Note 4)							
Turn-on Delay Time	t _{d(on)}		-	20	-	nS	
Turn-on Rise Time	t _r	V _{DD} =10V,I _D =30A	-	15	-	nS	
Turn-Off Delay Time	t _{d(off)}	V_{GS} =10V, R_{GEN} =2.7 Ω	-	60	-	nS	
Turn-Off Fall Time	t _f		-	10	-	nS	
Total Gate Charge	Qg	V -40VI -20A	-	60.5	-	nC	
Gate-Source Charge	Q _{gs}	$V_{DS}=10V,I_{D}=30A,$ $V_{GS}=10V$	-	8.1	-	nC	
Gate-Drain Charge	Q _{gd}	VGS-10V	-	7.8	-	nC	
Drain-Source Diode Characteristics							
Diode Forward Voltage (Note 3)	V _{SD}	V _{GS} =0V,I _S =24A	-	-	1.2	V	
Diode Forward Current (Note 2)	Is		-	-	80	Α	
Reverse Recovery Time	t _{rr}	TJ = 25°C, IF = 80A	-	32	50	nS	
Reverse Recovery Charge	Qrr	di/dt = 100A/µs ^(Note3)	-	12	20	nC	
Forward Turn-On Time	t _{on}	Intrinsic turn-on time is negligible (turn-on is dominated by LS+LD)					

Notes:

- 1. Repetitive Rating: Pulse width limited by maximum junction temperature.
- 2. Surface Mounted on FR4 Board, t ≤ 10 sec.
- **3.** Pulse Test: Pulse Width ≤ 300µs, Duty Cycle ≤ 2%.
- **4.** Guaranteed by design, not subject to production
- **5.** EAS condition: Tj=25 $^{\circ}$ C,VDD=15V,VG=10V,L=0.5mH,Rg=25 Ω , IAS=35A



Typical Electrical and Thermal Characteristics (Curves)

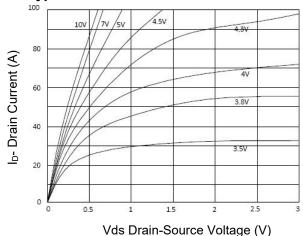


Figure 1 Output Characteristics

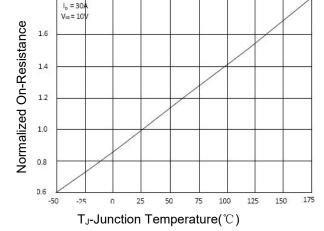


Figure 4 Rdson-JunctionTemperature

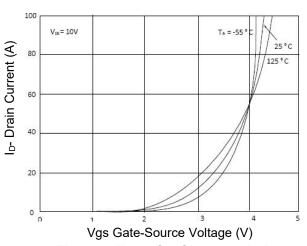


Figure 2 Transfer Characteristics

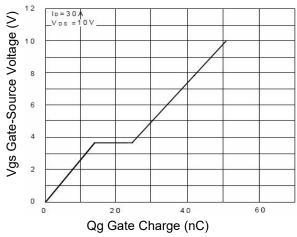


Figure 5 Gate Charge

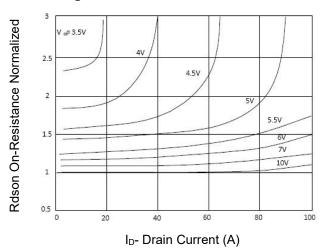
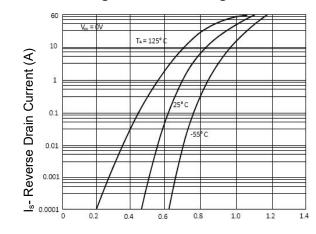


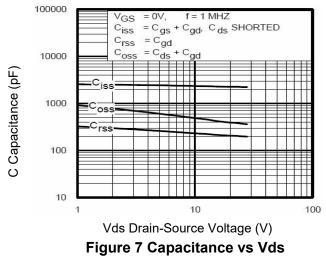
Figure 3 Rdson- Drain Current



Vsd Source-Drain Voltage (V)

Figure 6 Source- Drain Diode Forward





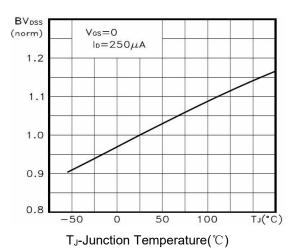
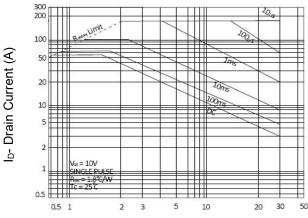
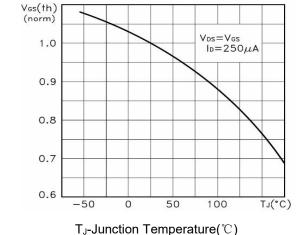


Figure 9 BV_{DSS} vs Junction Temperature

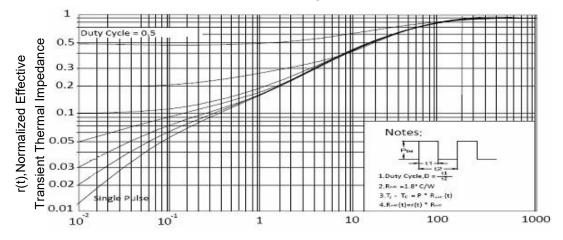




Vds Drain-Source Voltage (V)

Figure 10 V_{GS(th)} vs Junction Temperature





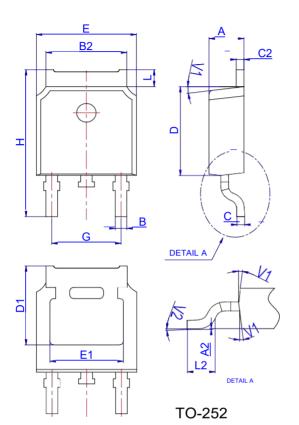
Normalized BVdss

Square Wave Pluse Duration(sec)

Figure 11 Normalized Maximum Transient Thermal Impedance



Package Mechanical Data TO 252



	Dimensions						
Ref.	Millimeters			Inches			
	Min.	Тур.	Max.	Min.	Тур.	Max.	
Α	2.10		2.50	0.083		0.098	
A2	0		0.10	0		0.004	
В	0.66		0.86	0.026		0.034	
B2	5.18		5.48	0.202		0.216	
С	0.40		0.60	0.016		0.024	
C2	0.44		0.58	0.017		0.023	
D	5.90		6.30	0.232		0.248	
D1	5.30REF			0.209REF			
E	6.40		6.80	0.252		0.268	
E1	4.63			0.182			
G	4.47		4.67	0.176		0.184	
Н	9.50		10.70	0.374		0.421	
L	1.09		1.21	0.043		0.048	
L2	1.35		1.65	0.053		0.065	
V1		7°			7°		
V2	0°		6°	0°		6°	



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