

Description

The SX40N20T is silicon N-channel Enhanced VDMOSFETs, is obtained by the self-aligned planar Technology which reduce the conduction loss, improve switching performance and enhance the avalanche energy. The transistor can be used in various power switching circuit for system miniaturization and higher efficiency.

General Features

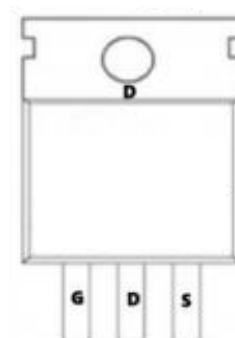
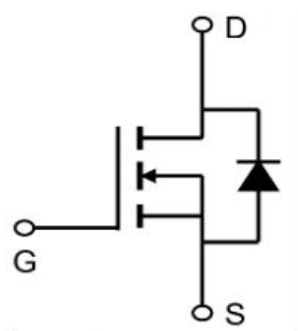
$V_{DS} = 200V$ $I_D = 40A$

$R_{DS(ON)} < 65m\Omega$ @ $V_{GS}=10V$

Application

Uninterruptible Power Supply(UPS)

Power Factor Correction (PFC)



Absolute Maximum Ratings ($T_c=25^{\circ}C$ unless otherwise noted)

Symbol	Parameter	Value	Unit
VDSS	Drain-Source Voltage	200	V
ID	Drain Current -continuous	40	A
IDM	Drain Current -pulse	112	A
VGSS	Gate-Source Voltage	± 30	V
EAS	Single Pulsed Avalanche Energy	588	mJ
IAR	Avalanche Current	28	A
EAR	Repetitive Avalanche Current	15.8	mJ
dv/dt	Peak Diode Recovery dv/dt	5.5	V/ns
PD $T_C=25^{\circ}C$	Power Dissipation	158	W
TJ, TSTG	Operating and Storage Temperature Range	$-55 \sim +150$	$^{\circ}C$
TL	Maximum Lead Temperature for Soldering Purposes	300	$^{\circ}C$
Rth(j-c)	Thermal Resistance, Junction to Case	0.79	$^{\circ}C/W$
Rth(j-A)	Thermal Resistance, Junction to Ambient	62.5	$^{\circ}C/W$

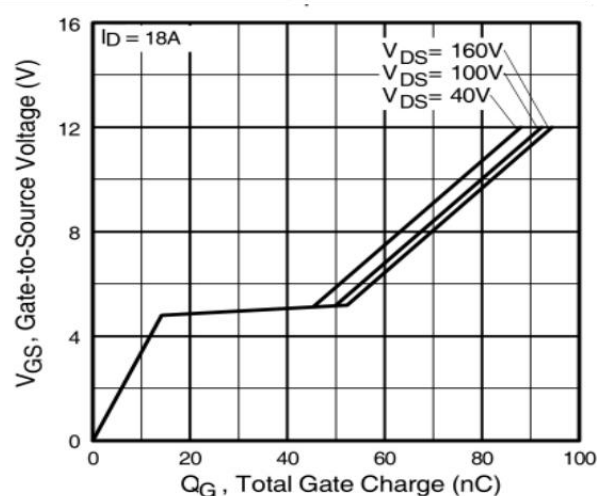
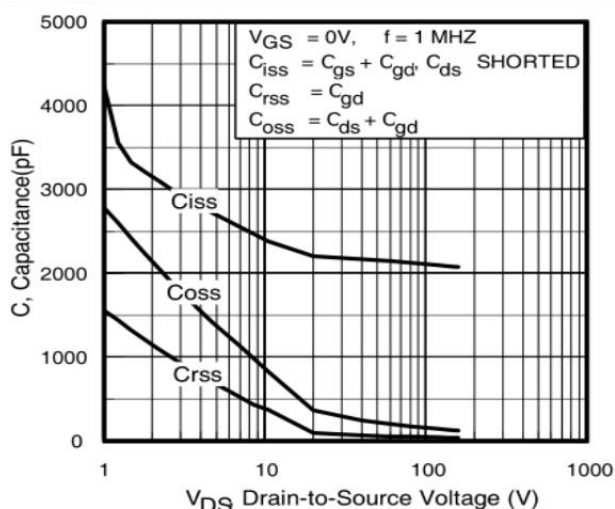
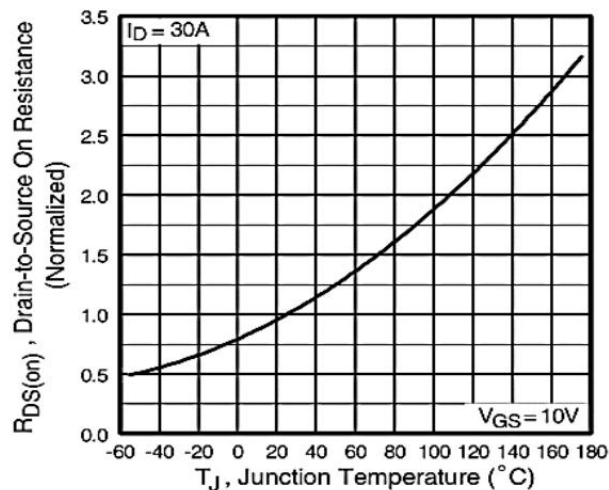
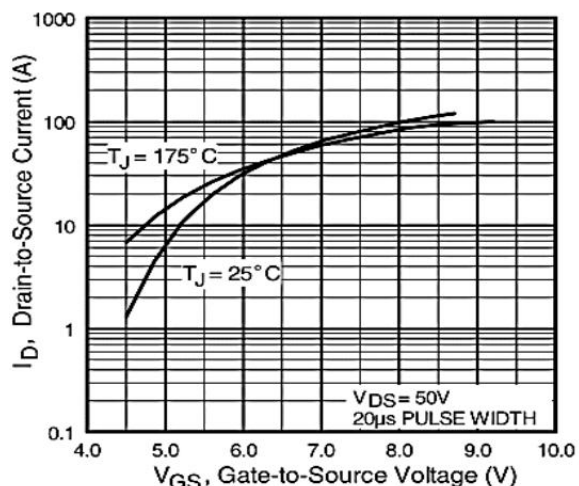
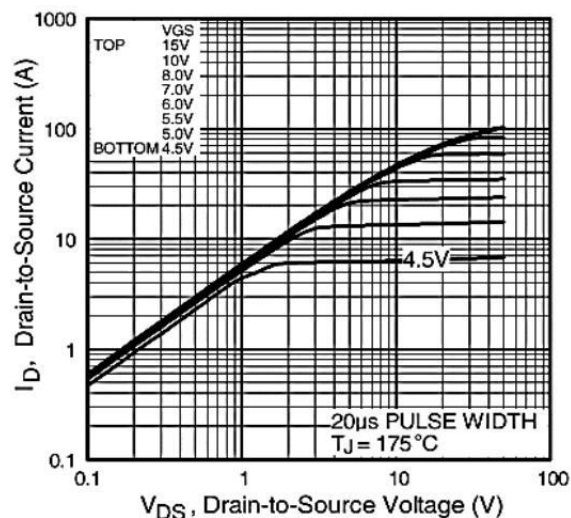
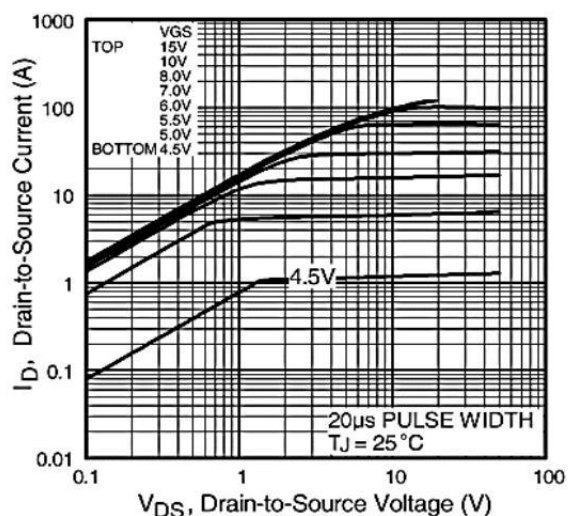
Electrical Characteristics (T_J=25°C, unless otherwise noted)

Symbol	Parameter	Tests conditions	Min	Typ	Max	Units
BV _{DSS}	Drain-Source Voltage	I _D =250μA, V _{GS} =0V	200	-	-	V
IDSS	Zero Gate Voltage Drain Current	V _{DS} =200V, V _{GS} =0V, T _C =25°C	-	-	1	μA
IGSSF	Gate-body leakage current, forward	V _{DS} =0V, V _{GS} =30V	-	-	100	nA
IGSSR	Gate-body leakage current, reverse	V _{DS} =0V, V _{GS} =-30V	-	-	-100	nA
VGS(th)	Gate Threshold Voltage	V _{DS} = V _{GS} , I _D =250μA	2.0	3.0	4.0	V
RDS(ON)	Static Drain-Source On-Resistance	V _{GS} =10V , I _D =14.0A	-	52	65	mΩ
g _{fs}	Forward Transconductance	V _{DS} = 40V , I _D =14.0A	-	24	-	S
C _{iss}	Input capacitance	V _{DS} =25V, V _{GS} =0V, f=1.0MHz	-	2879	3742	pF
C _{oss}	Output capacitance		-	362	470	pF
C _{rss}	Reverse transfer capacitance		-	81	105	pF
t _{d(on)}	Turn-On delay time	V _{DD} =100V, I _D =28A, R _G =25Ω V _{GS} =10V (note 4 , 5)	-	28	69	ns
t _r	Turn-On rise time		-	251	494	ns
t _{d(off)}	Turn-Off delay time		-	309	617	ns
t _f	Turn-Off Fall time		-	220	412	ns
Q _g	Total Gate Charge	V _{DS} =160V , I _D =28A V _{GS} =10V	-	103	136	nC
Q _{gs}	Gate-Source charge		-	16	-	nC
Q _{gd}	Gate-Drain charge		-	53	-	nC
I _S	Continuous Body Diode Current	T _C = 25 °C	-	-	28	A
I _{SM}	Pulsed Diode Forward Current		-	-	112	A
VSD	Body Diode Voltage	V _{GS} =0V, I _S =28A	-		1.4	V
t _{rr}	Reverse recovery time	V _{GS} =0V, I _S =28A dI _F /dt=100A/μs		218		ns
Q _{rr}	Reverse recovery charge			1.91		μC

Note :

- 1、The data tested by surface mounted on a 1 inch2 FR-4 board with 2OZ copper.
- 2、The EAS data shows Max. rating . L=1.5mH, I_{AS}=28A, V_{DD}=50V, R_G=25 Ω, Starting T_J=25°C
- 3、The test condition is Pulse Test: Pulse width ≤ 300μs, Duty Cycle ≤ 1%
- 4、The power dissipation is limited by 150°C junction temperature
- 5、The data is theoretically the same as ID and IDM , in real applications , should be limited by total power dissipation.

Typical Characteristics



Typical Characteristics

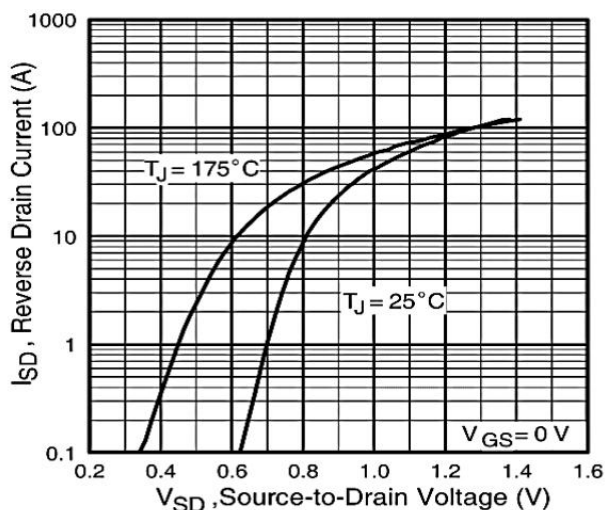


Fig 7. Typical Source-Drain Diode Forward Voltage

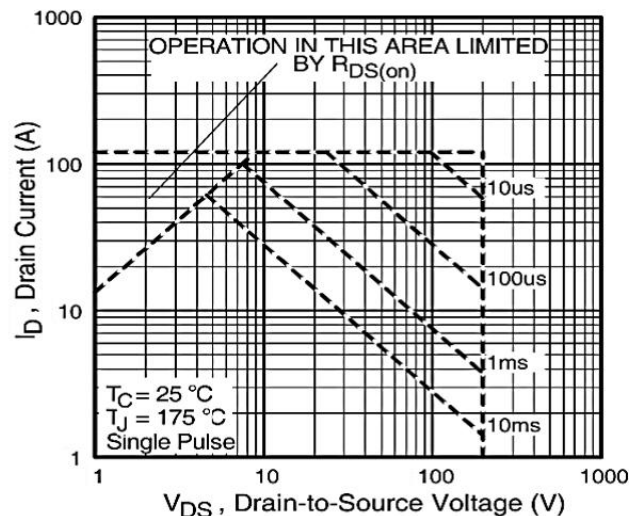


Fig 8. Maximum Safe Operating Area

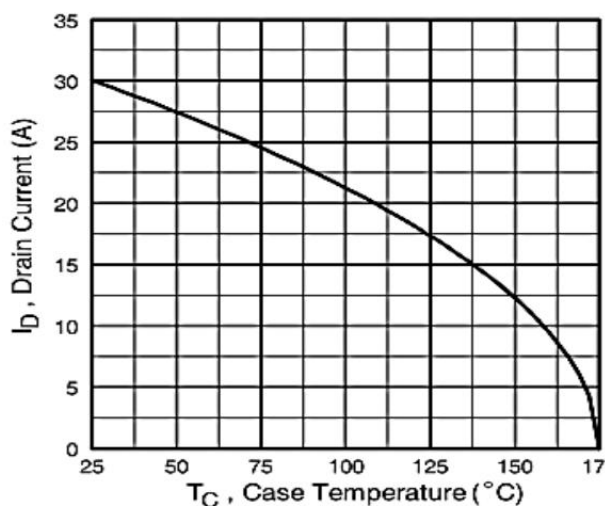


Fig 9. Maximum Drain Current Vs. Case Temperature

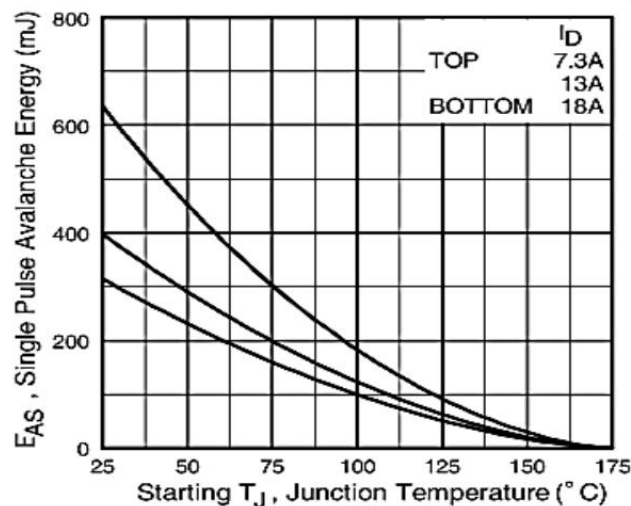


Fig 12c. Maximum Avalanche Energy Vs. Drain Current

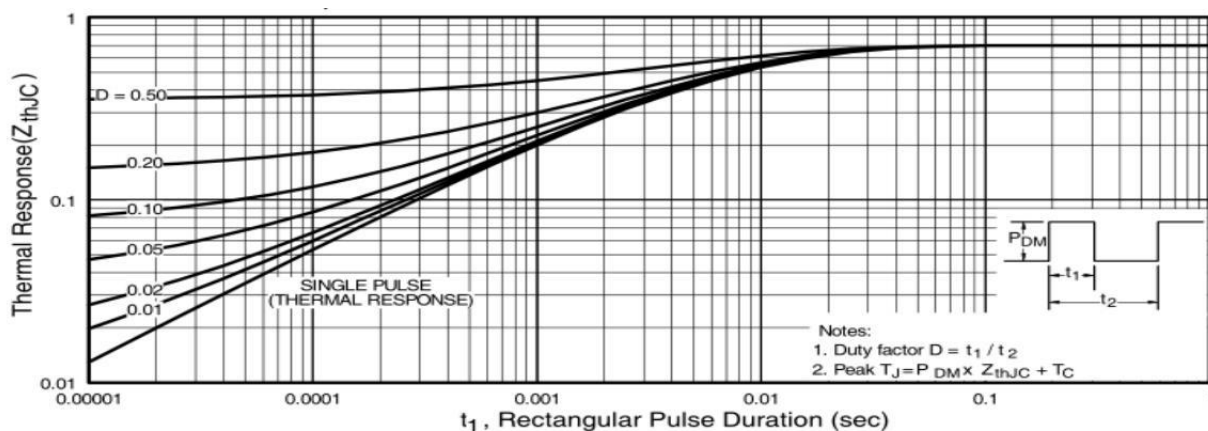
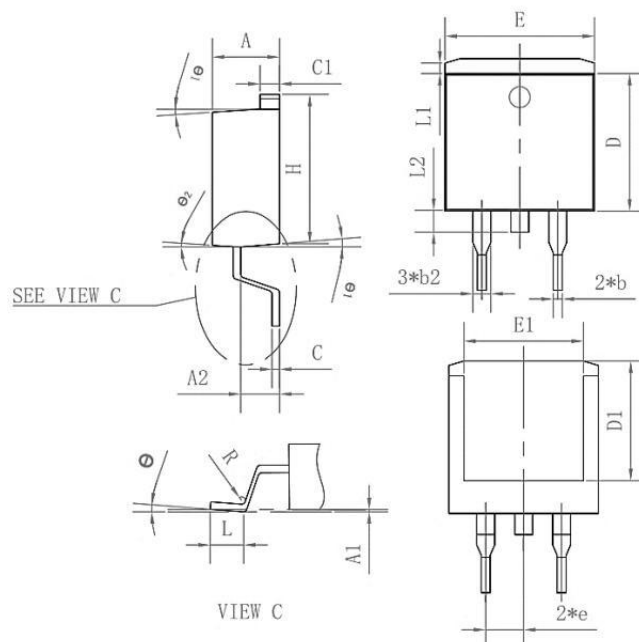


Fig 11. Maximum Effective Transient Thermal Impedance, Junction-to-Case

Package Mechanical Data-TO-263-3L-SLK



Symbol	Common		
	mm		
	Mim	Nom	Max
A	4.35	4.47	4.60
A1	0.09	0.10	0.11
A2	2.30	2.40	2.70
b	0.70	0.80	1.00
b2	1.25	1.36	1.50
C	0.45	0.50	0.65
C1	1.29	1.30	9.40
D	9.10	9.20	9.30
D1	7.90	8.00	8.10
E	9.85	10.00	10.20
E1	7.90	8.00	8.10
H	15.30	15.50	15.70
e	-	2.54	-
L	2.34	2.54	2.74
L1	1.00	1.10	1.20
L2	1.30	1.40	1.50
R	0.24	0.25	0.26
θ	0°	4°	8°
θ1	4°	7°	10°
θ2	0°	3°	6°

Package Marking and Ordering Information

Product ID	Pack	Marking	Qty(PCS)
TAPING	TO-263-3L		800