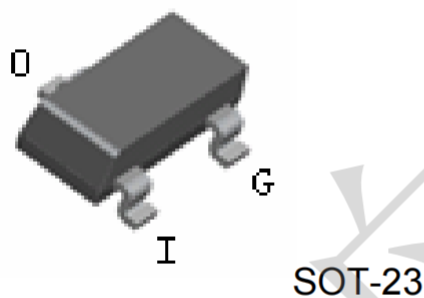


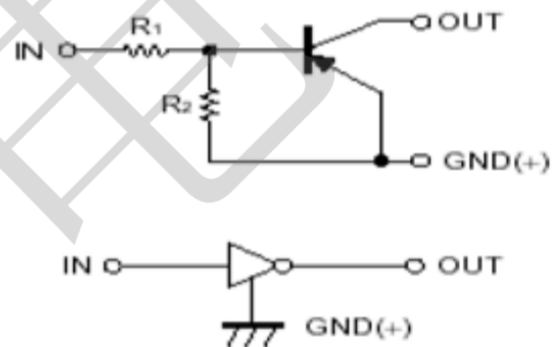
FEATURES

- Built-in bias resistor enable the configuration of an inverter circuit without connecting external input resistors.
- The bias resistors consist of thin-film resistors with complete isolation to allow negative biasing of the input. They also have the advantage of almost completely eliminating parasitic effects.
- Only the on/off conditions need to be set for operation, making device design easy.



APPLICATIONS

- The PNP style digital transistor.



MAXIMUM RATING @ Ta=25°C unless otherwise specified

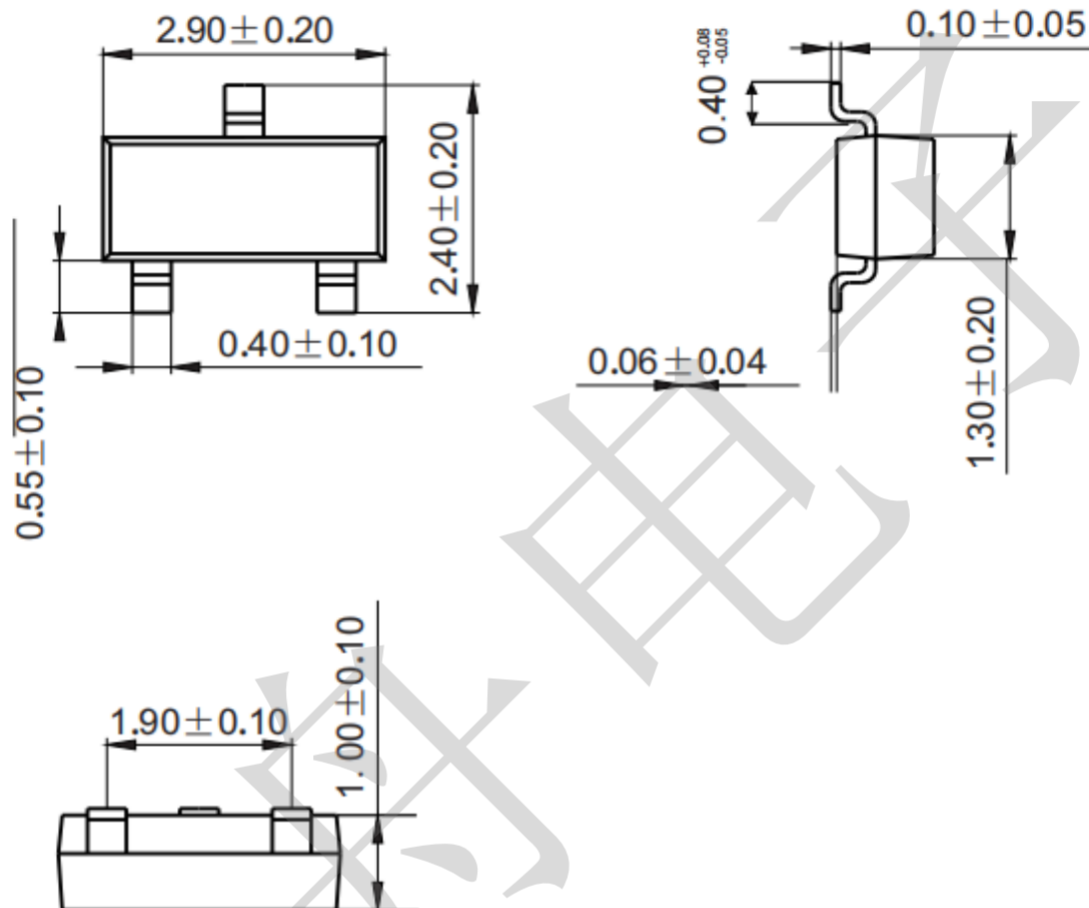
Symbol	Parameter	Value	Units
V_{CC}	Supply Voltage	-50	V
V_{IN}	Input Voltage	-10 to -30	V
I_O	Output Current	-100	mA
P_D	Power Dissipation	200	mW
$R_{\theta JA}$	Thermal Resistance, Junction to Ambient Air	833	°C/W
T_j, T_{stg}	Operating and Storage and Temperature Range	-55 to +150	°C

ELECTRICAL CHARACTERISTICS @ Ta=25°C unless otherwise specified

Parameter	Symbol	Test conditions	MIN	TYP	MAX	UNIT
Input Voltage	$V_{I(off)}$	$V_{CC}=-5V, I_O=-100\mu A$	-0.5	-1.1	-	V
	$V_{I(on)}$	$V_O=-0.3V, I_O=-20mA$	-	-1.9	-3	
Output Voltage	$V_{O(on)}$	$I_O/I_I=-10mA/-0.5mA,$	-	-0.1	-0.3	V
Input Current	I_I	$V_I=-5V$	-	-	-1.8	mA
Output Current	$I_{O(off)}$	$V_{CC}=-50V, V_I=0V$	-	-	-0.5	μA
DC Current Gain	G_I	$V_O=-5V, I_O=-10mA$	20	-	-	
Input Resistor (R_1) Tolerance	DR_1	-	30	-	30	%
Resistance Ratio	R_2/R_1	-	0.8	1	1.2	-
Gain-Bandwidth Product	f_T	$V_{CE}=10V, I_E=5mA,$ $f=100MHz$	-	250	-	MHz

Package Outline Dimensions (unit:mm)

SOT-23



Mounting Pad Layout (unit: mm)

