JLHF75W120R34E6DN

L34 module with GEN6 IGBT and emitter controlled diode

Features

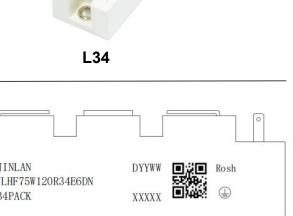
- Electrical features
 - V_{CES} = 1200 V
 - $IC_{nom} = 75 A / I_{CRM} = 150 A$
 - V_{CEsat} with positive temperature coefficient
- · Mechanical features
 - Standard housing
 - 2.5 kV AC 1 min insulation
 - High creepage and clearance distances
 - Isolated base plate

Typical Applications

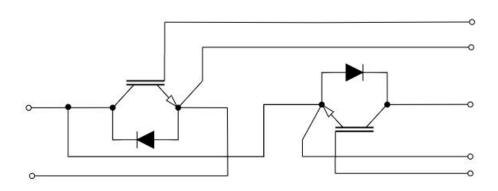
- Inverters
- Servo
- UPS (Uninterruptible Power Supplies)
- Welding

Description





QR code = Custom Assembly Information



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Package Insulation coordination

Symbol	Parameter	Note or test condition	Values	Unit
VisoL	Isolation test voltage	RMS,f=50Hz,t=60s	2.5	kV
d _{creep}	Creepage distance	terminal to heatsink	17.0	mm
d_{creep}	Creepage distance	terminal to terminal	20.0	mm
d _{clear}	Clearance	terminal to heatsink	17.0	mm
d _{clear}	Clearance terminal to terminal		9.5	mm
	Comparative tracking index			
СТІ	(electrical)		≥175	

Package Characteristic values

Symbol			Values				
Symbol	Description	Note or test condition		Min.	Тур.	Max.	Unit
L _{sCE}	Stray Inductance				30	-	nH
Rcc'+EE'	Module Lead Resistance, Terminal to Chip	Tc=25°C, per switch			0.75		mΩ
T _{stg}	Storage temperature			-40		125	°C
М	Mounting torque for module mounting	-Mounting according to valid application note	M5, Screw	2.5		5.0	Nm
М	Terminal connection torque	-Mounting according to valid application note	M6, Screw	3.0		5.0	Nm
G	Weight				150		g

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IGBT

Absolute Maximum Ratings (Tc = 25°C unless otherwise noted)

Symbol	Description	Note or test condition	Value	Unit
V_{CES}	Collector-Emitter Voltage	T _{vj} = 25 °C	1200	V
I _{CDC}	Continuous Collector	75	Α	
I _{CRM}	Repetitive peak collector current	Peak Collector Current@ tp=1ms	150	А
P _{tot}	Total power dissipation	$T_C = 25^{\circ}C, T_{vj \text{ max}} = 150^{\circ}C$	270	w
V_{GES}	Gate-emitter peak voltage		±30	V

Characteristics (Tc = 25°C unless otherwise noted)

Symbol	Parameter	Test Condition		Rating			Unit
Symbol	Farameter			Min	Тур	Max	
	Collector-Emitter Saturation Voltage	Ic=75A	Tj=25°C		2.0	2.8	V
$V_{CE(sat)}$	Collector-Entitler Saturation voltage	V _{GE} =15V	Tj=150°C		2.4		\ \ \
$V_{\text{GE(TH)}}$	Gate-Emitter Threshold Voltage	$I_C=3\text{mA}, V_{CE}=V_{GE}, T_{Vj}=25^{\circ}\text{C}$		4.5		6.5	V
I _{CES}	Collector-Emitter Cutoff Current	V _{GE} =0V,V _{CE} =120	0V,T _{vj} = 25°C	-		800	uA
I _{GES}	Gate-Emitter Leakage Current	$V_{GE} = \pm 20V$, $V_{CE} = 0$	0 V, T _{vj} = 25°C			100	nA
RGint	Internal Gate Resistance	T _{vj} = 25 °C			0.8		Ω
C _{ies}	Input Capacitance	$V_{CE}=30V, V_{GE}=0V, f=1MHz, T_{vj}=25^{\circ}C$			13830		
C _{oes}	Output Capacitance				320		pF
C _{res}	Reverse Transfer Capacitance				280		
Qg	Total Gate Charge				450		
Q _{ge}	Gate to Emitter Charge	V _{CC} =960V, I _C =75A, V _{GE} =15V,T _{VI} = 25°C			87		nC
Q _{gc}	Gate to Collector Charge				204		
$t_{\text{d(ON)}}$	Turn-on Delay Time				19		
t _r	Rise Time	V_{CE} =600V, I_{C} =75A, V_{GE} =15/-5V, R_{g} =7.8 Ω Inductive Load			17		
$t_{\text{d}(OFF)}$	Turn-Off Delay Time				170		ns
t_f	Fall Time				18		
Eon	Turn-On Switching Loss				5.5		
E _{off}	Turn-Off Switching Loss				2.5		mJ
Ets	Total Switching Loss				8.0		

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Jinlan Power Semiconductor(Wuxi).co.,LTD

Preliminary data

RthJC	Thermal resistance,junction to case	per IGBT		0.42		K/W
т.	Temperature under switching		4.0		4.504)	°C
l vj op	conditions		-40		150 ¹⁾	C

 $^{^{1)}}T_{vj op} > 150 ^{\circ}{
m C}$ is only allowed for operation at overload conditions. For detailed specifications please refer to AN 2018-14.

Diode

Absolute Maximum Ratings (Tc = 25°C unless otherwise noted)

	<u> </u>			
Symbol	Description	Note or test condition	Value	Unit
V_{RRM}	Repetitive peak reverse voltage	T _{vj} = 25 °C	1200	V
l _F	Continuous DC forward current		75	Α
I _{FRM}	Repetitive peak forward current	t _P = 1 ms	150	Α

Characteristics (Tc = 25°C unless otherwise noted)

Symbol	Parameter	Test Conditions	Rating			Units
			Min.	Тур.	Max.	Jills
V_{F}	Diode Forward Voltage	I _F =75A		3.3	4.2	V
Trr	Reverse Recovery Time			163		ns
I _{RRM}	Diode Peak Reverse Recovery Current	V _{CE} =600V,I _F =75A, V _{GE} =15/-5V		25		А
Q _{rr}	Reverse Recovery Charge	$R_g=7.8\Omega$		1.13		uC
E_{rec}	Reverse Recovery Energy			0.96		mJ
R _{thJC}	Thermal resistance,junction to case	per Diode		0.75		K/W
T _{vj op}	Temperature under switching conditions		-40		150 ²⁾	$^{\circ}$

 $^{^{2)}}T_{v_{j}op} > 150^{\circ}C$ is only allowed for operation at overload conditions. For detailed specifications please refer to AN 2018-14.

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Typical Electrical and Thermal Characteristics

Figure 1 Output Characteristics

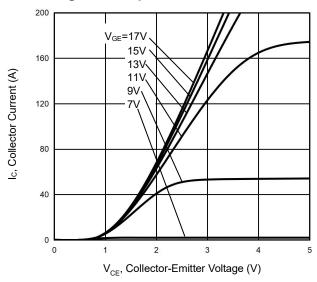


Figure 3 V_{CE(sat)} vs. Case Temperature

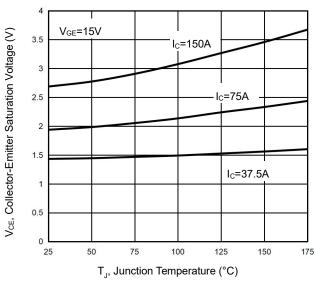


Figure 5 Capacitance Characteristics

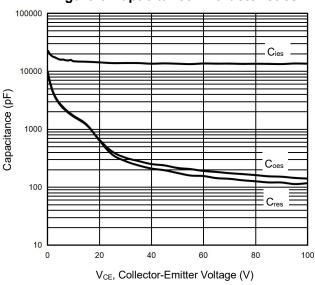


Figure 2 Transfer Characteristics

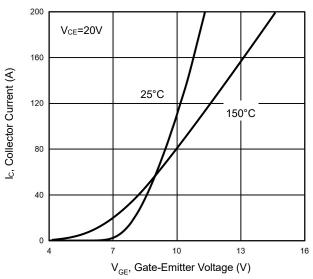


Figure 4 Saturation Voltage vs. V_{GE}

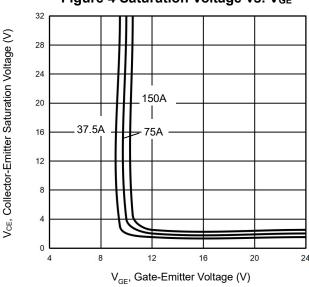
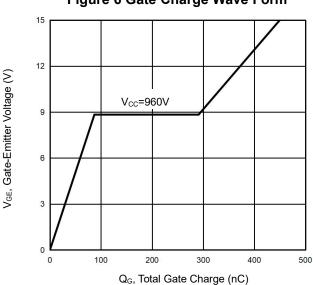


Figure 6 Gate Charge Wave Form



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Typical Electrical and Thermal Characteristic

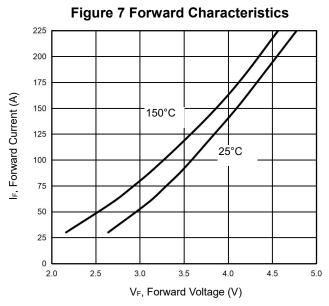


Figure 9 Switching Energy vs. Temperature

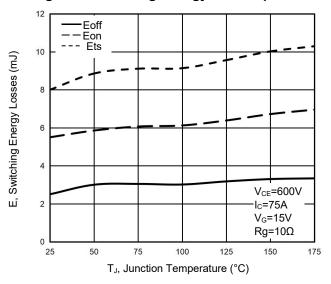


Figure 11 Gate-Emitter Threshold Voltage as a Function of Junction Temperature

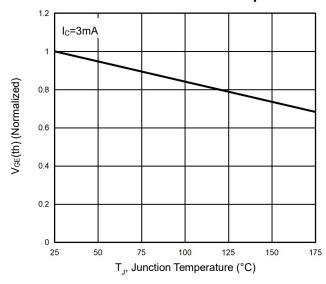


Figure 8 V_F vs. Temperature

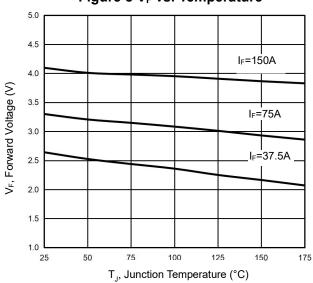


Figure 10 Forward Bias Safe Operating Area

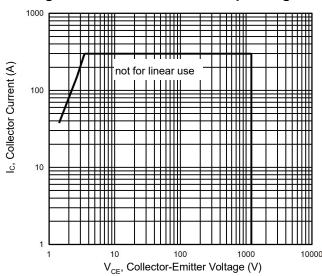
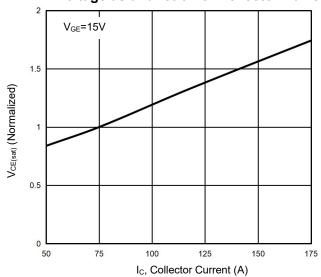


Figure 12 Typical Collector-Emitter Saturation
Voltage as a function of Collector Current



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Typical Electrical and Thermal Characteristics

Figure 13 Switching Loss vs. R_G

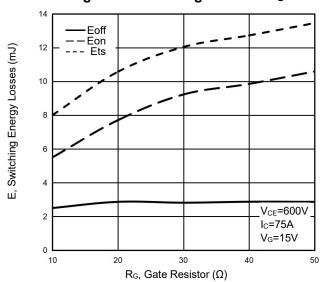


Figure 15 Switching Loss vs. Collector Current

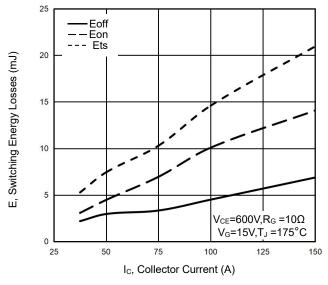


Figure 17 V_{CES} vs. Temperature

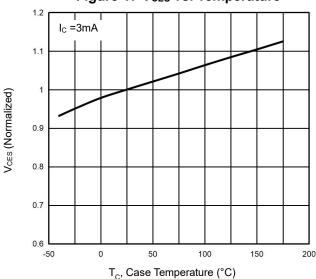


Figure 14 Switching Loss vs. Collector Current

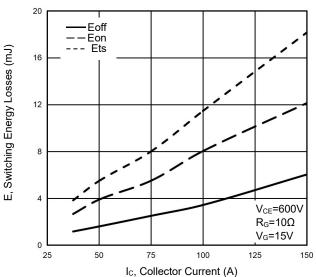
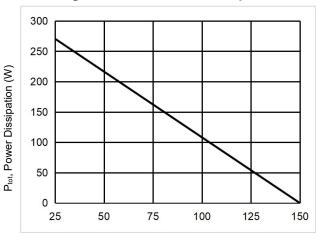
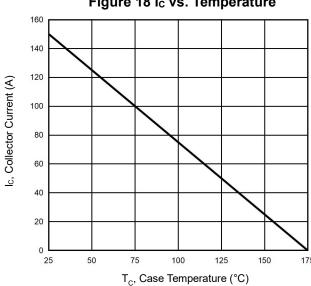


Figure 16 Ptot vs. Case Temperature



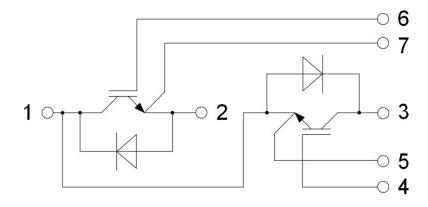
T_C, Case Temperature (°C)

Figure 18 Ic vs. Temperature



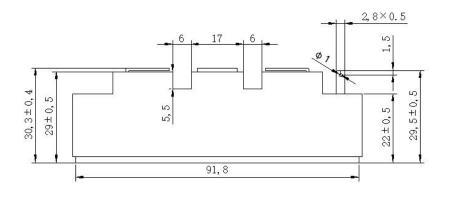


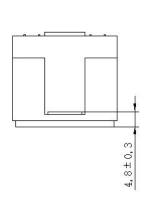
Circuit Diagram

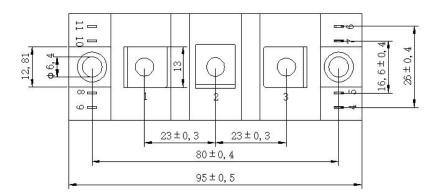


Package Dimensions

Dimensions in Millimeters









MODULE LABEL CODE



(9D): 2310

(Q): 18

(X): 85025

L340002P010001





L340002P01X85025Q(18)240

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L340002P01X85025Q(18)240





REVISION HISTORY

Document version	Date of release	Description of changes
Rev.00	2024-06-06	Preview

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