

General Description:

The LW3416AL uses trench technology and design to provide excellent $R_{DS(ON)}$ with low gate charge. It can be used in a wide variety of applications. The package form is SOT23-3L, which accords with the ROHS standard and Halogen Free standard.

Features:

- Fast Switching
- Low Gate Charge and $R_{DS(ON)}$
- Low Reverse transfer capacitances
- ESD Rating: HBM $\geq 1000V$

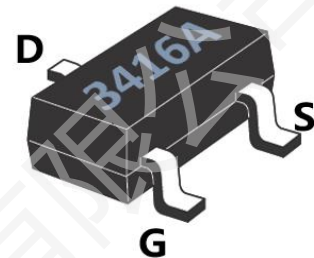
Applications:

- DC-DC converter
- Portable Equipment
- Power Management

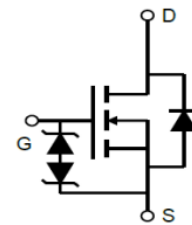


V_{DSS}	20	V
I_D	8.0	A
P_D	2.0	W
$R_{DS(ON)}$ TYPE	14	m Ω

Marking and Pin Assignment



Inner Equivalent Principium Chart



Package Marking and Ordering Information:

Marking	Part Number	Package	Packing	Qty.
3416A	LW3416AL	SOT23-3L	Reel	3000 Pcs

Absolute Maximum Ratings:

Symbol	Parameter		Value	Units
V_{DSS}	Drain-to-Source Voltage		20	V
I_D	Continuous Drain Current	$T_A = 25^\circ C$	8.0	A
	Continuous Drain Current	$T_A = 100^\circ C$	5.0	A
I_{DM}^{a1}	Pulsed Drain Current		32	A
V_{GS}	Gate-to-Source Voltage		± 10	V
P_D	Power Dissipation		2.0	W
T_J, T_{STG}	Operating Junction and Storage Temperature Range		150, -55 to 150	$^\circ C$
T_L	Maximum Temperature for Soldering		260	$^\circ C$

Thermal Characteristics:

Symbol	Parameter	Value	Units
$R_{\theta JA}^{a2}$	Thermal Resistance, Junction-to-Ambient	63	$^\circ C/W$

Electrical Characteristic ($T_A = 25\text{ }^{\circ}\text{C}$, unless otherwise specified):

Static Characteristics						
Symbol	Parameter	Test Conditions	Value			Units
			Min.	Typ.	Max.	
V_{DSS}	Drain to Source Breakdown Voltage	$V_{GS}=0V, I_D=250\mu A$	20	--	--	V
I_{DSS}	Drain to Source Leakage Current	$V_{DS}=20V, V_{GS}=0V$	--	--	1.0	μA
$I_{GSS(F)}$	Gate to Source Forward Leakage	$V_{GS}=+10V, V_{DS}=0V$	--	--	10	μA
$I_{GSS(R)}$	Gate to Source Reverse Leakage	$V_{GS}=-10V, V_{DS}=0V$	--	--	-10	μA
$V_{GS(TH)}$	Gate Threshold Voltage	$V_{DS}=V_{GS}, I_D=250\mu A$	0.4	0.7	1.0	V
$R_{DS(ON)1}$	Drain-to-Source On-Resistance	$V_{GS}=4.5V, I_D=7.0A$	--	14	18	$m\Omega$
$R_{DS(ON)2}$	Drain-to-Source On-Resistance	$V_{GS}=2.5V, I_D=4.0A$	--	18	23	$m\Omega$
$R_{DS(ON)3}$	Drain-to-Source On-Resistance	$V_{GS}=1.8V, I_D=3.0A$	--	26	39	$m\Omega$

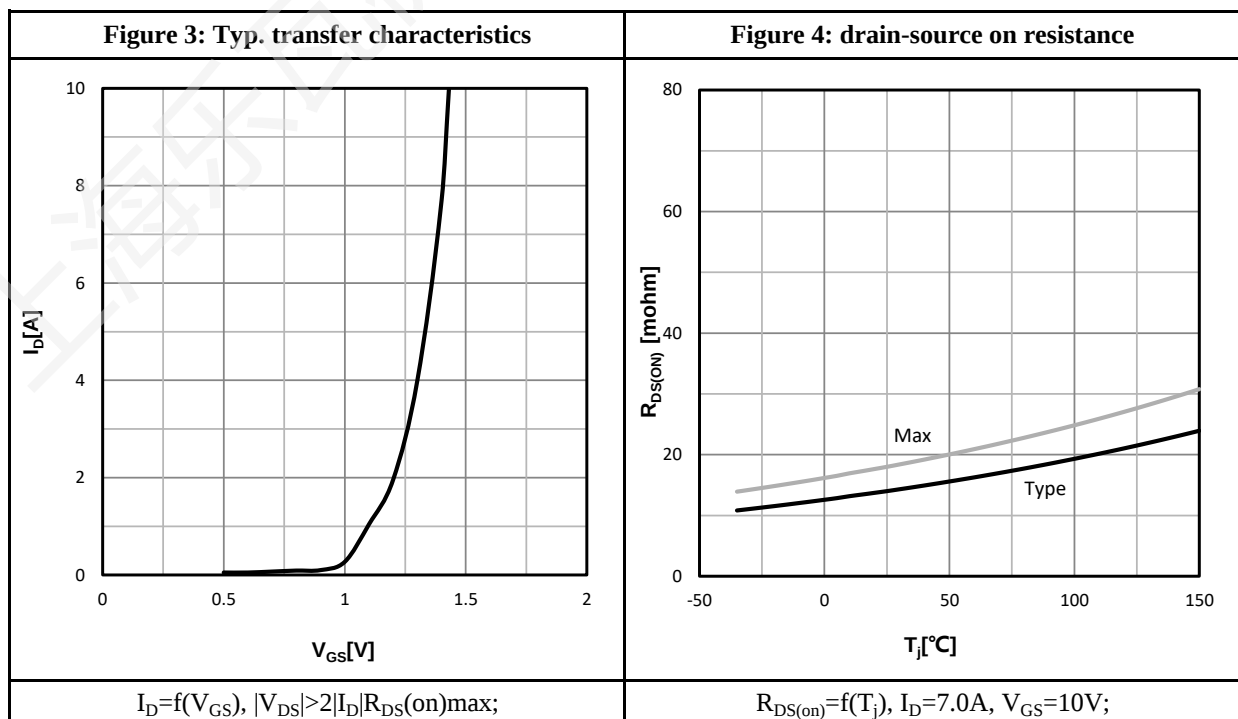
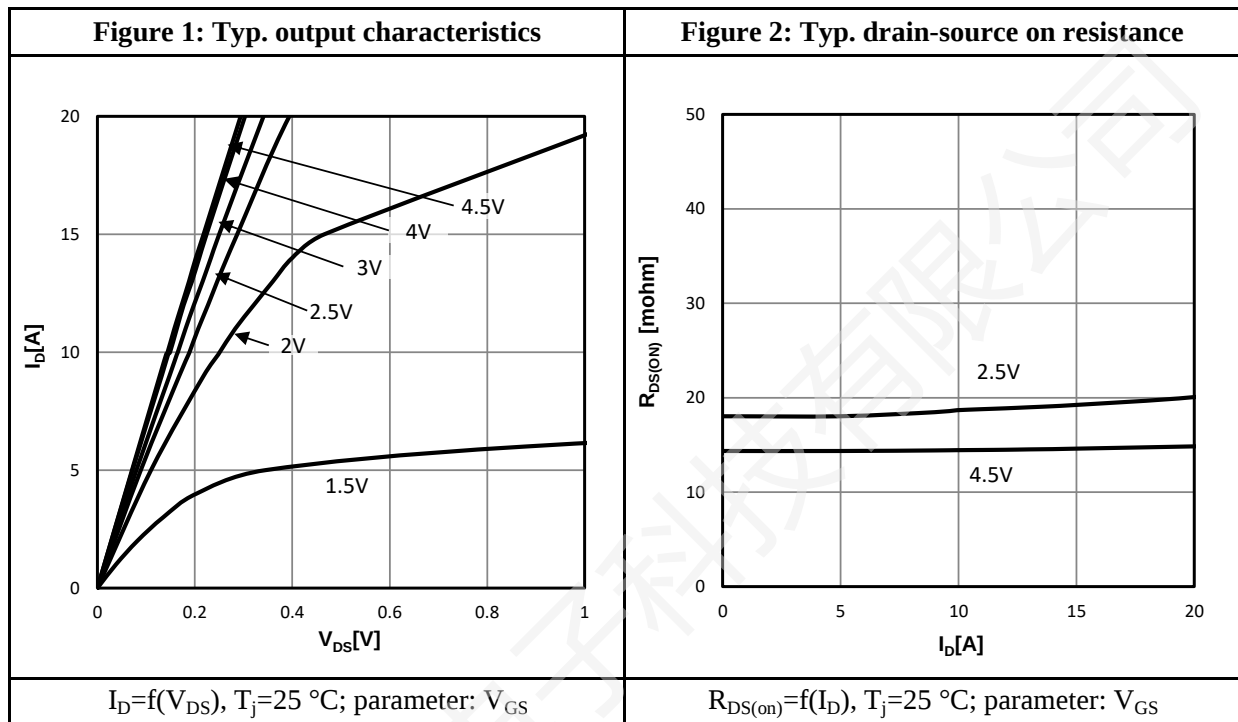
Dynamic Characteristics						
Symbol	Parameter	Test Conditions	Value			Units
			Min.	Typ.	Max.	
C_{iss}	Input Capacitance	$V_{GS} = 0V$ $V_{DS} = 10V$ $f = 1.0MHz$	--	686	--	pF
C_{oss}	Output Capacitance		--	108	--	
C_{rss}	Reverse Transfer Capacitance		--	101	--	
R_G	Gate resistance	$V_{GS} = 0V, V_{DS}=0V, f=1MHz$	--	5.0	--	Ω

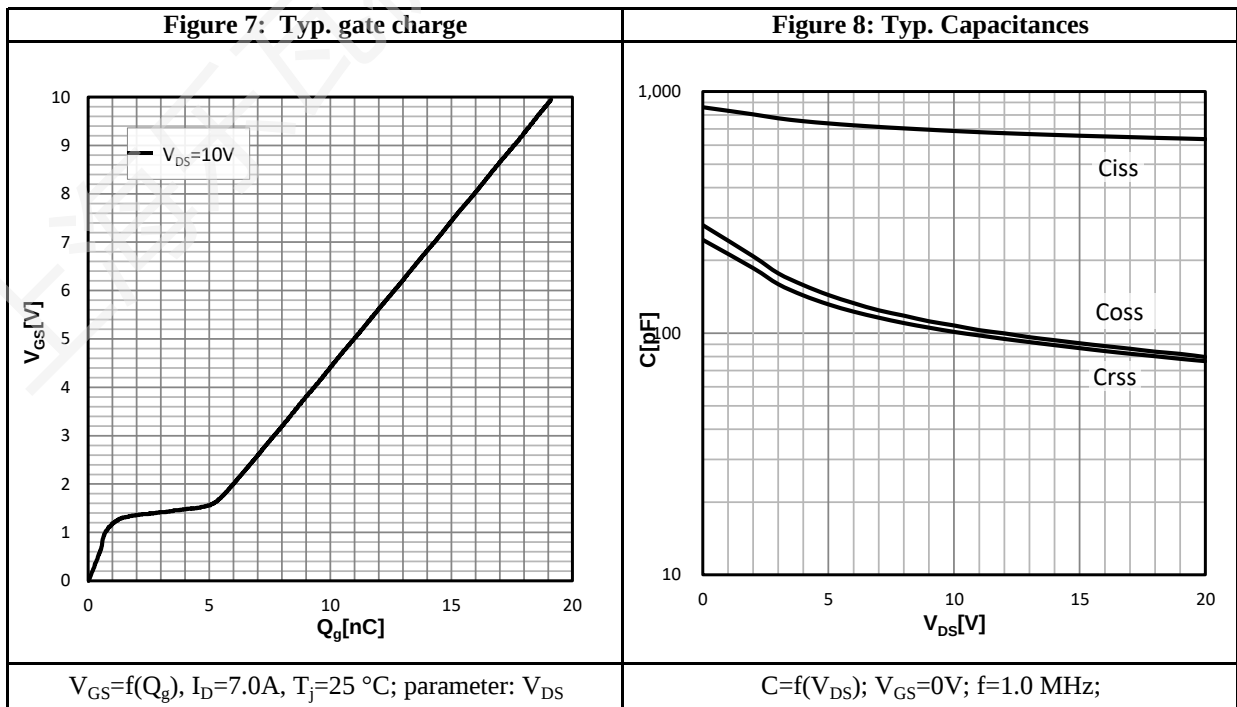
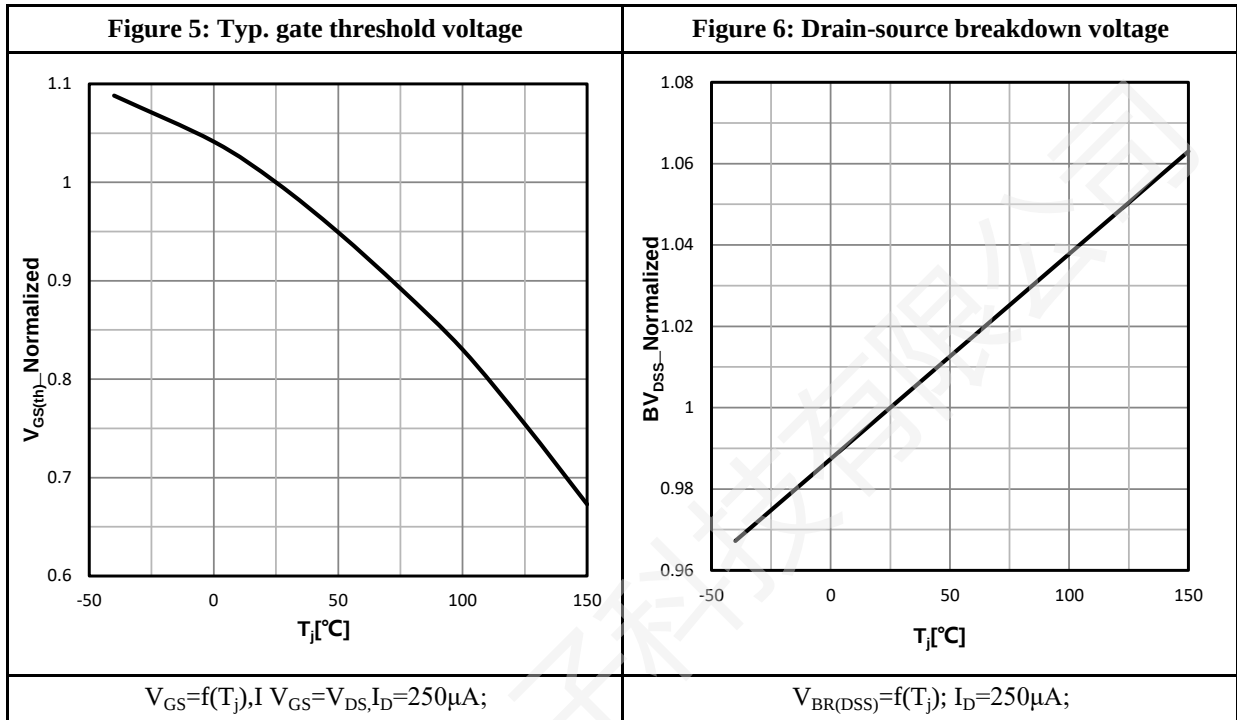
Resistive Switching Characteristics						
Symbol	Parameter	Test Conditions	Value			Units
			Min.	Typ.	Max.	
$t_{d(ON)}$	Turn-on Delay Time	$I_D = 7.0A$ $V_{DS} = 10V$ $V_{GS} = 10V$ $R_G = 3.0\Omega$	--	9.8	--	ns
t_r	Rise Time		--	61	--	
$t_{d(OFF)}$	Turn-Off Delay Time		--	35	--	
t_f	Fall Time		--	79	--	
Q_g	Total Gate Charge	$V_{GS} = 10V$ $V_{DS} = 10V$ $I_D = 7.0A$	--	19.2	--	nC
Q_{gs}	Gate Source Charge		--	0.83	--	
Q_{gd}	Gate Drain Charge		--	4.04	--	

Source-Drain Diode Characteristics						
Symbol	Parameter	Test Conditions	Value			Units
			Min.	Typ.	Max.	
I_S	Diode Forward Current	$T_A = 25\text{ }^{\circ}\text{C}$	--	--	8.0	A
V_{SD}	Diode Forward Voltage	$I_S = 7.0A, V_{GS} = 0V$	--	--	1.2	V
t_{rr}	Reverse Recovery time	$I_S = 7.0A, V_{DD} = 10V$ $dI/dt = 100A/\mu s$	--	16	--	ns
Q_{rr}	Reverse Recovery Charge		--	3.1	--	nC

a1: Repetitive rating; pulse width limited by maximum junction temperature

a2: Surface Mounted on FR4 Board, $t \leq 10\text{ sec.}$

Characteristics Curve:




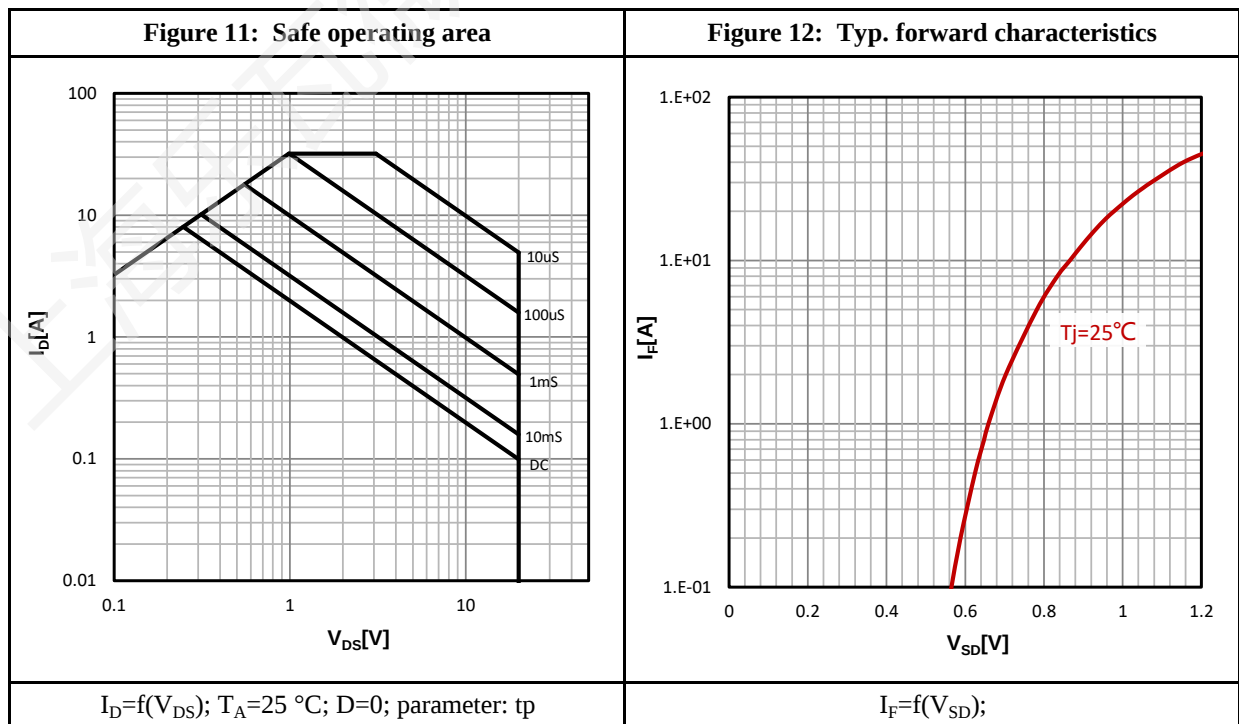
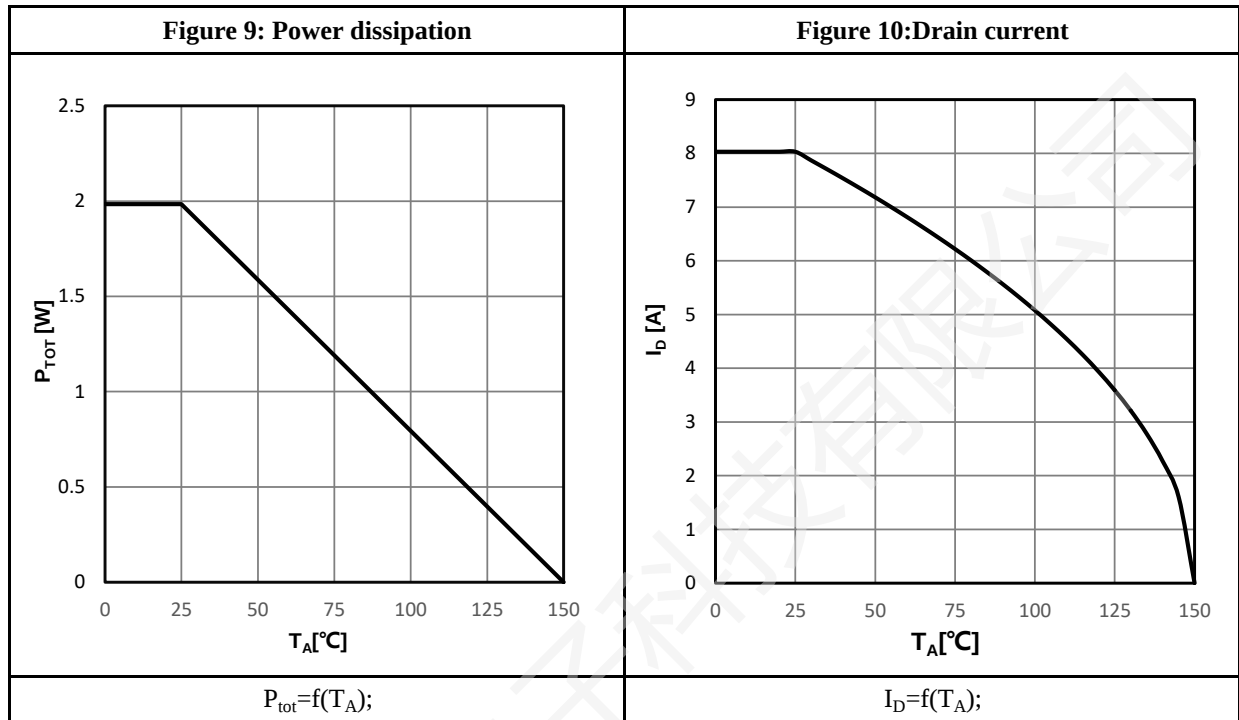
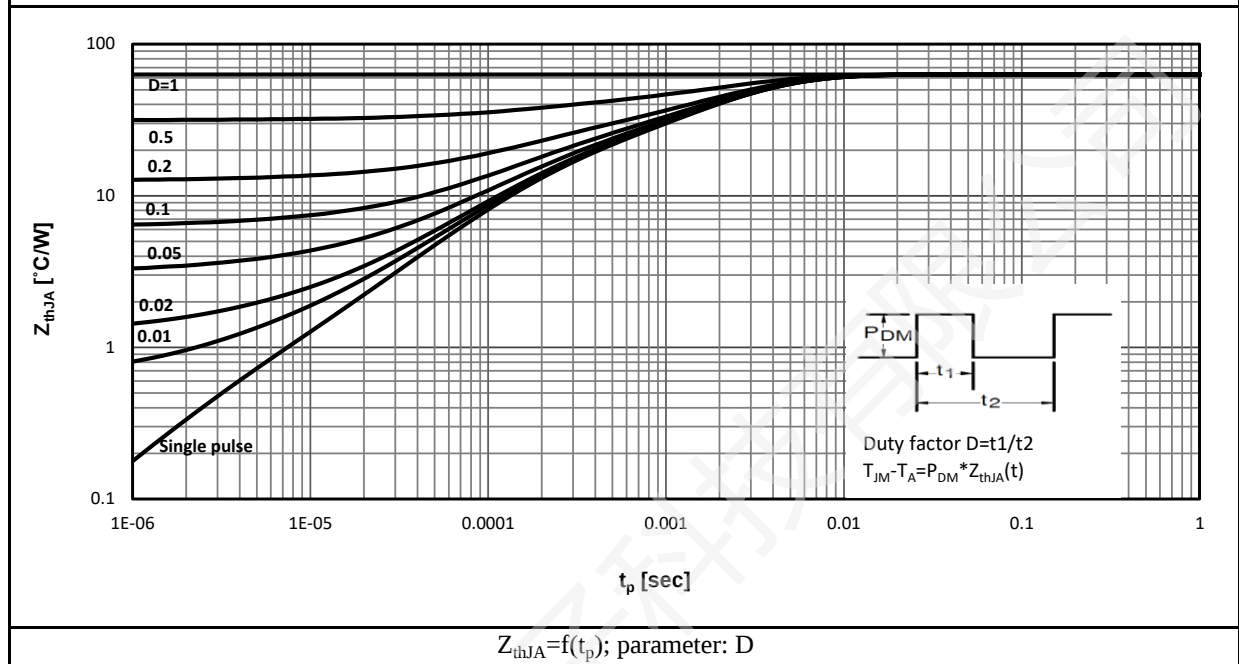
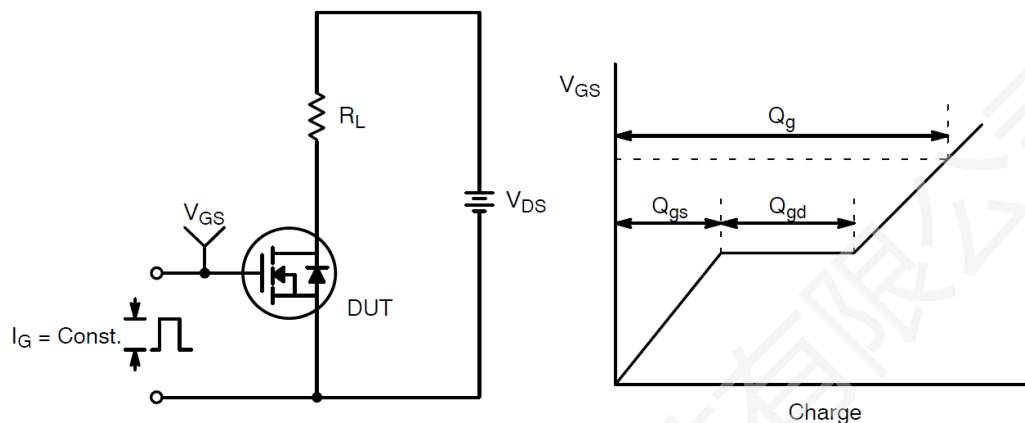
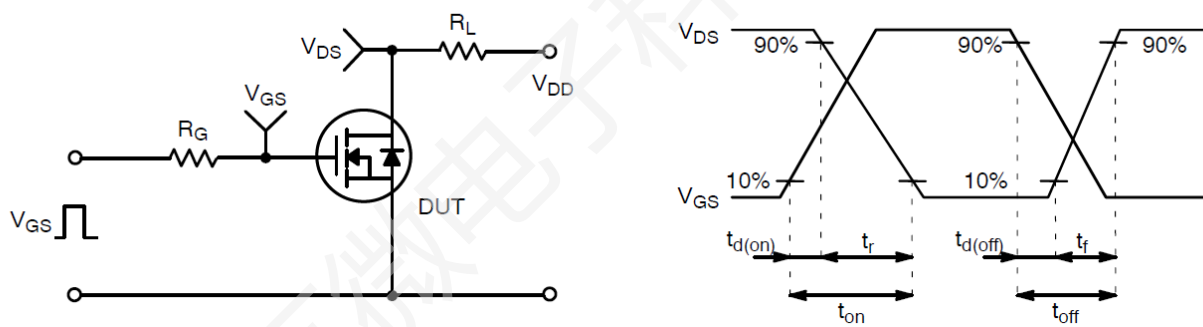
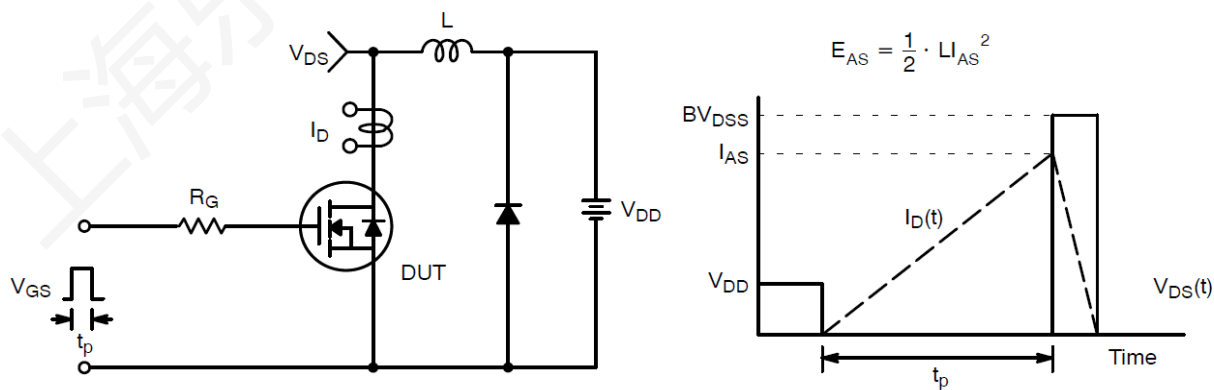
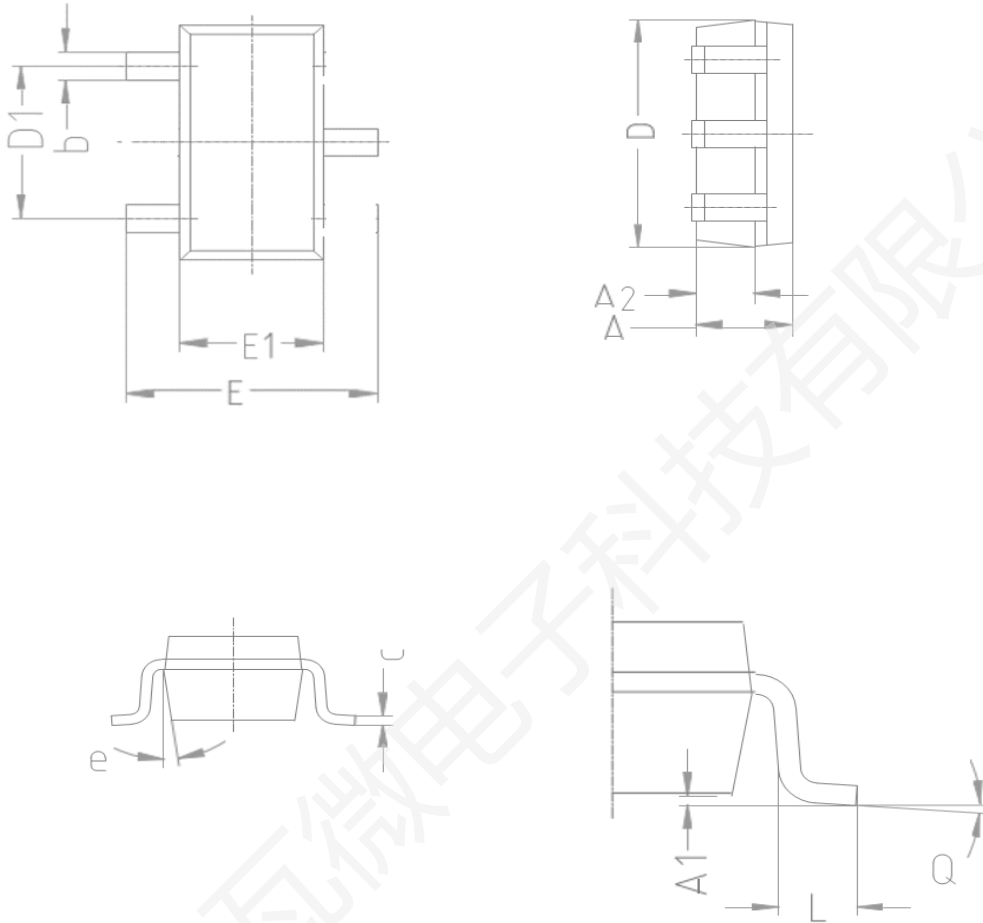


Figure 13: Max. Transient Thermal Impedance


Test Circuit & Waveform:

Figure 14: Gate Charge Test Circuit & Waveform

Figure 15: Resistive Switching Test Circuit & Waveforms

Figure 16: Unclamped Inductive Switching Test Circuit & Waveforms

Package Outline:


COMMON DIMENSION (MM)			
PKG	SOT23-3L		
Symbol	MIN	NOM	MAX
A	1.050	1.100	1.150
A1	0.010	0.060	0.150
A2	0.640	0.670	0.700
b	0.300	0.350	0.400
c	0.125	0.150	0.175
D	2.870	2.925	2.980
D1	1.800	1.900	2.000
E	2.650	2.825	3.000
E1	1.550	1.610	1.670
L	0.300	0.450	0.600
e	8°		
Q	0°	4°	8°

Revision History:

Revison	Date	Descriptions
Rev 1.1	Aug.2025	Formal Version
Rev 1.0	Mar.2024	Initial Version

Disclaimer:

The information in this document is believed to be accurate and reliable. However, no responsibility is assumed by LW-Micro for its use. All operating parameters must be designed, validated and tested to ensure they meet the requirements of your application. LW-Micro reserves the right to make any specification and/or circuitry changes without prior notification. Before starting a brand-new project, please contact LW-Micro Sales to get the most recent relevant information.

Mailing Address: Unit 02&04&05, 10th Floor, Building 5, No.666 Shengxia Road, No.122 Yindong Road,
China (Shanghai) Pilot Free Trade Zone
Shanghai Lewa Micro-electronics Technology Co., Ltd