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DWG. NO.
JBX01NSMPC

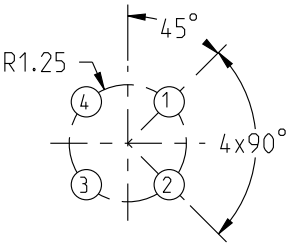
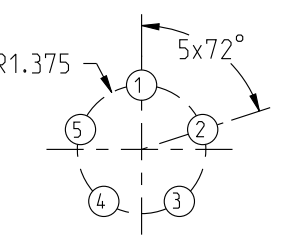
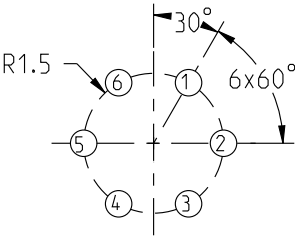
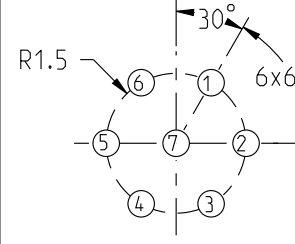
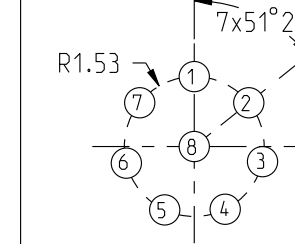
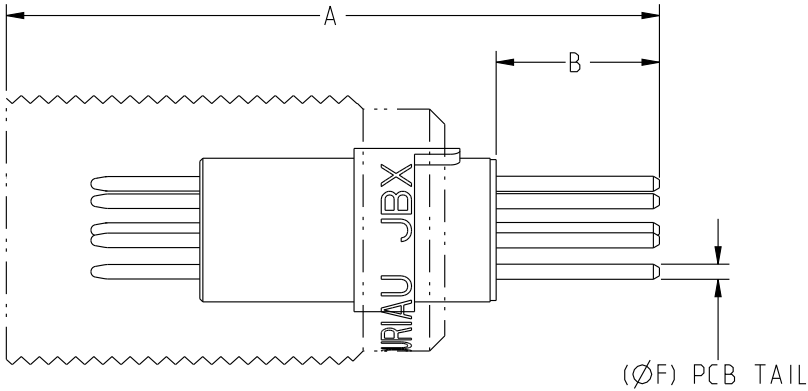
CONTACT LAYOUT	04	05	06	07	08
PCB LAYOUT (SHOWN FROM MATING FACE OF THE INSULATOR)					
1. ALL DIMENSIONS ARE ±0.05 2. SEE TABLE 1 FOR RECOMMENDED FINISHED PCB HOLE SIZE.					

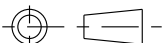
TABLE 1	
CONTACT PCB TAIL SIZE DIA	FINISHED PCB HOLE SIZE DIA
0.5	0.75



SAMPLE ASSEMBLY FOR INFORMATION

LAYOUT	04	05	06	07	08
A	24.3		21.6		
B	5.9		5.4		

ORIGIN	COUNTRY	JURISDICTION	CLASSIFICATION
X	FR	Not listed	N/A
	US	EAR	ECCN =EAR99
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LINEAR MEASURE:	
	
THIRD ANGLE PROJECTION	
	
TOLERANCES UNLESS OTHERWISE SPECIFIED	
No. OF PLACES	mm [INCH]
ONE PLACE	±3 [±.1]
TWO PLACES	±0.5 [±.02]
THREE PLACES	±0.25 [±.010]
ANGLES±1°	

DRAWING TITLE						
JBX, SIZE O, MALE INSULATOR, PCB CONTACT						
CAT NO.						
JBXOINSMP						
CAGE NO: 09922	SIZE: B		APPROVAL	DRAWN: SKV	02-02-15	
CUSTOMER	SOURIAU			CHKD :	EV	02-02-15
				DSGN :	EV	02-02-15
				MFG :	DH	02-02-15
				QC :	BB	02-02-15
				DRAWING SCALE: NA		
DRAWING NO.		REV				
JBXOINSMP C		B				
INTERPRET THIS DRAWING IN ACCORDANCE WITH ASME Y14.5M - 1994.		WWW.SOURIAU.COM		2 OF 2		

B

A