



MT29F512G08CECBBJ4-37

- [DATA SHEETS \(2\)](#) | [SPECS](#) | [SIM MODELS & SOFTWARE](#) | [ROHS CERTIFICATES](#) | [DOCUMENTATION & SUPPORT](#) | [PARTS WITH THE SAME DATA SHEET \(4\)](#) | [WHERE TO BUY](#)

Data Sheets (2)

[Data Sheet Variance](#)

[MT29F256G_512G_1T MLC DSV](#)
MT29F256G_512G_1T MLC DSV
File Type: PDF **Updated:** 12/2016
[3D NAND](#)
[MLC NAND](#)
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[Data Sheet](#)

[Datasheet: 256/512Gb/1Tb, MLC Async/Sync NAND, L06B](#)
Datasheet: 256/512Gb/1Tb, MLC Async/Sync NAND, L06B
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Specs

Orderable Parts for: MT29F512G08CECBBJ4-37

[SEE ALL MLC NAND PARTS](#)

	Status	Media	FBGA Code	SPD Data	Chipset Validation	PLP	Start Date	Alternative Part
MT29F512G08CECBBJ4-37:B	Production	N/A	NW826	N/A	N/A	No		N/A

Detailed Specifications

Density	512Gb	Status	Production	NAND Type	3D	RoHS	Yes
Width	x8	Voltage	3.3V	Package	VBGA	Pin Count	132-ball

Op. Temp.

0C to +70C

MT/s

533 MT/s

I/O

Separate

Product
Name

Sim Models & Software

Sim Models

Sim Model



Verilog: NAND 256Gb/512Gb/1Tb MLC L06B model (ZIP)

MLC NAND
NAND Flash

L06B NAND 256Gb/512Gb/1Tb/2Tb/4Tb Verilog model

File Type: ZIP

Updated: 01/20/2017

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HSpice



HSPICE: 256/512Gb/1Tb/2Tb/4Tb, MLC Async/Sync NAND, L06B (ZIP)

MLC NAND
NAND Flash

HSPICE: 256/512Gb/1Tb/2Tb/4Tb, MLC Async/Sync NAND, L06B Rev 2.0

File Type: ZIP

Updated: 12/13/2016

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IBIS



IBIS: 256/512Gb/1Tb/2Tb/4Tb, MLC Async/Sync NAND, L06B (ZIP)

MLC NAND
NAND Flash

IBIS: 256/512Gb/1Tb/2Tb/4Tb, MLC Async/Sync NAND, L06B Rev 2.0

File Type: ZIP

Updated: 12/13/2016

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Software

Software Support Pack

fixup-ubi-cannot-recover-master node issue (ZIP)

MLC NAND
NAND Flash

For MLC NAND, paired page issue is now a common known issue. This patch is just for master node cannot be recovered while there will two pages be damaged in one single master block.

File Type: ZIP

Updated: 03/31/2016

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Software Support Pack

mtd-nand-use-a-lower-value-for-badblockbits-when-working-with-MLC-NAND (ZIP)

MLC NAND
NAND Flash

MLC NANDs have more bit flips than SLC. When looking for bad block marker we have a lot of false positive if we check for the whole byte. To avoid this tolerate a few (4 here) bit flips for byte.

File Type: ZIP

Updated: 03/31/2016

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Software Support Pack

YAFFS mark bad block issue (ZIP)

MLC NAND
NAND Flash
SLC NAND

If the bitflips num over mtd->bitflip_threshold the mtd_read_oob will return -EUCLEAN and tags->ecc_result > YAFFS_ECC_RESULT_NO_ERROR. Then we will call yaffs_handle_chunk_error.

File Type: ZIP

Updated: 03/31/2016

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Software Support Pack

UBI power loss issue for paired page Ver2.0 (ZIP)

MLC NAND
NAND Flash

These patches aim to solve MLC NAND paired page power loss issue by adding a bakvol (backup volume) module in UBI layer. 70 series and 80 series families.

File Type: ZIP

Updated: 03/31/2016

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Software Support Pack

NAND FLASH lockunlock bug fix (ZIP)

MLC NAND
NAND Flash
SLC NAND

Reset NAND before write protect check.

File Type: ZIP

Updated: 03/31/2016

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RoHS Certificates

RoHS Certificates

RoHS Certificate of Compliance (PDF)

MT29F512G08CECBBJ4 37

Part-specific certification of how this product meets the requirements of the current DIRECTIVE 2002/95/EC, a.k.a. Restriction of Hazardous Substances (RoHS) Directive.

File Type: (PDF)

Updated: 01/2017

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RoHS Certificates

China RoHS Certificate (PDF)

MT29F512G08CECBBJ4 37

Part-specific certification as required by China's Management Methods for Controlling Pollution by Electronic Information Products.

File Type: (PDF)

Updated: 01/2017

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Documentation & Support

[See All MLC NAND Documentation](#)

Technical Notes

SEARCH (4) MLC NAND TECHNICAL NOTES

Technical Notes

TN-00-33: Power Integrity Simulation with IBIS 5.0 Models (PDF)

(TN-00-33) This technical note describes the new features in IBIS 5.0 enabling PI simulation. It also provides an overview of some of the modeling accuracy challenges and compares SSO simulation results using various electronic design automation (EDA) software tools.

File Type: PDF

Updated: 01/25/2016

3D NAND
DDR3 SDRAM
DDR4 SDRAM
DRAM
[See More Tags](#)

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Technical Notes

TN-00-01: Moisture Sensitivity of Plastic Packages (PDF)

(TN-00-01) This technical note describes shipping procedures for preventing memory devices from absorbing moisture and recommendations for baking devices exposed to excessive moisture.

File Type: PDF

Updated: 02/14/2013

DDR SDRAM
DDR2 SDRAM
DDR3 SDRAM
DDR3L-RS
[See More Tags](#)

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Technical Notes

TN-29-61: Wear Leveling in NAND Flash Memory (PDF)

(TN-29-61) This technical note describes the recommended wear leveling algorithm to be implemented in the FTL software for NAND Flash memory.

File Type: PDF

Updated: 10/15/2010

MLC NAND
NAND Flash
SLC NAND

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Technical Notes

TN-29-65: Software Device Drivers for Very Large Page Micron NAND Flash Memory (PDF)

(TN-29-65) Download the low-level driver described in this document [here](#).

File Type: PDF

Updated: 10/15/2010

MLC NAND
NAND Flash
SLC NAND

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SEARCH (4) MLC NAND TECHNICAL NOTES

Parts with the same Data Sheet (4)

MT29F512G08CECBBJ4-37 (Current)		MT29F256G08CBCBBWP-10	MT29F512G08CFCBBWP-10	MT29F256G08CBCBBJ4-37
Status	Production	Production	Production	Production
Density	512Gb	256Gb	512Gb	256Gb
NAND Type	3D	3D	3D	3D
RoHS	Yes	Yes	Yes	Yes
Width	x8	x8	x8	x8
Voltage	3.3V	3.3V	3.3V	3.3V
Package	VBGA	TSOP	TSOP	VBGA
Pin Count	132-ball	48-pin	48-pin	132-ball
Op. Temp.	0C to +70C	0C to +70C	0C to +70C	0C to +70C
MT/s	533 MT/s	200 MT/s	200 MT/s	533 MT/s
I/O	Separate	Common	Common	Common
Product Name				

Where to Buy

Orderable Parts

	Status	Media	FBGA Code	SPD Data	Chipset Validation	PLP	Start Date	Alternative Part
MT29F512G08CECBBJ4-37:B	Production	N/A	NW826	N/A	N/A	No		N/A

Contact Your Sales Rep

- OR - Check with Distributors

CONTACT A REP

Your Region: Americas



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