

GENERAL FEATURES

- $V_{DS} = -30V$ $I_D = -8A$
- $R_{DS(ON)} < -22m\Omega$ @ $V_{GS}=10V$
- $R_{DS(ON)} < -26m\Omega$ @ $V_{GS}=4.5V$

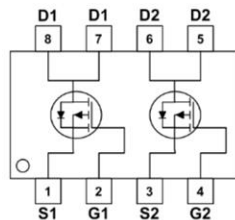
Application

- Load/Power Switching
- Interfacing Switching
- Logic Level Shift

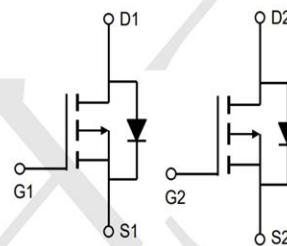
Package and Pin Configuration



SOP-8 top view



Circuit diagram



Absolute Maximum Ratings ($T_A=25^\circ C$ unless otherwise noted)

Symbol	Parameter	Rating	Units
V_{DS}	Drain-Source Voltage	30	V
V_{GS}	Gate-Source Voltage	± 20	V
$I_D @ T_A=25^\circ C$	Continuous Drain Current ³	8	A
$I_D @ T_A=70^\circ C$	Continuous Drain Current ³	7	A
I_{DM}	Pulsed Drain Current ¹	40	A
$P_D @ T_A=25^\circ C$	Total Power Dissipation	2	W
	Linear Derating Factor	0.016	W/ $^\circ C$
T_{STG}	Storage Temperature Range	-55 to 150	$^\circ C$
T_J	Operating Junction Temperature Range	-55 to 150	$^\circ C$

Thermal Data

Symbol	Parameter	Value	Unit
R_{thj-a}	Thermal Resistance Junction-ambient ³ Max.	62.5	$^\circ C/W$

Electrical Characteristics ($T_A=25^\circ\text{C}$ unless otherwise noted)

Symbol	Parameter	Test Conditions	Min.	Typ.	Max.	Units
BV_{DSS}	Drain-Source Breakdown Voltage	$V_{GS}=0V, I_D=250\mu A$	30	-	-	V
$\Delta BV_{DSS}/\Delta T_j$	Breakdown Voltage Temperature Coefficient	Reference to 25°C , $I_D=1\text{mA}$	-	0.01	-	$V/^\circ\text{C}$
$R_{DS(ON)}$	Static Drain-Source On-Resistance ²	$V_{GS}=10V, I_D=8A$		-	22	$m\Omega$
		$V_{GS}=4.5V, I_D=6A$		-	26	$m\Omega$
$V_{GS(th)}$	Gate Threshold Voltage	$V_{DS}=V_{GS}, I_D=250\mu A$	1	-	3	V
g_{fs}	Forward Transconductance	$V_{DS}=10V, I_D=8A$	-	8	-	S
I_{DSS}	Drain-Source Leakage Current ($T_j=25^\circ\text{C}$)	$V_{DS}=30V, V_{GS}=0V$	-	-	1	μA
	Drain-Source Leakage Current ($T_j=70^\circ\text{C}$)	$V_{DS}=24V, V_{GS}=0V$	-	-	25	μA
I_{GSS}	Gate-Source Leakage	$V_{GS}=\pm 20V$	-	-	± 100	nA
Q_g	Total Gate Charge ²	$I_D=8A$	-	14.5	23	nC
Q_{gs}	Gate-Source Charge	$V_{DS}=24V$	-	2.3	-	nC
Q_{gd}	Gate-Drain ("Miller") Charge	$V_{GS}=4.5V$	-	7.7	-	nC
$t_{d(on)}$	Turn-on Delay Time ²	$V_{DS}=15V$	-	7.2	-	ns
t_r	Rise Time	$I_D=1A$	-	8.6	-	ns
$t_{d(off)}$	Turn-off Delay Time	$R_G=3.3\Omega, V_{GS}=10V$	-	24.8	-	ns
t_f	Fall Time	$R_D=15\Omega$	-	8.6	-	ns
C_{iss}	Input Capacitance	$V_{GS}=0V$	-	950	1420	pF
C_{oss}	Output Capacitance	$V_{DS}=25V$	-	220	-	pF
C_{rss}	Reverse Transfer Capacitance	$f=1.0\text{MHz}$	-	160	-	pF
R_g	Gate Resistance	$f=1.0\text{MHz}$	-	1	1.5	Ω

Source-Drain Diode

Symbol	Parameter	Test Conditions	Min.	Typ.	Max.	Units
V_{SD}	Forward On Voltage ²	$I_S=1.7A, V_{GS}=0V$	-	-	1.2	V
t_{rr}	Reverse Recovery Time ²	$I_S=8A, V_{GS}=0V,$	-	25	-	ns
Q_{rr}	Reverse Recovery Charge	$di/dt=100A/\mu s$	-	21	-	nC

Typical Electrical and Thermal Characteristics

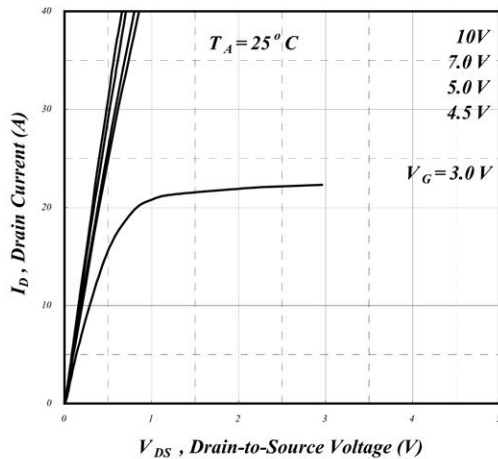


Fig 1. Typical Output Characteristics

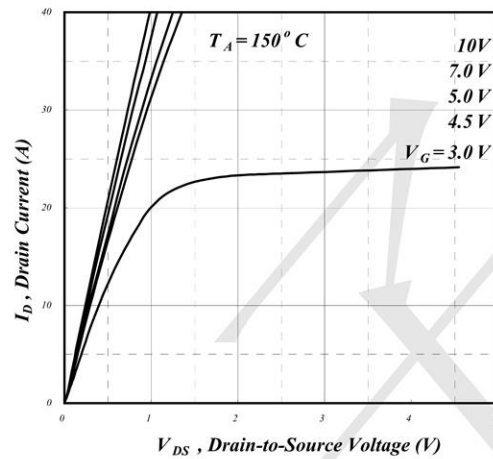


Fig 2. Typical Output Characteristics

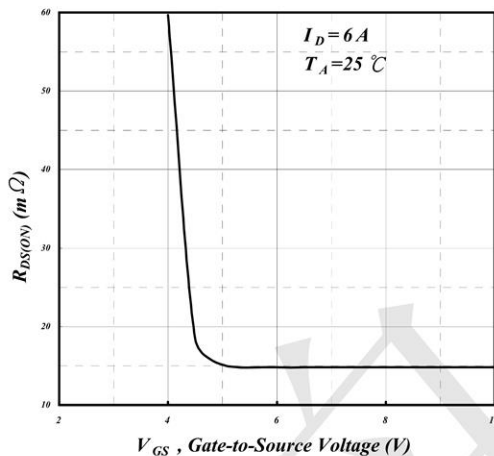


Fig 3. On-Resistance v.s. Gate Voltage

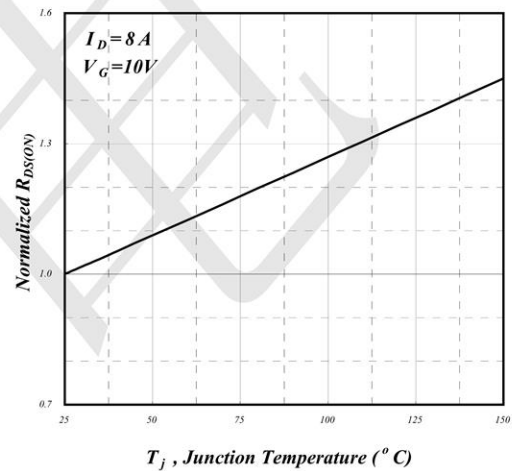


Fig 4. Normalized On-Resistance v.s. Junction Temperature

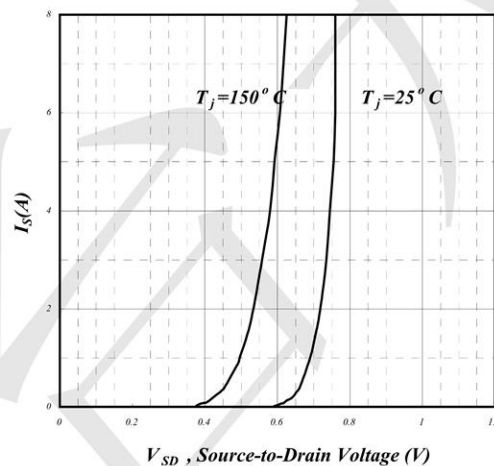


Fig 5. Forward Characteristic of Reverse Diode

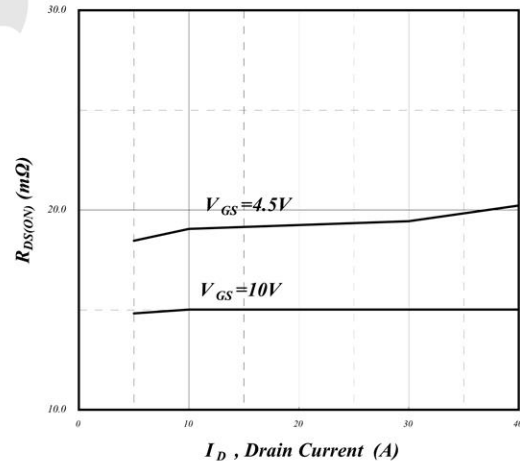


Fig 6. On-Resistance vs. Drain Current

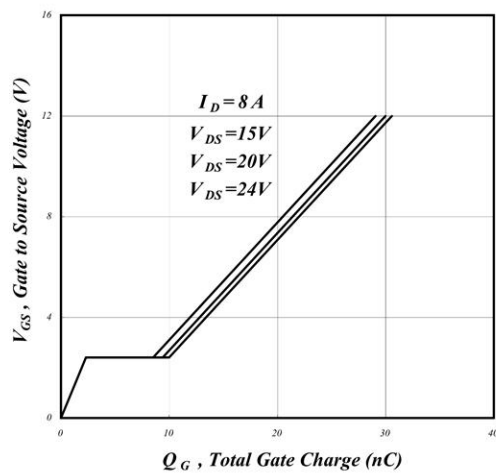


Fig 7. Gate Charge Characteristics

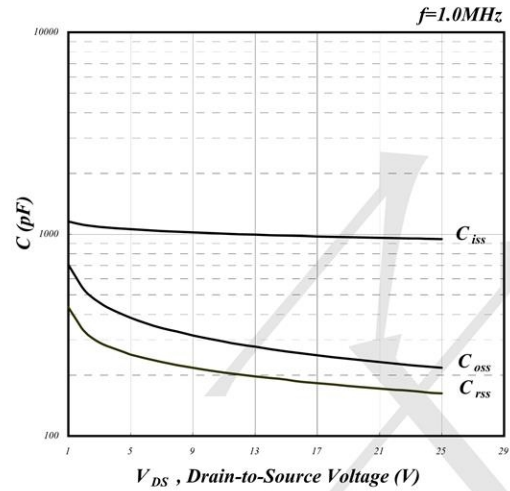


Fig 8. Typical Capacitance Characteristics

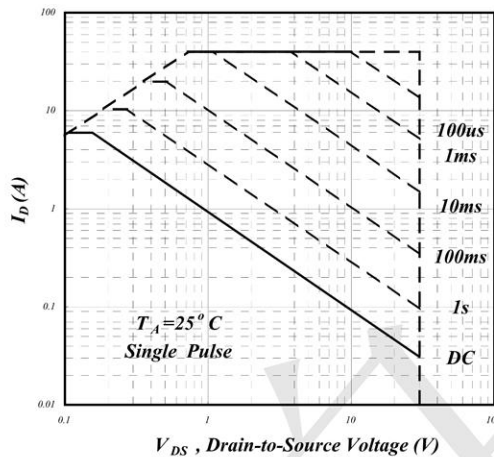


Fig 9. Maximum Safe Operating Area

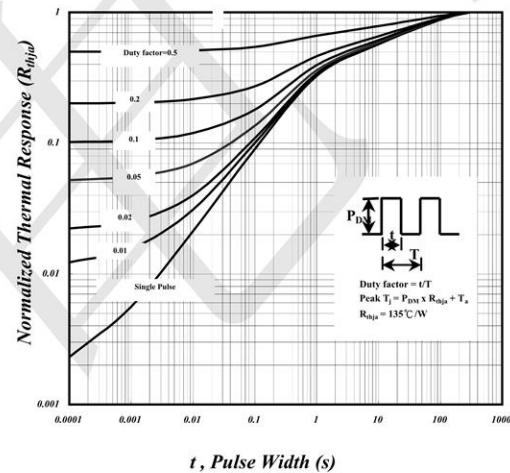


Fig 10. Effective Transient Thermal Impedance

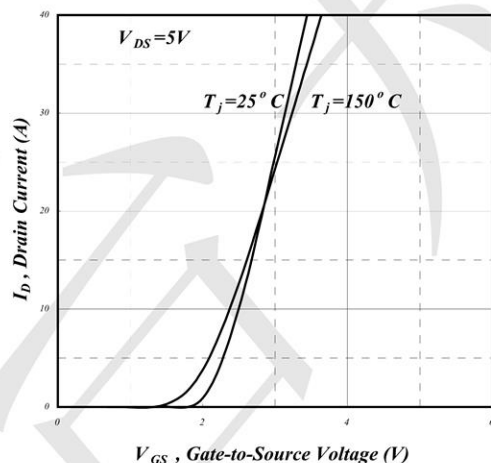


Fig 11. Transfer Characteristics

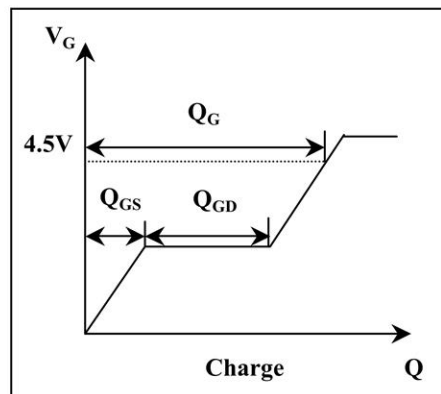
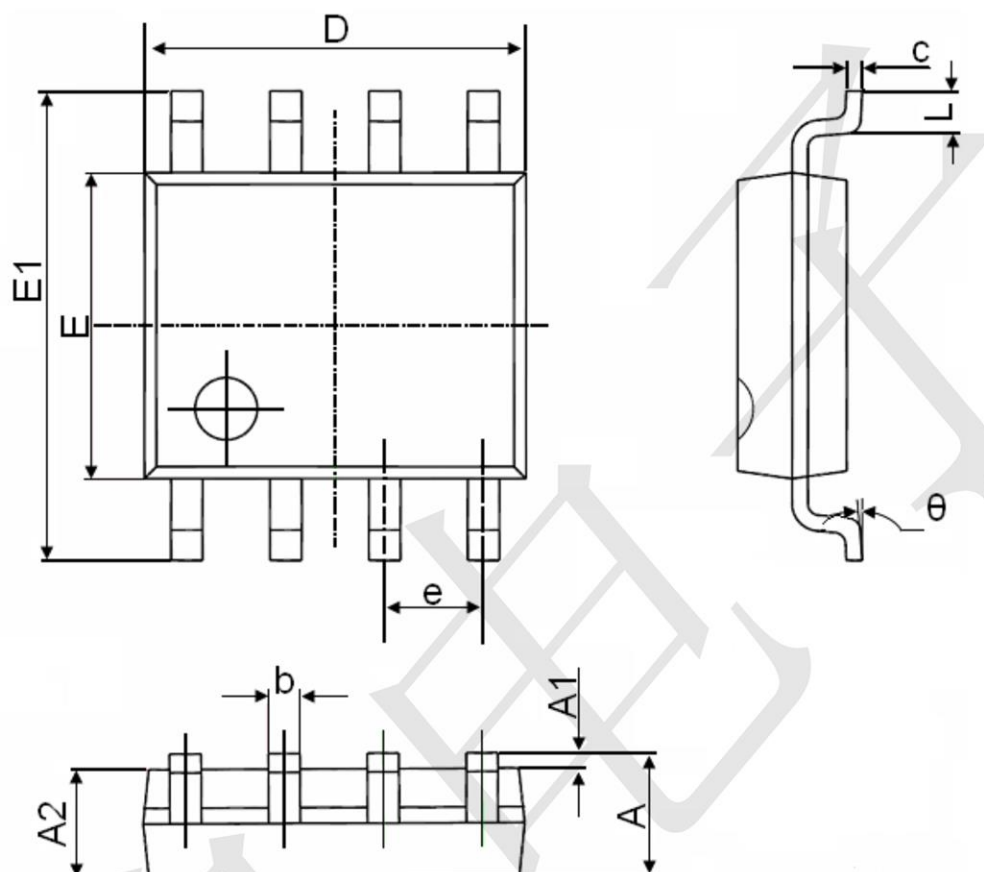


Fig 12. Gate Charge Waveform

SOP-8 Package Information



Symbol	Dimensions In Millimeters		Dimensions In Inches	
	Min.	Max.	Min.	Max.
A	1.350	1.750	0.053	0.069
A1	0.100	0.250	0.004	0.010
A2	1.350	1.550	0.053	0.061
b	0.330	0.510	0.013	0.020
c	0.170	0.250	0.006	0.010
D	4.700	5.100	0.185	0.200
E	3.800	4.000	0.150	0.157
E1	5.800	6.200	0.228	0.244
e	1.270(BSC)		0.050(BSC)	
L	0.400	1.270	0.016	0.050
θ	0°	8°	0°	8°