



Discription

The HPESDLC2FD5VUA protects sensitive semiconductor components from damage or upset due to electrostatic discharge (ESD) and other voltage induced transient events. Excellent clamping capability, low leakage, low capacitance, and fast response time provide best in class protection on designs that are exposed to ESD.

It gives designer the flexibility to protect one unidirectional line in applications where arrays are not practical.



DFN1006-2L

Features

- ★ Transient protection for high-speed data lines
IEC 61000-4-2(ESD) $\pm 20\text{kV}$ (Contact)
 $\pm 20\text{kV}$ (Air)
IEC 61000-4-4(EFT) 40A (5/50 ns)
- ★ Peak power dissipation: 60W (8/20 μs)
- ★ Working voltages : 5V
- ★ Ultra-small package (1.0mmx0.6mmx0.5mm)
- ★ Protects one data, control line
- ★ Low capacitance: 0.4pF (Typical)
- ★ Low clamping voltage
- ★ Low leakage current



Circuit Diagram

Ordering information

Product ID	Pack	Qty(PCS)
HPESDLC2FD5VUA	DFN1006-2L	10000

Absolute Ratings(Tamb = 25°C)

Symbol	Parameter	Value	Units
P _{PP}	Peak Pulse Power (t _p = 8/20 μs)	60	W
T _L	Maximum lead temperature for soldering during 10s	260	°C
T _{stg}	Storage Temperature Range	-55 to +150	°C
T _{op}	Operating Temperature Range	-55 to +150	°C
T _j	Maximum junction temperature	150	°C
	IEC61000-4-2 (ESD) air discharge	± 20	KV
	contact discharge	± 20	
	IEC61000-4-4 (EFT)	40	A



Electrical Characteristics Ratings at 25°C

Symbol	Parameter	Test Condition	Min	Typ	Max	Units
V_{RWM}	Reverse Working Voltage				5.0	V
V_{BR}	Reverse Breakdown Voltage	$I_T = 1mA$	6.0			V
I_R	Reverse Leakage Current	$V_{RWM} = 5V$			100	nA
V_C	Clamping Voltage	$I_{PP} = 1A, t_p = 8/20\mu s$			10	V
		$I_{PP} = 4A, t_p = 8/20\mu s$			15	V
C_J	Junction Capacitance	$V_R = 0V, f = 1MHz$		0.60	0.80	pF



Typical Characteristics

Fig 1 Power Derating Curve

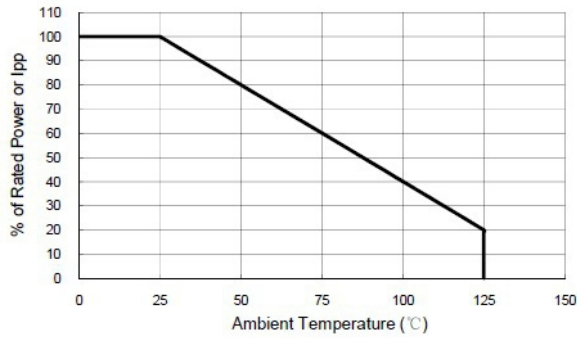


Fig 2 Clamping Voltage vs Peak Pulse Current

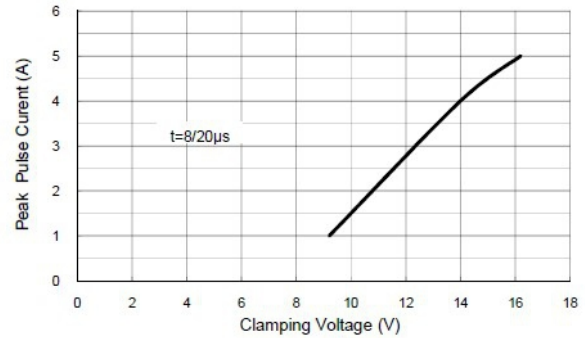


Fig 3 Voltage Sweeping

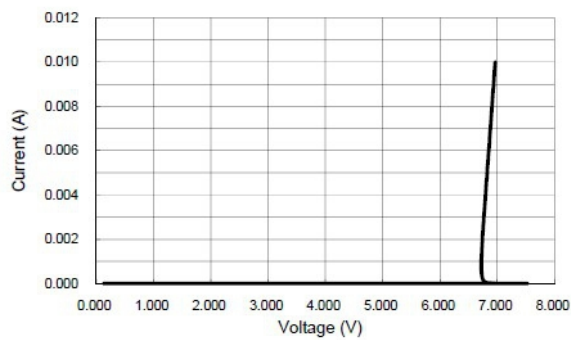


Fig 4 Voltage vs Capacitance

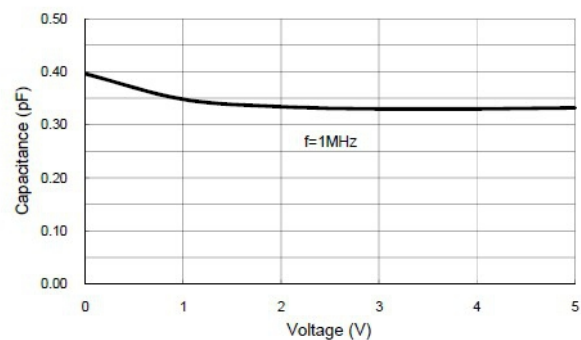


Fig 5 ESD Clamping
(+8kV Contact per IEC 61000-4-2)

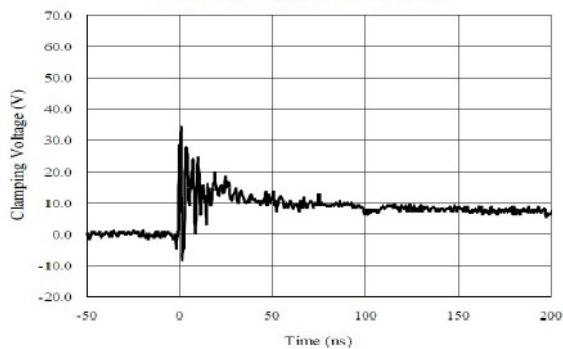
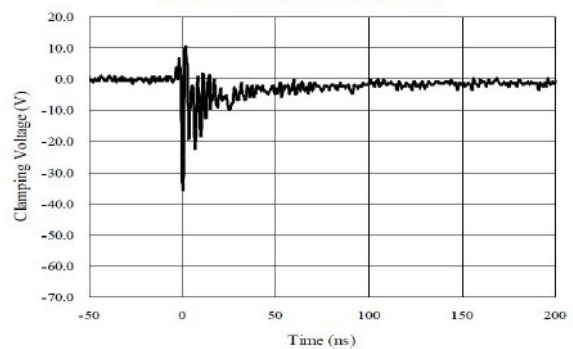
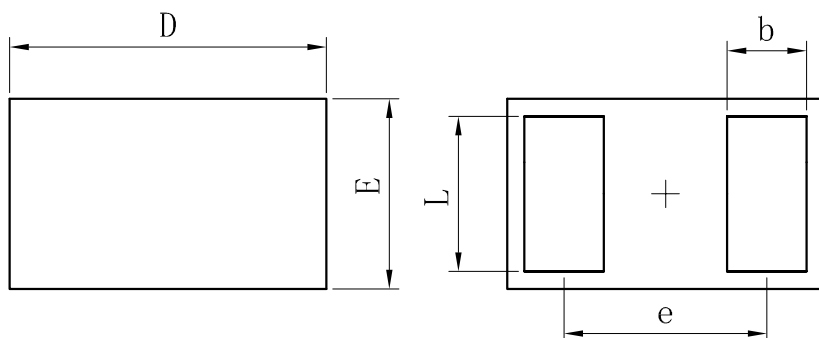


Fig 6 ESD Clamping
(-8kV Contact per IEC 61000-4-2)





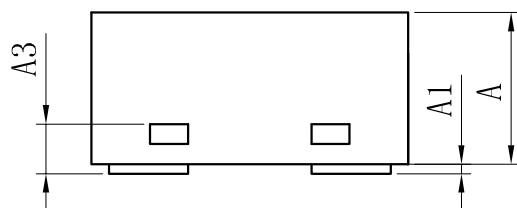
Outline And Dimensions



TOP VIEW

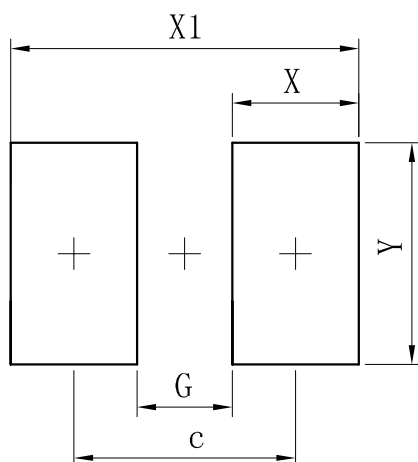
BOTTOM VIEW

DFN1006-2L			
Dim	Min	Typ	Max
D	0.95	1.00	1.05
E	0.55	0.60	0.65
e	–	0.64	–
L	0.44	0.49	0.54
b	0.20	0.25	0.30
A	0.43	0.48	0.53
A1	0	–	0.05
A3	0.127REF.		
All Dimensions in mm			



SIDE VIEW

Soledering Footprint



Dimensions	(mm)
c	0.70
G	0.30
X	0.40
X1	1.10
Y	0.70



Attention

- Any and all HUA XUAN YANG ELECTRONICS products described or contained herein do not have specifications that can handle applications that require extremely high levels of reliability, such as life-support systems, aircraft's control systems, or other applications whose failure can be reasonably expected to result in serious physical and/or material damage. Consult with your HUA XUAN YANG ELECTRONICS representative nearest you before using any HUA XUAN YANG ELECTRONICS products described or contained herein in such applications.
- HUA XUAN YANG ELECTRONICS assumes no responsibility for equipment failures that result from using products at values that exceed, even momentarily, rated values (such as maximum ratings, operating condition ranges, or other parameters) listed in products specifications of any and all HUA XUAN YANG ELECTRONICS products described or contained herein.
- Specifications of any and all HUA XUAN YANG ELECTRONICS products described or contained herein stipulate the performance, characteristics, and functions of the described products in the independent state, and are not guarantees of the performance, characteristics, and functions of the described products as mounted in the customer's products or equipment. To verify symptoms and states that cannot be evaluated in an independent device, the customer should always evaluate and test devices mounted in the customer's products or equipment.
- HUA XUAN YANG ELECTRONICS CO.,LTD. strives to supply high-quality high-reliability products. However, any and all semiconductor products fail with some probability. It is possible that these probabilistic failures could give rise to accidents or events that could endanger human lives, that could give rise to smoke or fire, or that could cause damage to other property. When designing equipment, adopt safety measures so that these kinds of accidents or events cannot occur. Such measures include but are not limited to protective circuits and error prevention circuits for safe design, redundant design, and structural design.
- In the event that any or all HUA XUAN YANG ELECTRONICS products(including technical data, services) described or contained herein are controlled under any of applicable local export control laws and regulations, such products must not be exported without obtaining the export license from the authorities concerned in accordance with the above law.
- No part of this publication may be reproduced or transmitted in any form or by any means, electronic or mechanical, including photocopying and recording, or any information storage or retrieval system, or otherwise, without the prior written permission of HUA XUAN YANG ELECTRONICS CO.,LTD.
- Information (including circuit diagrams and circuit parameters) herein is for example only ; it is not guaranteed for volume production. HUA XUAN YANG ELECTRONICS believes information herein is accurate and reliable, but no guarantees are made or implied regarding its use or any infringements of intellectual property rights or other rights of third parties.
- Any and all information described or contained herein are subject to change without notice due to product/technology improvement, etc. When designing equipment, refer to the "Delivery Specification" for the HUA XUAN YANG ELECTRONICS product that you intend to use.