

SN74AUP2G04DCKR-TP

Low-power dual inverter

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Features

- Wide supply voltage range from 0.8V to 3.6V
- Inputs accept voltages up to 3.6V
- I_{OFF} supports partial-power-down mode
- Low static power consumption; $I_{CC}=0.5\mu A$ (Max.)
- ESD Protection Exceeds JESD 22
 - 2000-V Human-Body Model (A114-A)
 - 200-V Machine Model (A115-A)
 - 1000-V Charged-Device Model (C101)

General Description

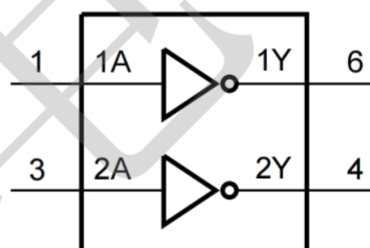
The SN74AUP2G04DCKR-TP is a dual inverter gate, it provides the function $Y = \overline{A}$.

This device has power-down protective circuit, preventing device destruction when it is powered down.

Applications

- AV Receiver
- Audio Dock: Portable
- Blu-ray Player and Home Theater
- Embedded PC
- Personal Digital Assistant (PDA)
- Power: Telecom/Server AC/DC Supply: Single Controller: Analog and Digital
- Solid State Drive (SSD): Client and Enterprise
- Wireless Headset, Keyboard, and Mouse

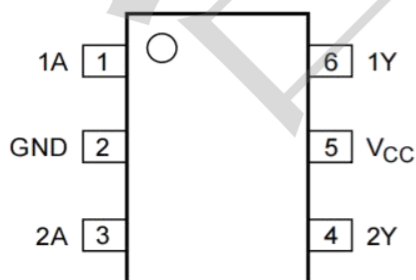
Logic Diagram



Ordering Information

ORDER NUMBER	PACKAGE DESCRIPTION	PACKAGE OPTION
SN74AUP2G04DCKR-TP	SOT363	Tape and Reel, 3000

Pin Configuration (TOP VIEW)



SOT363

Function Table (each gate)

Input	Output
nA	nY
L	H
H	L

Note: H: HIGH voltage level; L: LOW voltage level.

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Absolute Maximum Ratings

PARAMETER	SYMBOL	CONDITIONS	RATINGS	UNIT
Supply Voltage	V _{CC}		-0.5 ~ +4.6	V
Input Voltage	V _{IN}		-0.5 ~ +4.6	V
Output Voltage	V _{OUT}	Output in the high or low state	-0.5 ~ +V _{CC} +0.5V	V
		Output in the power-off state	-0.5 ~ +4.6	V
VCC or GND Current	I _{CC}		±50	mA
Continuous Output Current	I _{OUT}	V _{OUT} =0~V _{CC}	±20	mA
Input Clamp Current	I _{IK}	V _{IN} <0	-50	mA
Output Clamp Current	I _{OK}	V _{OUT} <0	-50	mA
Storage Temperature Range	T _{STG}		-65 ~ +150	°C
Junction to Ambient	θ _{JA}	SOT363	280	°C/W

Note: Absolute maximum ratings are those values beyond which the device could be permanently damaged. Absolute maximum ratings are stress ratings only and functional device operation is not implied.

Recommended Operating Conditions

PARAMETER	SYMBOL	TEST CONDITIONS	MIN	TYP	MAX	UNIT
Supply Voltage	V _{CC}	Operating	0.8	--	3.6	V
Input Voltage	V _{IN}		0	--	3.6	V
Output Voltage	V _{OUT}	High or low state	0	--	V _{CC}	V
Input Transition Rise or Fall Rate	Δt/Δv	V _{CC} =0.8V ~ 3.6V	--	--	200	ns/V
Operating Temperature	T _A		-40	--	+125	°C

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Electrical Characteristics (T_A=25°C ,unless otherwise specified)

PARAMETER	SYMBOL	TEST CONDITIONS	MIN	TYP	MAX	UNIT
High-level Input Voltage	V _{IH}	V _{CC} =0.8V	0.7×V _{CC}	--	--	V
		V _{CC} =1.1V ~ 1.95V	0.65×V _{CC}	--	--	V
		V _{CC} =2.3V ~ 2.7V	1.6	--	--	V
		V _{CC} =3V ~ 3.6V	2	--	--	V
Low-level Input Voltage	V _{IL}	V _{CC} =0.8V	--	--	0.3×V _{CC}	V
		V _{CC} =1.1V ~ 1.95V	--	--	0.35×V _{CC}	V
		V _{CC} =2.3V ~ 2.7V	--	--	0.7	V
		V _{CC} =3V ~ 3.6V	--	--	0.9	V
High-Level Output Voltage	V _{OH}	V _{CC} =0.8 ~ 3.6V, I _{OH} =-20μA	V _{CC} -0.1	--	--	V
		V _{CC} =1.1V, I _{OH} =-1.1mA	0.75×V _{CC}	--	--	V
		V _{CC} =1.4V, I _{OH} =-1.7mA	1.11	--	--	V
		V _{CC} =1.65V, I _{OH} =-1.9mA	1.32	--	--	V
		V _{CC} =2.3V	I _{OH} =-2.3mA	2.05	--	V
			I _{OH} =-3.1mA	1.9	--	V
		V _{CC} =3V	I _{OH} =-2.7mA	2.72	--	V
			I _{OH} =-4mA	2.6	--	V
Low-Level Output Voltage	V _{OL}	V _{CC} =0.8 ~ 3.6V, I _{OL} =20μA	--	--	0.1	V
		V _{CC} =1.1V, I _{OL} =1.1mA	--	--	0.3×V _{CC}	V
		V _{CC} =1.4V, I _{OL} =1.7mA	--	--	0.31	V
		V _{CC} =1.65V, I _{OL} =1.9mA	--	--	0.31	V
		V _{CC} =2.3V	I _{OL} =2.3mA	--	0.31	V
			I _{OL} =3.1mA	--	0.44	V
		V _{CC} =3V	I _{OL} =2.7mA	--	0.31	V
			I _{OL} =4mA	--	0.44	V
Input Leakage Current	I _{I(LEAK)}	V _{CC} =0 ~ 3.6V, V _{IN} =GND ~ 3.6V	--	--	±0.1	μA
Power OFF Leakage Current	I _{off}	V _{CC} =0 V, V _{IN} or V _{OUT} =0 ~ 3.6V	--	--	±0.2	μA
Additional Power OFF Leakage Current	ΔI _{off}	V _{CC} =0 V~0.2V, V _{IN} or V _{OUT} =0 ~ 3.6V	--	--	±0.2	μA
Quiescent Supply Current	I _{CC}	V _{CC} =0.8 ~3.6V, V _{IN} =V _{CC} or GND, I _{OUT} =0	--	--	0.5	μA
Additional Quiescent Supply Current Per Input Pin	ΔI _{CC}	V _{CC} =3.3 V, V _{IN} =V _{CC} -0.6V, I _{OUT} =0	--	--	40	μA
Input Capacitance	C _I	V _{CC} =0V-3.6V, V _{IN} =V _{CC} or GND	--	1.5	--	pF
Output Capacitance	C _{OUT}	V _{CC} =0V, V _{OUT} =GND	--	3	--	pF

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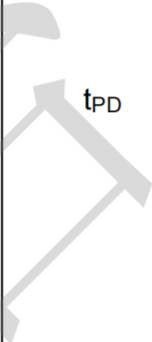
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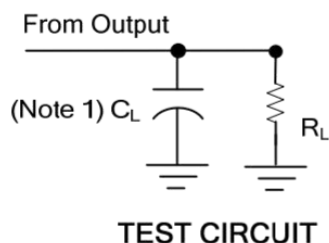
OPERATING CHARACTERISTICS (f=10MHz, TA =25°C , unless otherwise specified)

PARAMETER	SYMBOL	TEST CONDITIONS	MIN	TYP	MAX	UNIT
Power Dissipation Capacitance	C _{PD}	V _{CC} =0.8V	--	3.9	--	pF
		V _{CC} =1.2±0.1V	--	3.9	--	pF
		V _{CC} =1.5±0.1V	--	3.9	--	pF
		V _{CC} =1.8±0.15V	--	3.9	--	pF
		V _{CC} =2.5±0.2V	--	3.9	--	pF
		V _{CC} =3.3±0.3V	--	4.1	--	pF

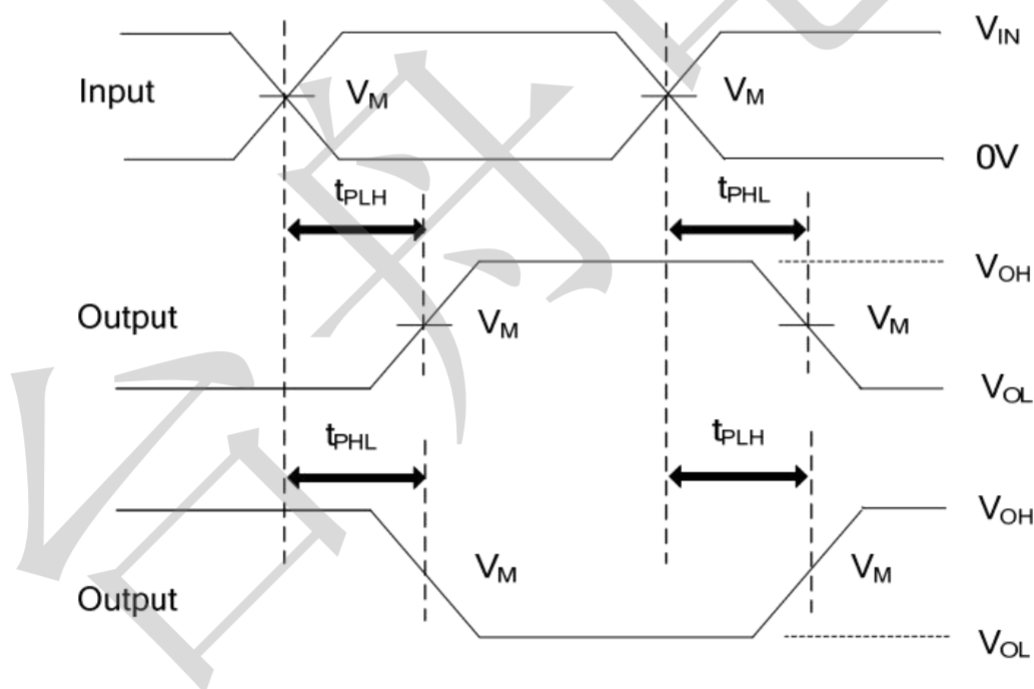
SWITCHING CHARACTERISTICS (TA =25°C, unless otherwise specified)

PARAMETER	SYMBOL	TEST CONDITIONS		MIN	TYP	MAX	UNIT
Propagation delay from inputs (A) to output(Y)		CL=5pF, RL=1MΩ	V _{CC} =0.8V	--	15.6	--	ns
			V _{CC} =1.2±0.1V	3.3	5.9	10.8	ns
			V _{CC} =1.5±0.1V	2.5	4.2	7.0	ns
			V _{CC} =1.8±0.15V	2.2	3.4	5.9	ns
			V _{CC} =2.5±0.2V	1.7	2.5	4.0	ns
			V _{CC} =3.3±0.3V	1.4	2.1	3.2	ns
		CL=10pF, RL=1MΩ	V _{CC} =0.8V	--	17.7	--	ns
			V _{CC} =1.2±0.1V	3.9	6.9	12.2	ns
			V _{CC} =1.5±0.1V	3.0	5.0	8.1	ns
			V _{CC} =1.8±0.15V	2.6	4.0	6.9	ns
			V _{CC} =2.5±0.2V	2.1	3.0	4.6	ns
			V _{CC} =3.3±0.3V	1.8	2.5	3.8	ns
		CL=15pF, RL=1MΩ	V _{CC} =0.8V	--	19.5	--	ns
			V _{CC} =1.2±0.1V	4.7	7.8	13	ns
			V _{CC} =1.5±0.1V	3.7	5.6	8.6	ns
			V _{CC} =1.8±0.15V	3.2	4.6	7.4	ns
			V _{CC} =2.5±0.2V	2.5	3.5	5.1	ns
			V _{CC} =3.3±0.3V	2.2	2.9	4.2	ns
		CL=30pF, RL=1MΩ	V _{CC} =0.8V	--	25.4	--	ns
			V _{CC} =1.2±0.1V	6.8	10.4	16	ns
			V _{CC} =1.5±0.1V	5.3	7.6	10.8	ns
			V _{CC} =1.8±0.15V	4.6	6.3	9.2	ns
			V _{CC} =2.5±0.2V	3.6	4.8	6.5	ns
			V _{CC} =3.3±0.3V	3.2	4.0	5.4	ns

TEST CIRCUIT AND WAVEFORMS



V_{CC}	V_{IN}	t_R / t_F	V_M	C_L	R_L
0.8V	V_{CC}	$\leq 3\text{ns}$	$V_{CC}/2$	5, 10, 15, 30pF	1M Ω
1.2V $\pm$ 0.1V	V_{CC}	$\leq 3\text{ns}$	$V_{CC}/2$	5, 10, 15, 30pF	1M Ω
1.5V $\pm$ 0.1V	V_{CC}	$\leq 3\text{ns}$	$V_{CC}/2$	5, 10, 15, 30pF	1M Ω
1.8V $\pm$ 0.15V	V_{CC}	$\leq 3\text{ns}$	$V_{CC}/2$	5, 10, 15, 30pF	1M Ω
2.5V $\pm$ 0.2V	V_{CC}	$\leq 3\text{ns}$	$V_{CC}/2$	5, 10, 15, 30pF	1M Ω
3.3V $\pm$ 0.3V	V_{CC}	$\leq 3\text{ns}$	$V_{CC}/2$	5, 10, 15, 30pF	1M Ω



PROPAGATION DELAY TIMES

Notes:

1. C_L includes probe and jig capacitance.
2. All input pulses are supplied by generators having the following characteristics: PRR $\leq 10\text{MHz}$, $Z_O = 50\Omega$.

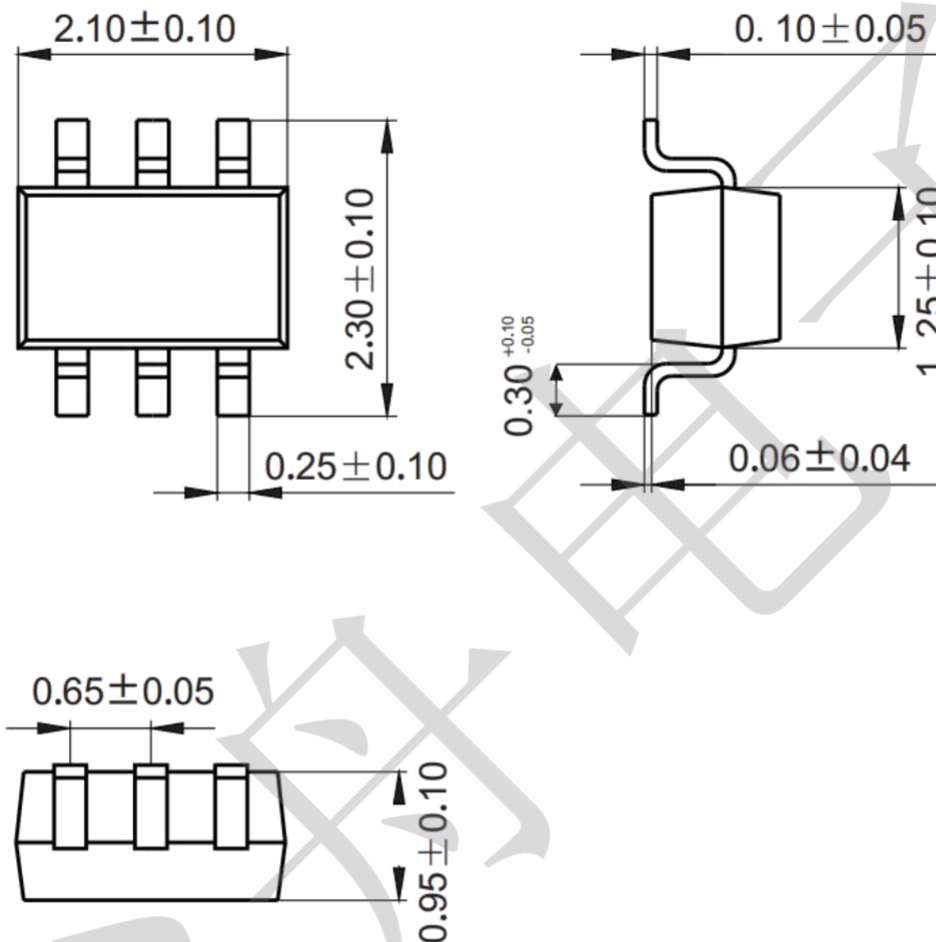
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Package information (Unit: mm)

SOT363



Mounting Pad Layout (unit: mm)

