

**Description**

The SX45P06D uses advanced trench technology to provide excellent  $R_{DS(ON)}$ , low gate charge and operation with gate voltages as low as 4.5V. This device is suitable for use as a Battery protection or in other Switching application.

**General Features**

$V_{DS} = -60V$   $I_D = -45A$

$R_{DS(ON)} < -25m\Omega @ V_{GS}=-10V$

**Application**

Lithium battery protection

Wireless impact

Mobile phone fast charging

**Absolute Maximum Ratings ( $T_c=25^\circ C$  unless otherwise noted)**

Symbol	Parameter	Rating	Units
$V_{DS}$	Drain-Source Voltage	-60	V
$V_{GS}$	Gate-Source Voltage	$\pm 20$	V
$I_D @ T_c=25^\circ C$	Continuous Drain Current, $-V_{GS} @ -10V^1$	-45	A
$I_D @ T_c=100^\circ C$	Continuous Drain Current, $-V_{GS} @ -10V^1$	-28	A
$I_{DM}$	Pulsed Drain Current <sup>2</sup>	-85	A
$E_{AS}$	Single Pulse Avalanche Energy <sup>3</sup>	113	mJ
$I_{AS}$	Avalanche Current	47.6	A
$P_D @ T_c=25^\circ C$	Total Power Dissipation <sup>4</sup>	52.1	W
$T_{STG}$	Storage Temperature Range	-55 to 150	°C
$T_J$	Operating Junction Temperature Range	-55 to 150	°C
$R_{\theta JA}$	Thermal Resistance Junction-Ambient <sup>1</sup>	62.5	°C/W
$R_{\theta JC}$	Thermal Resistance Junction-Case <sup>1</sup>	2.4	°C/W

**Electrical Characteristics ( $T_c=25^\circ\text{C}$  unless otherwise noted)**

Symbol	Parameter	Conditions	Min.	Typ.	Max.	Unit
BVDSS	Drain-Source Breakdown Voltage	$V_{GS}=0\text{V}$ , $I_D=-250\mu\text{A}$	-60	-68	---	V
$\Delta BVDSS/\Delta T_J$	$BVDSS$ Temperature Coefficient	Reference to $25^\circ\text{C}$ , $I_D=-1\text{mA}$	---	-0.035	---	$\text{V}/^\circ\text{C}$
RDS(ON)	Static Drain-Source On-Resistance <sup>2</sup>	$V_{GS}=-10\text{V}$ , $I_D=-10\text{A}$	---	20	25	$\text{m}\Omega$
		$V_{GS}=-4.5\text{V}$ , $I_D=-8\text{A}$	---	26	33	
VGS(th)	Gate Threshold Voltage	$V_{GS}=V_{DS}$ , $I_D = -250\mu\text{A}$	-1.0	-1.6	-2.5	V
$\Delta V_{GS(\text{th})}$	$V_{GS(\text{th})}$ Temperature Coefficient		---	4.28	---	$\text{mV}/^\circ\text{C}$
IDSS	Drain-Source Leakage Current	$V_{DS}=-48\text{V}$ , $V_{GS}=0\text{V}$ , $T_J=25^\circ\text{C}$	---	---	1	$\text{uA}$
		$V_{DS}=-48\text{V}$ , $V_{GS}=0\text{V}$ , $T_J=55^\circ\text{C}$	---	---	5	
IGSS	Gate-Source Leakage Current	$V_{GS}=\pm 20\text{V}$ , $V_{DS}=0\text{V}$	---	---	$\pm 100$	nA
gfs	Forward Transconductance	$V_{DS}=-10\text{V}$ , $I_D=-18\text{A}$	---	23	---	S
Rg	Gate Resistance	$V_{DS}=0\text{V}$ , $V_{GS}=0\text{V}$ , $f=1\text{MHz}$	---	7	---	$\Omega$
Qg	Total Gate Charge (-4.5V)	$V_{DS}=-20\text{V}$ , $V_{GS}=-4.5\text{V}$ , $I_D=-12\text{A}$	---	25	---	nC
Qgs	Gate-Source Charge		---	6.7	---	
Qgd	Gate-Drain Charge		---	5.5	---	
Td(on)	Turn-On Delay Time	$V_{DD}=-15\text{V}$ , $V_{GS}=-10\text{V}$ , $R_G=3.3\Omega$ , $I_D=-1\text{A}$	---	38	---	ns
Tr	Rise Time		---	23.6	---	
Td(off)	Turn-Off Delay Time		---	100	---	
Tf	Fall Time		---	6.8	---	
Ciss	Input Capacitance	$V_{DS}=-15\text{V}$ , $V_{GS}=0\text{V}$ , $f=1\text{MHz}$	---	3635	---	pF
Coss	Output Capacitance		---	224	---	
Crss	Reverse Transfer Capacitance		---	141	---	
Is	Continuous Source Current <sup>1,5</sup>	$V_G=V_D=0\text{V}$ , Force Current	---	---	-35	A
ISM	Pulsed Source Current <sup>2,5</sup>		---	---	-70	A
VSD	Diode Forward Voltage <sup>2</sup>	$V_{GS}=0\text{V}$ , $I_S=-1\text{A}$ , $T_J=25^\circ\text{C}$	---	---	-1	V

**Note :**

- 1、The data tested by surface mounted on a 1 inch 2 FR-4 board with 2OZ copper.
- 2、The data tested by pulsed , pulse width  $\leq 300\mu\text{s}$  , duty cycle  $\leq 2\%$
- 3、The EAS data shows Max. rating . The test condition is  $VDD=-48\text{V}$ , $VGS =-10\text{V}$ , $L=0.1\text{mH}$ , $IAS =-47.6\text{A}$
- 4、The power dissipation is limited by  $150^\circ\text{C}$  junction temperature
- 5、The data is theoretically the same as  $I_D$  and  $I_{DM}$  , in real applications , should be limited by total power dissipation.

### Typical Characteristics

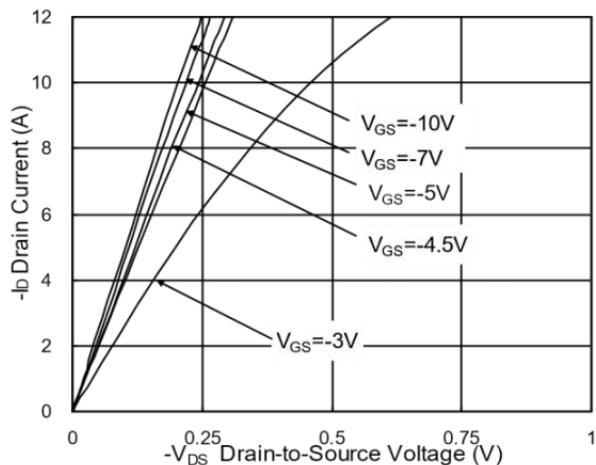


Fig.1 Typical Output Characteristics

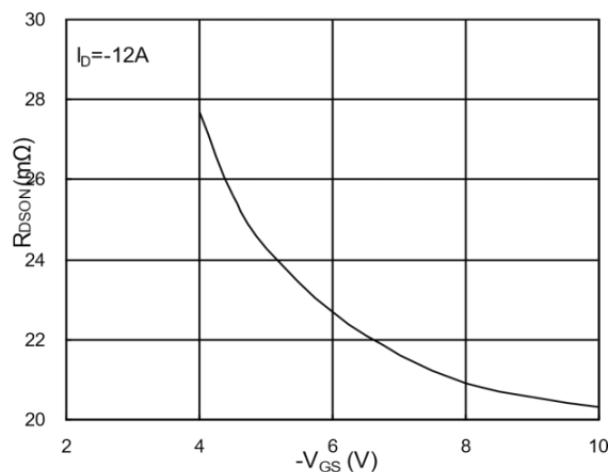


Fig.2 On-Resistance v.s Gate-Source

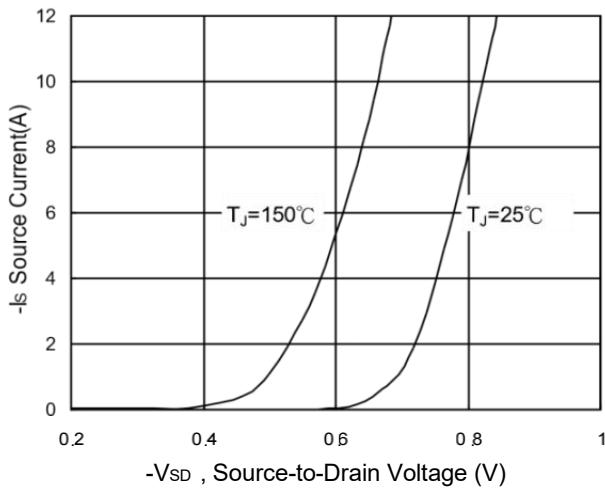


Fig.3 Forward Characteristics Of Reverse

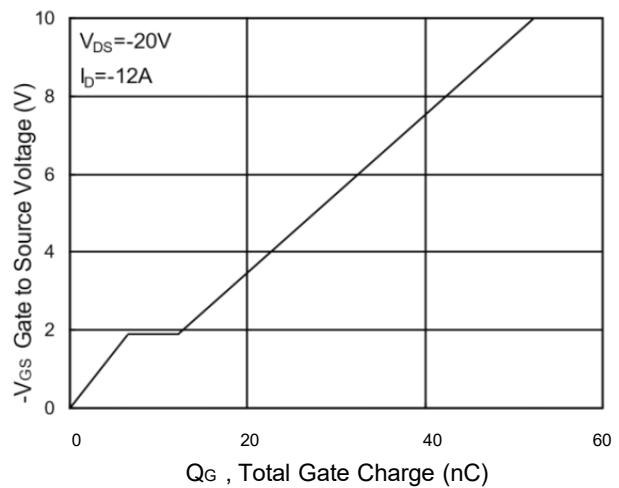


Fig.4 Gate-Charge Characteristics

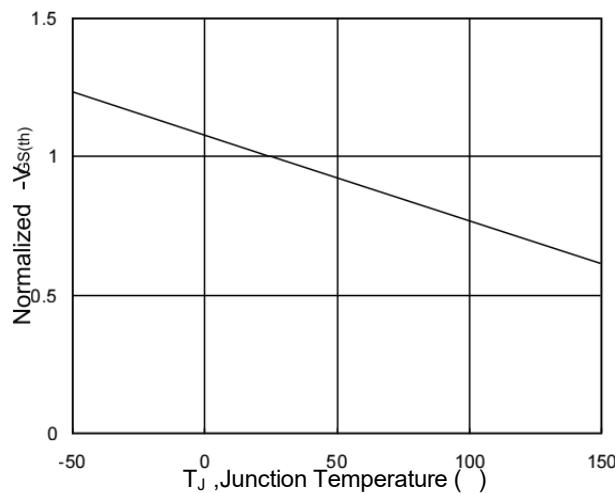


Fig.5 Normalized  $V_{GS(th)}$  v.s  $T_J$

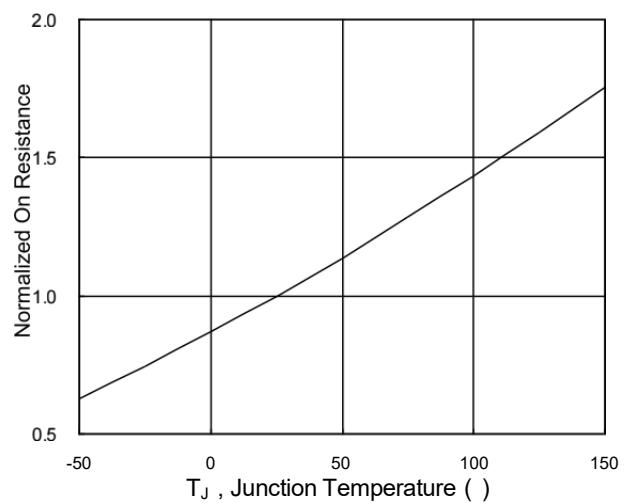


Fig.6 Normalized  $R_{DS(on)}$  v.s  $T_J$

## Typical Characteristics

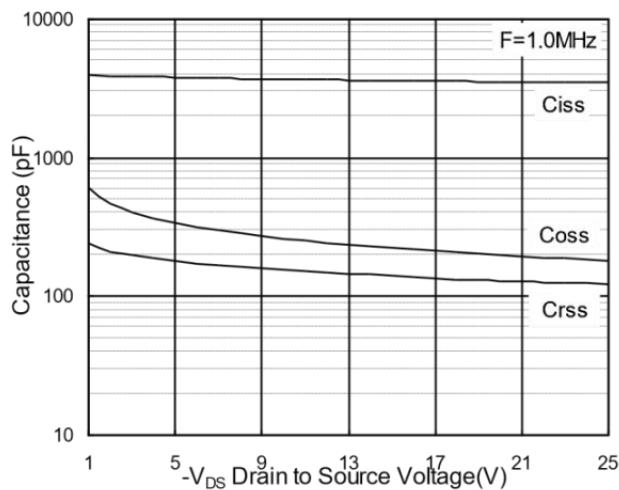


Fig.7 Capacitance

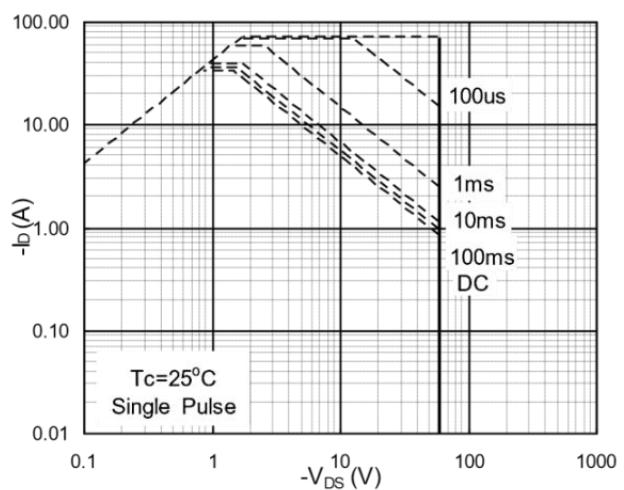


Fig.8 Safe Operating Area

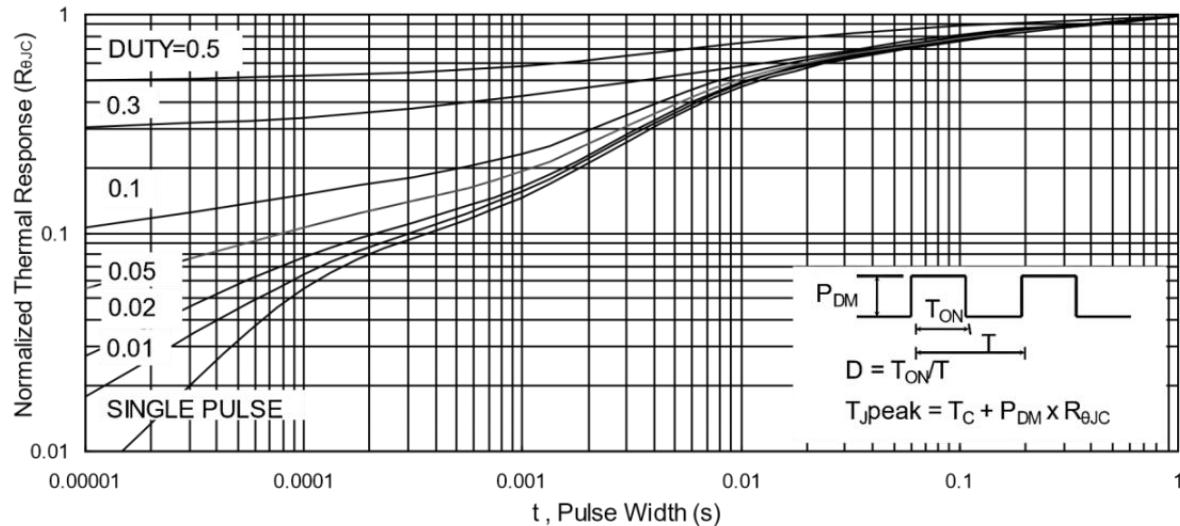


Fig.9 Normalized Maximum Transient Thermal Impedance

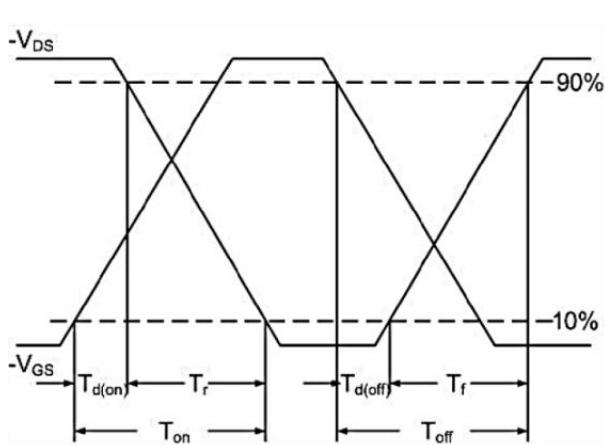


Fig.10 Switching Time Waveform

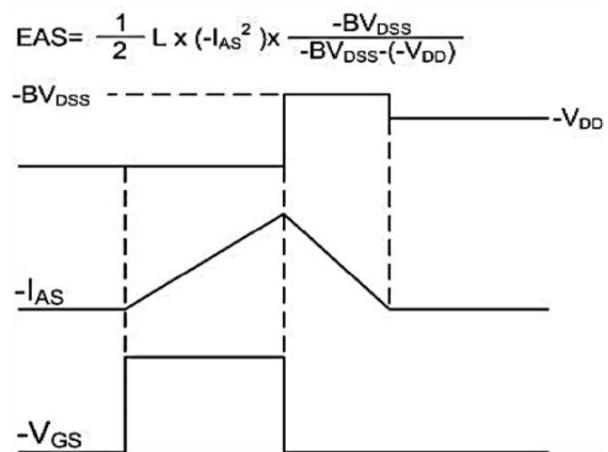
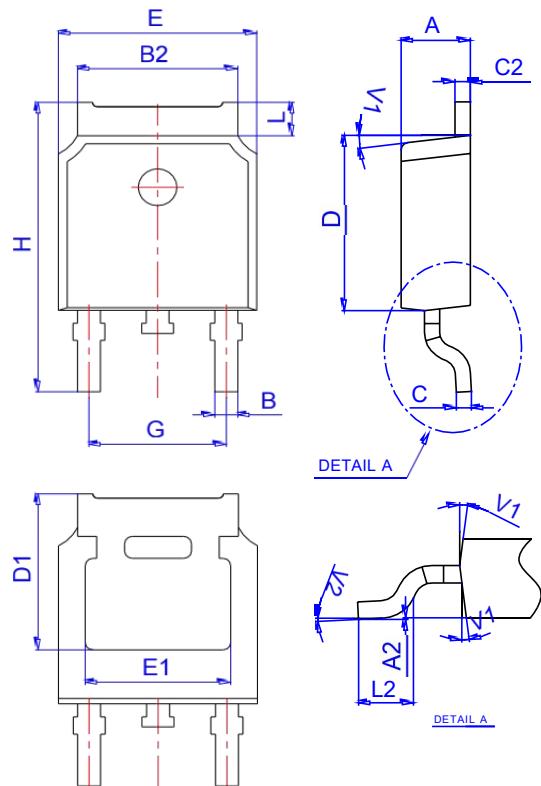


Fig.11 Unclamped Inductive Waveform

## Package Mechanical Data:TO-252-3L



Ref.	Dimensions					
	Millimeters			Inches		
	Min.	Typ.	Max.	Min.	Typ.	Max.
A	2.10		2.50	0.083		0.098
A2	0		0.10	0		0.004
B	0.66		0.86	0.026		0.034
B2	5.18		5.48	0.202		0.216
C	0.40		0.60	0.016		0.024
C2	0.44		0.58	0.017		0.023
D	5.90		6.30	0.232		0.248
D1	5.30REF			0.209REF		
E	6.40		6.80	0.252		0.268
E1	4.63			0.182		
G	4.47		4.67	0.176		0.184
H	9.50		10.70	0.374		0.421
L	1.09		1.21	0.043		0.048
L2	1.35		1.65	0.053		0.065
V1		7°			7°	
V2	0°		6°	0°		6°

### Package Marking and Ordering Information

Product ID	Pack	Marking	Qty(PCS)
TAPING	TO-252-3L		2500