

BCBF65N25W1

N-Channel Silicon Carbide Power MOSFET

650 V, 83.3 A, 25 mΩ



bestirpower

Features

- High switching speed with a low gate charge
- Fast intrinsic diode with low reverse recovery
- Robust Avalanche Capability
- 100% Avalanche Tested
- Halogen Free, and RoHS Compliant

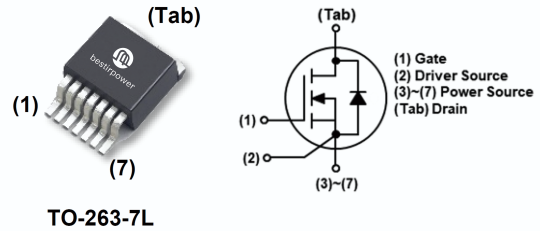
$BV_{DSS}, T_c=25^{\circ}\text{C}$	$I_D, T_c=25^{\circ}\text{C}$	$R_{DS(on),typ}$	$Q_{g,typ}$
650 V	83.3 A	25 mΩ	124 nC

Benefits

- System efficiency improvement
- Higher frequency applicability
- Increased power density
- Reduced cooling effort

Applications

- Solar inverter / ESS / UPS
- EV charging station
- Server & Telecom power
- Industrial power supply



Absolute Maximum Ratings ($T_J = 25^{\circ}\text{C}$ unless otherwise noted)

Symbol	Parameter		Value	Unit
V_{DSS}	Drain to Source Voltage		650	V
V_{GS}	Gate to Source Voltage (DC)		-13 / +22	V
V_{GSop}	Recommended Operation Value		-5 / +18	V
I_D	Drain Current	$V_{GS} = 18 \text{ V}, (T_C = 25^{\circ}\text{C})$	83.3	A
		$V_{GS} = 18 \text{ V}, (T_C = 100^{\circ}\text{C})$	58.9	
I_{DM}	Drain Current	Pulsed (Note1)	350	A
P_D	Power Dissipation	$(T_C = 25^{\circ}\text{C})$	375	W
		Derate Above 25°C	2.5	W/ $^{\circ}\text{C}$
T_J, T_{STG}	Operating and Storage Temperature Range		-55 to 175	$^{\circ}\text{C}$

※Note 1 : Limited by maximum junction temperature.

Thermal Characteristics

Symbol	Parameter	Value	Unit
$R_{\theta JC}$	Thermal Resistance, Junction to Case, Max.	0.40	$^{\circ}\text{C}/\text{W}$
$R_{\theta JA}$	Thermal Resistance, Junction to Ambient, Max.	27.55	
T_{sold}	Soldering temperature, wave soldering only allowed at leads	260	$^{\circ}\text{C}$

Electrical Characteristics ($T_J = 25^\circ\text{C}$ unless otherwise noted)

Symbol	Parameter	Test Conditions	Min	Typ	Max	Unit
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Off Characteristics

BV_{DSS}	Drain to Source Breakdown Voltage	$V_{GS} = 0\text{ V}, I_D = 100\text{ }\mu\text{A}$	650	-	-	V
I_{DSS}	Zero Gate Voltage Drain Current	$V_{DS} = 650\text{ V}, V_{GS} = 0\text{ V}$	-	-	100	μA
		$V_{DS} = 650\text{ V}, V_{GS} = 0\text{ V}, T_J = 175^\circ\text{C}$	-	-	3	mA
I_{GSS}	Gate-Source Leakage Current	$V_{GS} = +20\text{ V}, V_{DS} = 0\text{ V}$	-	-	+200	nA
		$V_{GS} = -10\text{ V}, V_{DS} = 0\text{ V}$	-	-	-200	

On Characteristics

$V_{GS(th)}$	Gate Threshold Voltage	$V_{GS} = V_{DS}, I_D = 10\text{ mA}$	1.6	2.6	3.6	V
$R_{DS(on)}$	Static Drain to Source On Resistance	$V_{GS} = 18\text{ V}, I_D = 20\text{ A}$	-	25	30	mΩ
		$V_{GS} = 18\text{ V}, I_D = 20\text{ A}, T_J = 175^\circ\text{C}$	-	45	54	
g_{fs}	Transconductance	$V_{DS} = 20\text{ V}, I_D = 20\text{ A}$	-	13	-	S

Dynamic Characteristics

C_{iss}	Input Capacitance	$V_{DS} = 400\text{ V}, V_{GS} = 0\text{ V}, f = 1\text{ MHz}$	-	2900	-	pF
C_{oss}	Output Capacitance		-	185	-	
C_{rss}	Reverse Capacitance		-	10.1	-	
E_{oss}	Stored Energy in Output Capacitance	$V_{DS} = 0\text{ V to } 400\text{ V}, V_{GS} = 0\text{ V}$	-	230	-	μJ
$C_{o(er)}$	Energy Related Output Capacitance		-	2875	-	pF
$C_{o(tr)}$	Time Related Output Capacitance		-	2870	-	
$Q_{g(tot)}$	Total Gate Charge	$V_{DS} = 400\text{ V}, I_D = 20\text{ A},$ $V_{GS} = -5\text{ V} / 18\text{ V},$ Inductive load	-	124	-	nC
Q_{gs}	Gate to Source Charge		-	30	-	
Q_{gd}	Gate to Drain "Miller" Charge		-	20	-	
R_G	Internal Gate Resistance	$f = 1\text{ MHz}, V_{AC} = 25\text{ mV}$ open drain	-	1.5	-	Ω

Switching Characteristics

$t_{d(on)}$	Turn-On Delay Time	$V_{DS} = 400\text{ V}, I_D = 20\text{ A},$ $V_{GS} = -5\text{ V} / 18\text{ V}, R_G = 5\text{ }\Omega,$ FWD : body diode at $V_{GS} = -5\text{ V},$ Inductive load	-	20	-	ns
t_r	Turn-On Rise Time		-	45	-	
$t_{d(off)}$	Turn-Off Delay Time		-	18	-	
t_f	Turn-Off Fall Time		-	10	-	
E_{on}	Turn-on Switching Energy		-	170	-	μJ
E_{off}	Turn-off Switching Energy		-	90	-	
E_{tot}	Total Switching Energy		-	260	-	

Source-Drain Diode Characteristics

I_S	Maximum Continuous Diode Forward Current	$V_{GS} = -5\text{ V}, I_{SD} = 15\text{ A}$	-	-	83.3	A
I_{SM}	Maximum Pulsed Diode Forward Current		-	-	350	
V_{SD}	Diode Forward Voltage	$V_{GS} = -5\text{ V}, I_{SD} = 15\text{ A}$	-	3.2	-	V
t_{rr}	Reverse Recovery Time	$V_{DD} = 400\text{ V}, I_{SD} = 15\text{ A},$ $di_f/dt = 1200\text{ A}/\mu\text{s}$, Includes Q_{OSS}	-	32	-	ns
Q_{rr}	Reverse Recovery Charge		-	123	-	nC
I_{rrm}	Peak Reverse Recovery Current		-	10	-	A

Typical Performance Characteristics

Figure 1. On-Region Characteristics $T_J = -55^\circ\text{C}$

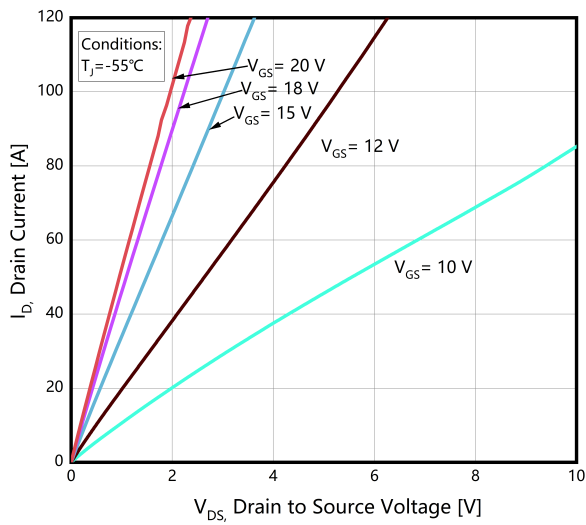


Figure 2. On-Region Characteristics $T_J = 25^\circ\text{C}$

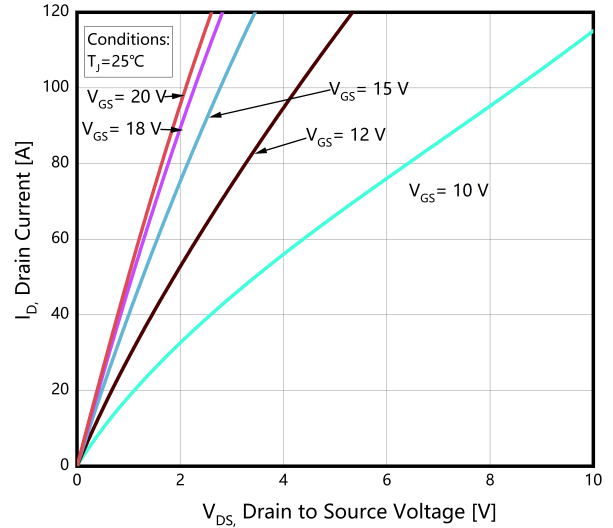


Figure 3. On-Region Characteristics $T_J = 175^\circ\text{C}$

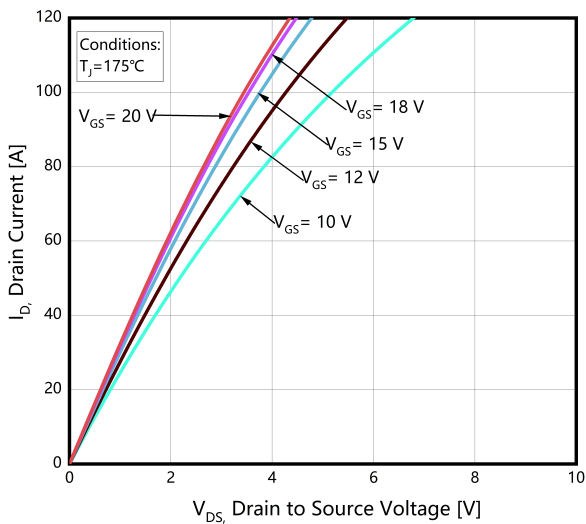


Figure 4. On-Resistance Characteristics vs. Temperature

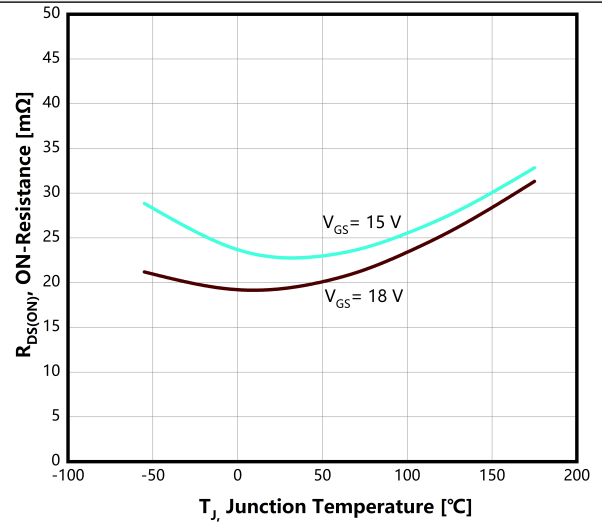


Figure 5. Transfer Characteristics

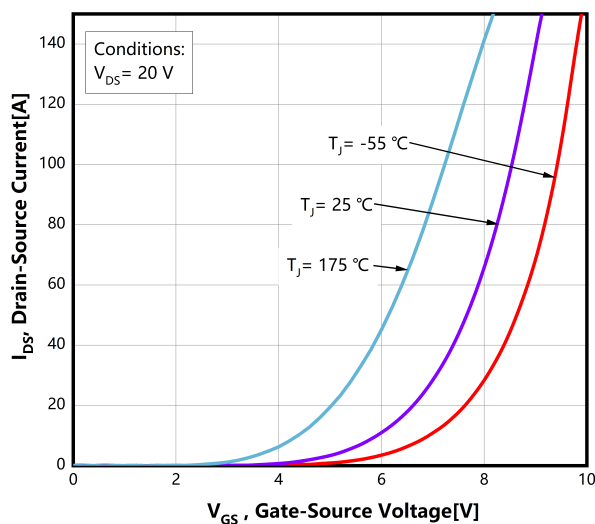
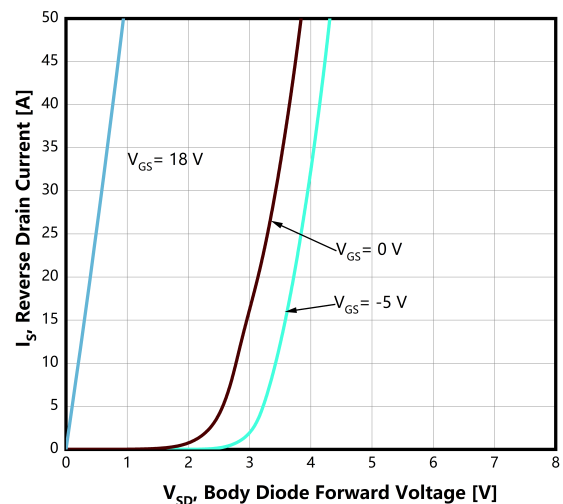


Figure 6. Diode Forward Voltage Characteristics vs. Source-Drain Current $T_J = -55^\circ\text{C}$



Typical Performance Characteristics

Figure 7. Diode Forward Voltage Characteristics vs. Source-Drain Current $T_J = 25^\circ\text{C}$

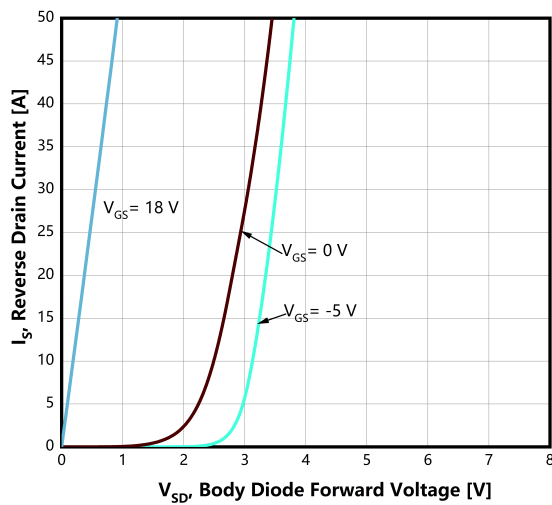


Figure 8. Diode Forward Voltage Characteristics vs. Source-Drain Current $T_J = 175^\circ\text{C}$

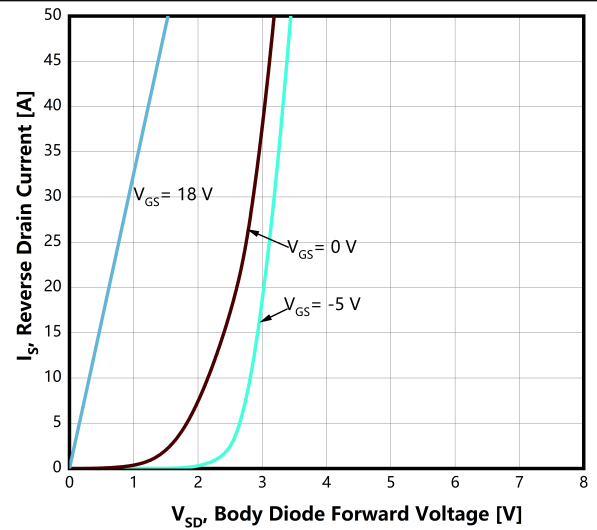


Figure 9. Threshold Voltage vs. Temperature

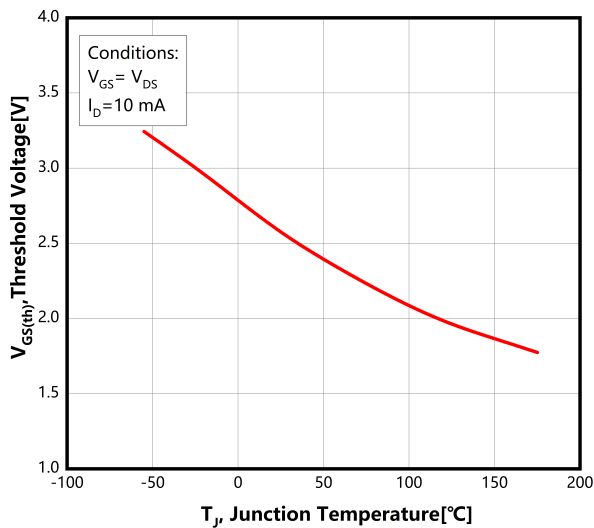


Figure 10. Gate Charge Characteristics

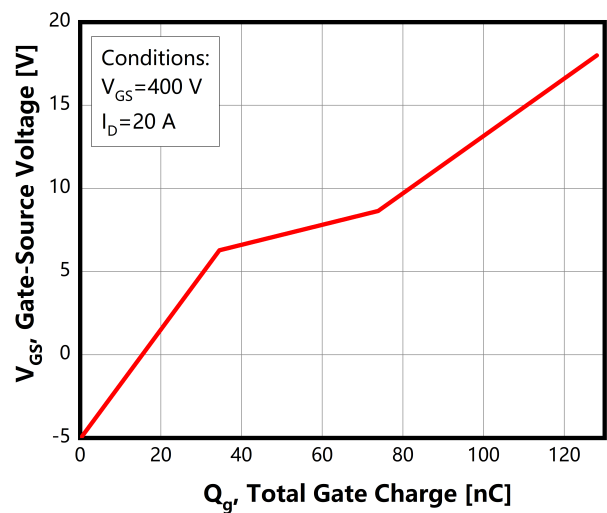


Figure 11. Stored Energy in Output Capacitance

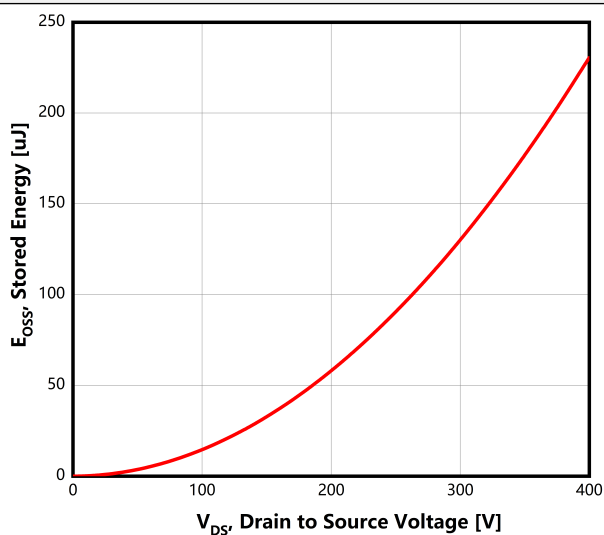
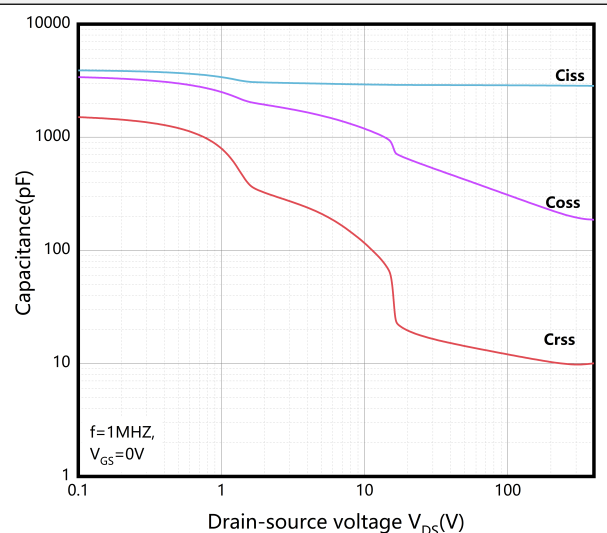


Figure 12. Capacitance Characteristics



Typical Performance Characteristics

Figure 13. Maximum Power Dissipation Derating vs. Case Temperature

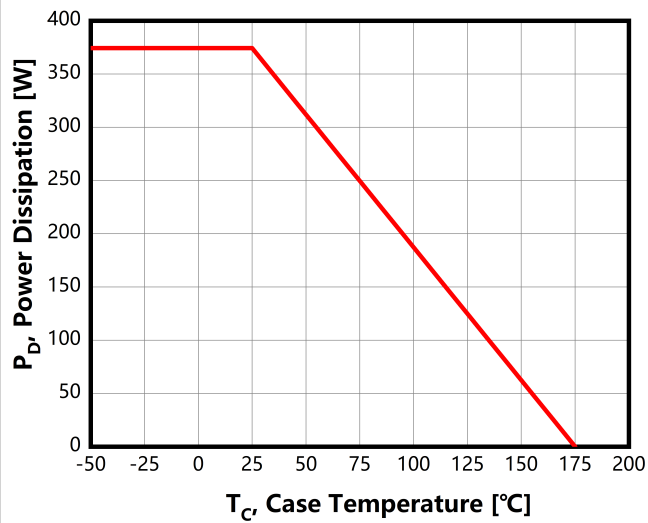


Figure 14. Maximum Safe Operating Area

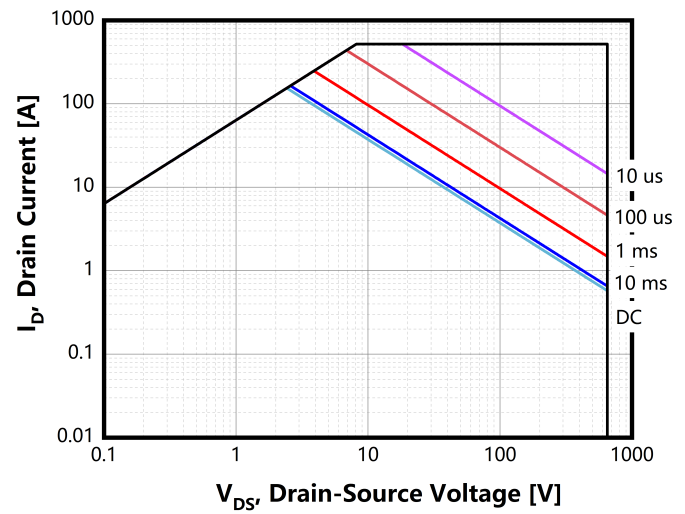
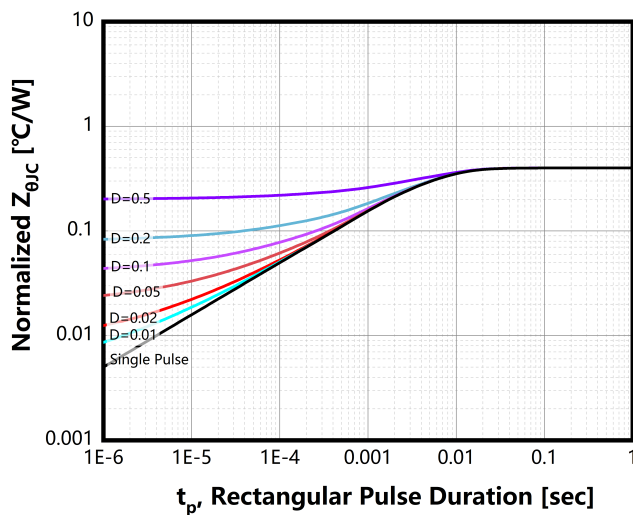


Figure 15. Transient Thermal Response Curve



Typical Performance Characteristics

Figure 16. Inductive Load Switching Test Circuit and Waveforms

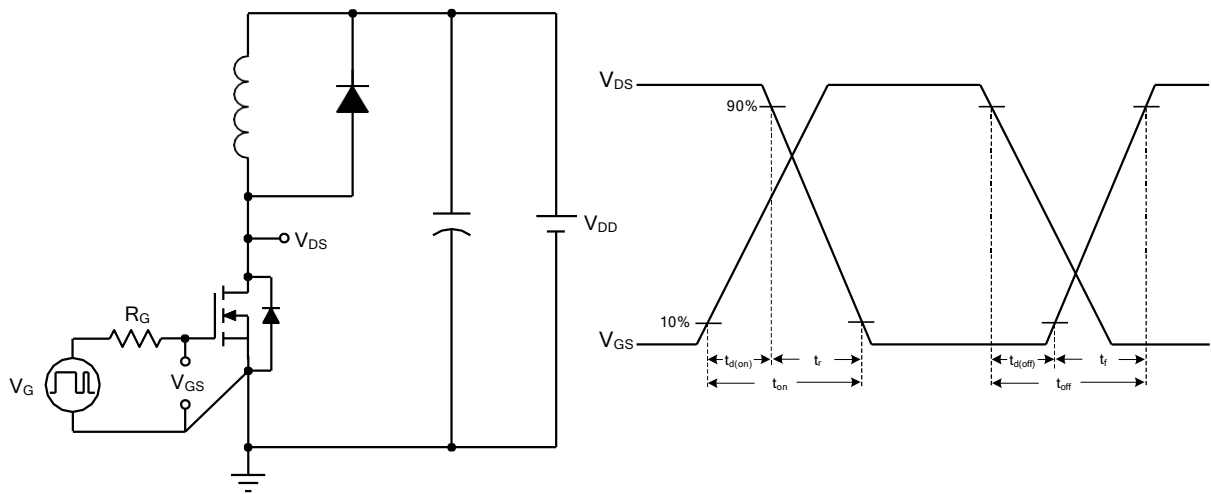
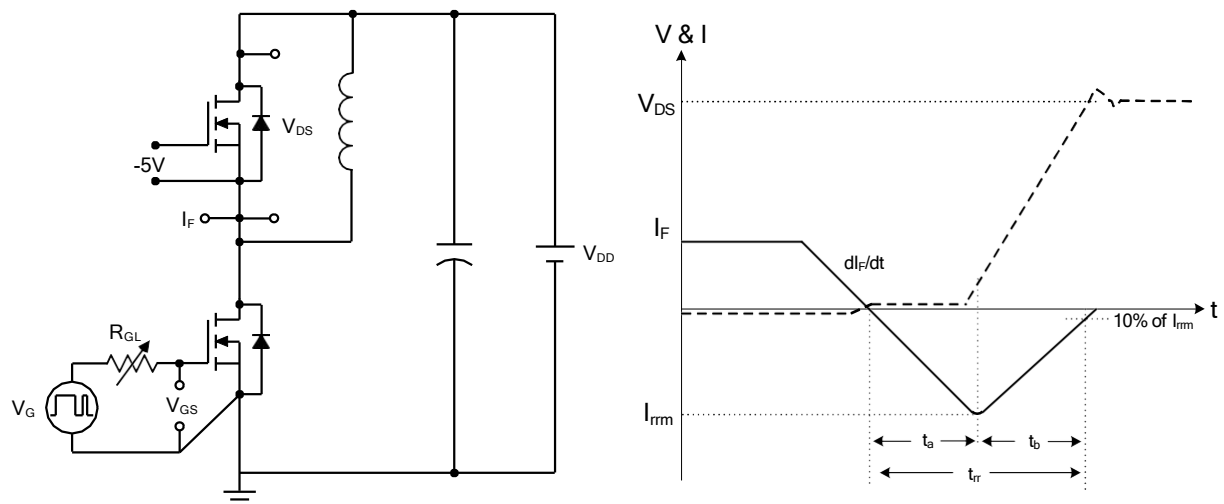
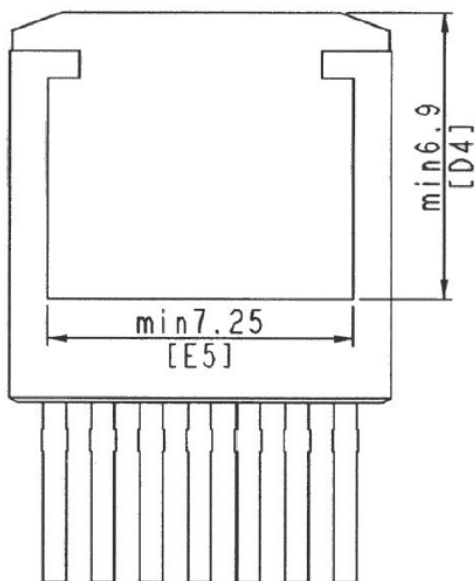
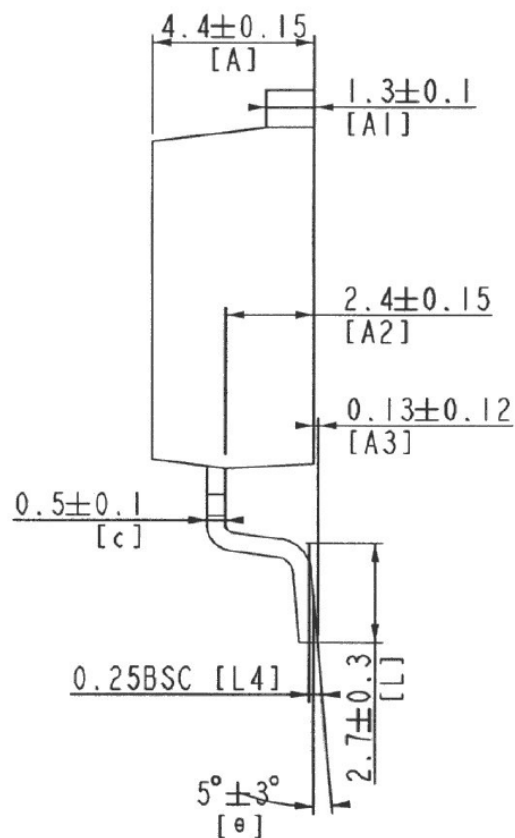
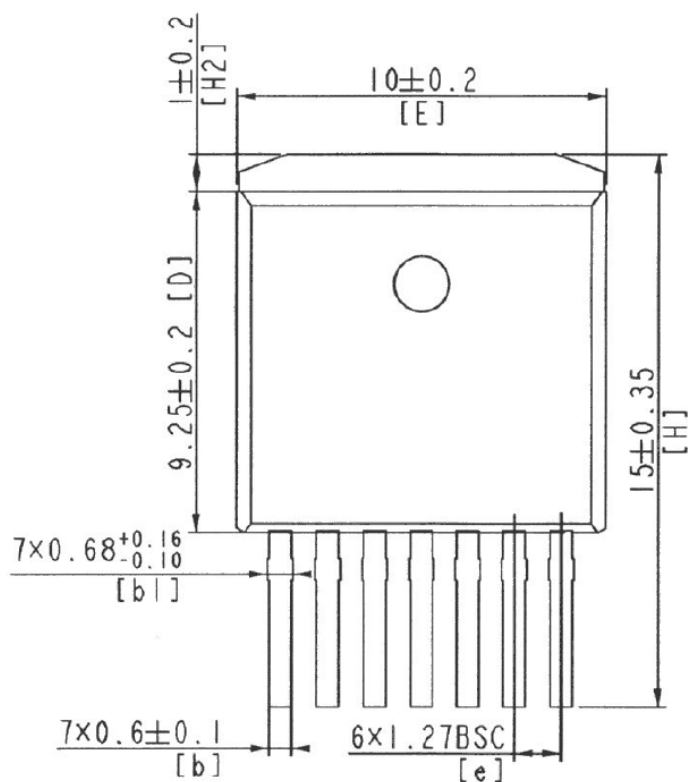


Figure 17. Peak Diode Recovery dv/dt Test Circuit and Waveforms



Package Outlines

TO263-7L



* Dimensions in millimeters

Package Marking and Ordering Information

Part Number	Top Marking	Package	Packing Method	Quantity
BCBF65N25W1	BCBF65N25W1	TO-263-7L	Tape & Reel	800 units

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