

#### **Description**

The FDD8451 uses advanced trench technology to provide excellent  $R_{\rm DS(ON)}$ , low gate charge and operation with gate voltages as low as 4.5V. This device is suitable for use as a

Battery protection or in other Switching application.



TO-252-2L

#### **General Features**

 $V_{DS} = 40V I_{D} = 30A$ 

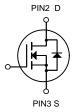
 $R_{DS(ON)}$  < 22m $\Omega$  @  $V_{GS}$ =10V

#### **Application**

Battery protection

Load switch

Uninterruptible power supply



N-Channel MOSFET

### **Package Marking and Ordering Information**

Product ID	Pack	Brand	Qty(PCS)
FDD8451	TO-252-2L	HXY MOSFET	2500

#### Absolute Maximum Ratings (T<sub>c</sub>=25<sup>°</sup>Cunless otherwise noted)

Symbol	Parameter	Rating	Units
Vos	Drain-Source Voltage	40	V
Vgs	Gate-Source Voltage	±20	V
I <sub>D</sub> @T <sub>C</sub> =25°C	Continuous Drain Current, V <sub>GS</sub> @ 10V <sup>1</sup>	30	А
I <sub>D</sub> @T <sub>C</sub> =100°C	Continuous Drain Current, V <sub>GS</sub> @ 10V <sup>1</sup>	15	А
Ідм	Pulsed Drain Current <sup>2</sup>	40	А
EAS	Single Pulse Avalanche Energy³	17.1	mJ
las	Avalanche Current	10	А
P <sub>D</sub> @T <sub>C</sub> =25°C	Total Power Dissipation <sup>4</sup>	4	W
Тѕтс	Storage Temperature Range	-55 to 150	°C
TJ	Operating Junction Temperature Range	-55 to 150	°C
$R_{ heta}$ JA	Thermal Resistance Junction-ambient (Steady State) <sup>1</sup>	62	°C/W
R₀Jc	Thermal Resistance Junction-Case <sup>1</sup>	2.8	°C/W



## N-Channel Electrical Characteristics (T<sub>J</sub>=25 °C unless otherwise specified)

Symbol	Parameter	Test Condition	Min.	Тур.	Max.	Units
V <sub>(BR)DSS</sub>	Drain-Source Breakdown Voltage	V <sub>GS</sub> =0V, I <sub>D</sub> =250μA	40	-	-	V
I <sub>DSS</sub>	Zero Gate Voltage Drain Current	V <sub>DS</sub> =40V, V <sub>GS</sub> =0V	-	-	1.0	μA
I <sub>GSS</sub>	Gate to Body Leakage Current	V <sub>DS</sub> =0V, V <sub>GS</sub> =±20V	-	-	±100	nA
$V_{\text{GS(th)}}$	Gate Threshold Voltage	$V_{DS}=V_{GS}$ , $I_{D}=250\mu A$	1.0	1.5	2.5	V
D	Static Drain-Source on-Resistance	V <sub>GS</sub> =10V, I <sub>D</sub> =8A	-	18	22	mΩ
$R_{DS(on)}$	note3	V <sub>GS</sub> =4.5V, I <sub>D</sub> =5A	-	25	35	mΩ
C <sub>iss</sub>	Input Capacitance	V <sub>DS</sub> =20V, V <sub>GS</sub> =0V,	-	633	-	pF
Coss	Output Capacitance	f=1.0MHz	-	67	-	pF
C <sub>rss</sub>	Reverse Transfer Capacitance	- 1-1.0WILIZ	-	58	-	pF
Qg	Total Gate Charge	V <sub>DS</sub> =20V, I <sub>D</sub> =8A, V <sub>GS</sub> =10V	-	12	-	nC
Q <sub>gs</sub>	Gate-Source Charge		-	3.2	-	nC
$Q_gd$	Gate-Drain("Miller") Charge	- VGS-10V	-	3.1	-	nC
t <sub>d(on)</sub>	Turn-on Delay Time		-	4	-	ns
t <sub>r</sub>	Turn-on Rise Time	$V_{DD}$ = 20V, $R_L$ =2.5Ω $V_{GS}$ =10V, $R_{REN}$ =3Ω	-	3	-	ns
t <sub>d(off)</sub>	Turn-off Delay Time		-	15	-	ns
t <sub>f</sub>	Turn-off Fall Time		-	2	-	ns
Is	Maximum Continuous Drain to Source	e Diode Forward	_	_	30	Α
15	Current			_		
I <sub>SM</sub>	Maximum Pulsed Drain to Source Did	de Forward Current	-	-	40	Α
$V_{SD}$	Drain to Source Diode Forward	rain to Source Diode Forward V <sub>GS</sub> =0V, I <sub>S</sub> = 8A		_	1.2	V
<b>V</b> 3D	Voltage	V GG O V , 13 O/ V	_	_	1.2	V

Notes:1. Repetitive Rating: Pulse Width Limited by Maximum Junction Temperature

2. EAS condition :  $T_J$ =25 °C, $V_{DD}$ =20V, $V_G$ =10V,L=0.5mH,Rg=25 $\Omega$ , $I_{AS}$ =7.2A  $T_J$ =25 °C, $V_{DD}$ =-20V, $V_G$ = -10V,L=0.5mH,Rg=25 $\Omega$ , $I_{AS}$ =-8.4A

3. Pulse Test: Pulse Width≤300µs, Duty Cycle≤2%



# **Typical Performance Characteristics**

Figure1: Output Characteristics

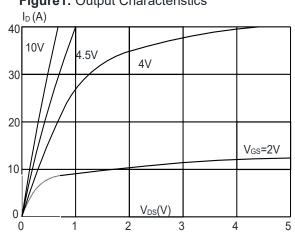


Figure 2: Typical Transfer Characteristics

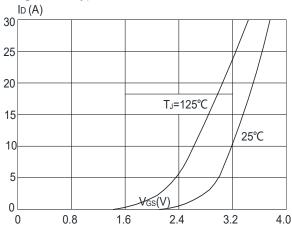


Figure 3:On-resistance vs. Drain Current

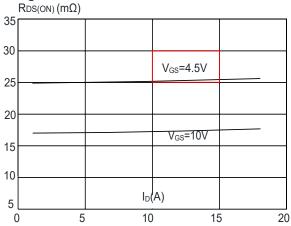


Figure 4: Body Diode Characteristics

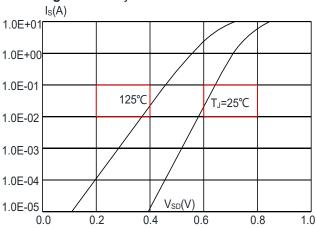


Figure 5: Gate Charge Characteristics

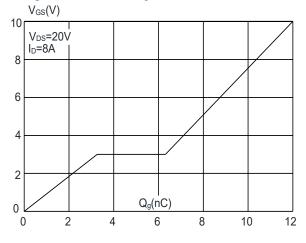
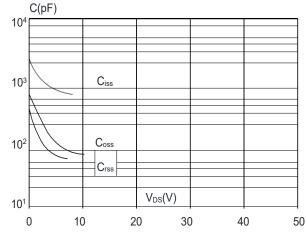
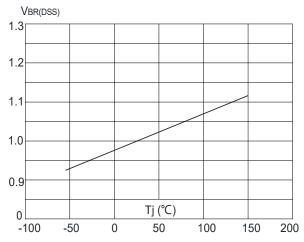


Figure 6: Capacitance Characteristics





**Figure 7:** Normalized Breakdown Voltage vs. Junction Temperature



**Figure 8:** Normalized on Resistance vs. Junction Temperature

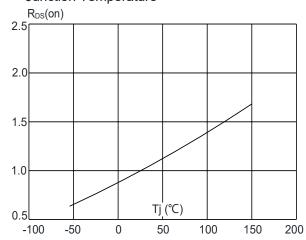
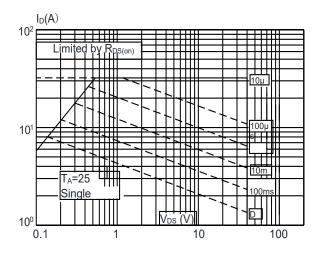


Figure 9: Maximum Safe Operating Area



**Figure 10:** Maximum Continuous Drain Current vs. Ambient Temperature

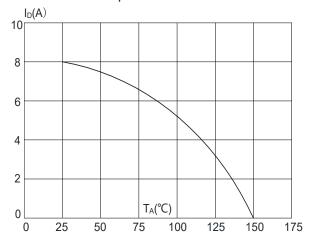
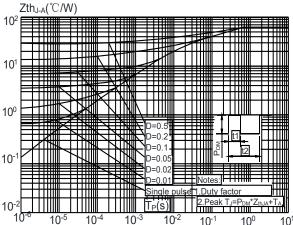


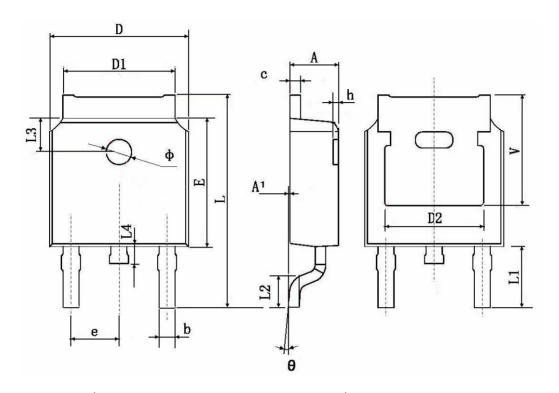
Figure.11: Maximum Effective

Transient Thermal Impedance, Junction-to-Ambient

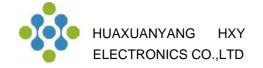




## **TO-252-2L Package Infommation**



Symbol	Dimensions In Millimeters		Dimensions In Inches		
	Min.	Max.	Min.	Max.	
А	2.200	2.400	0.087	0.094	
A1	0.000	0.127	0.000	0.005	
b	0.660	0.860	0.026	0.034	
С	0.460	0.580	0.018	0.023	
D	6.500	6.700	0.256	0.264	
D1	5.100	5.460	0.201	0.215	
D2	0.483	0.483 TYP.		0.190 TYP.	
E	6.000	6.200	0.236	0.244	
е	2.186	2.386	0.086	0.094	
L	9.800	10.400	0.386	0.409	
L1	2.900 TYP.		0.114 TYP.		
L2	1.400	1.700	0.055	0.067	
L3	1.600	TYP.	0.063 TYP.		
L4	0.600	1.000	0.024	0.039	
Ф	1.100	1.300	0.043	0.051	
θ	0°	8°	0°	8°	
h	0.000	0.300	0.000	0.012	
V	5.350 TYP.		0.211 TYP.		



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