

Reverse Voltage - 20 to 200V

Forward Current - 5.0A

FEATURES

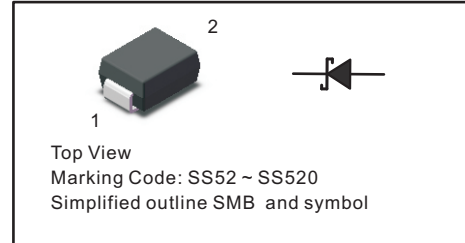
- Metal silicon junction, majority carrier conduction
- For surface mounted applications
- Low power loss, high efficiency
- High forward surge current capability
- For use in low voltage, high frequency inverters, free wheeling, and polarity protection applications

MECHANICAL DATA

- Case: SMB
- Terminals: Solderable per MIL-STD-750, Method 2026
- Approx. Weight: 0.1g / 0.0034oz

PINNING

PIN	DESCRIPTION
1	Cathode
2	Anode



Absolute Maximum Ratings and Electrical characteristics

Ratings at 25 °C ambient temperature unless otherwise specified. Single phase, half wave, 60Hz resistive or inductive load, for capacitive load, derate by 20 %

Parameter	Symbols	SS52B	SS54B	SS56B	SS58B	SS510B	SS512B	SS515B	SS520B	Units
Maximum Repetitive Peak Reverse Voltage	V _{RRM}	20	40	60	80	100	120	150	200	V
Maximum RMS voltage	V _{RMS}	14	28	42	56	70	84	105	140	V
Maximum DC Blocking Voltage	V _{DC}	20	40	60	80	100	120	150	200	V
Maximum Average Forward Rectified Current	I _{F(AV)}	5.0								A
Peak Forward Surge Current,8.3ms Single Half Sine-wave Superimposed on Rated Load (JEDEC method)	I _{FSM}	150								A
Max Instantaneous Forward Voltage at 5 A	V _F	0.55		0.70		0.85				V
Maximum DC Reverse Current T _a = 25°C at Rated DC Reverse Voltage T _a =100°C	I _R	1.0 50		0.3 25					mA	
Typical Junction Capacitance ⁽¹⁾	C _j	500		300					pF	
Typical Thermal Resistance ⁽²⁾	R _{θJA}	50								°C/W
Operating Junction Temperature Range	T _j	-55 ~ +150								°C
Storage Temperature Range	T _{stg}	-55 ~ +150								°C

(1) Measured at 1MHz and applied reverse voltage of 4 V D.C.

(2) P.C.B. mounted with 2.0" X 2.0" (5 X 5 cm) copper pad areas.

Fig.1 Forward Current Derating Curve

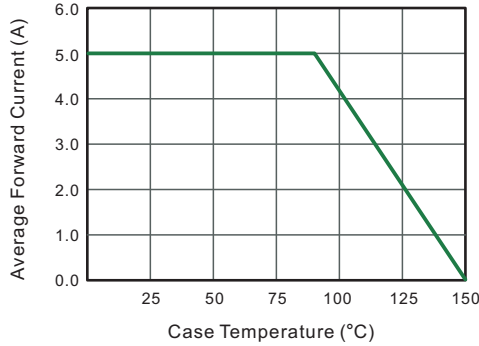


Fig.2 Typical Reverse Characteristics

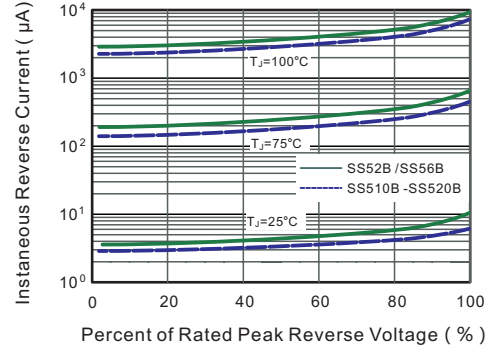


Fig.3 Typical Forward Characteristic

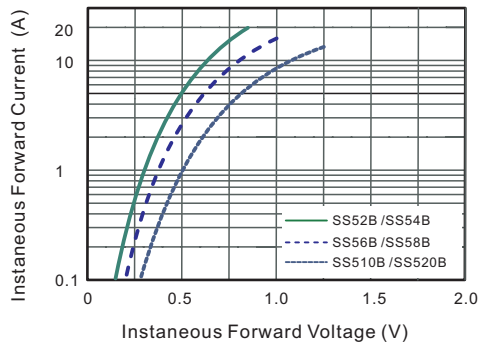


Fig.4 Typical Junction Capacitance

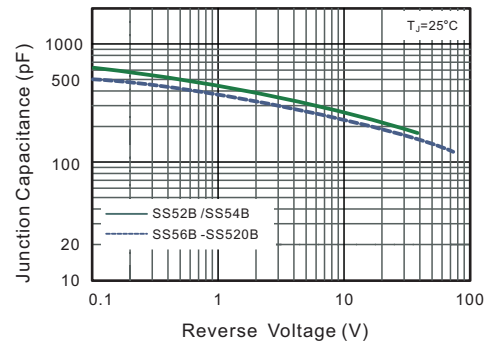


Fig.5 Maximum Non-Repetitive Peak Forward Surge Current

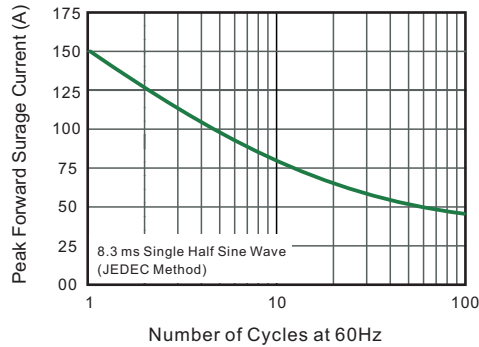
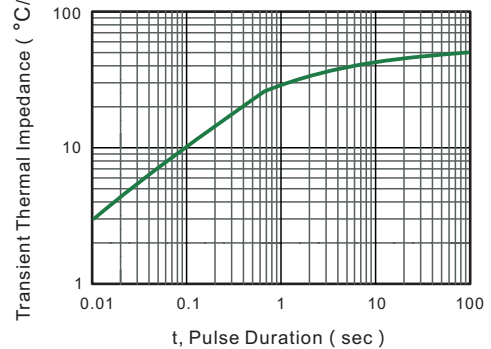
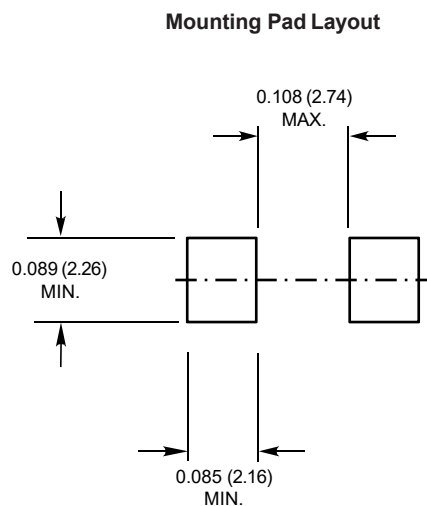
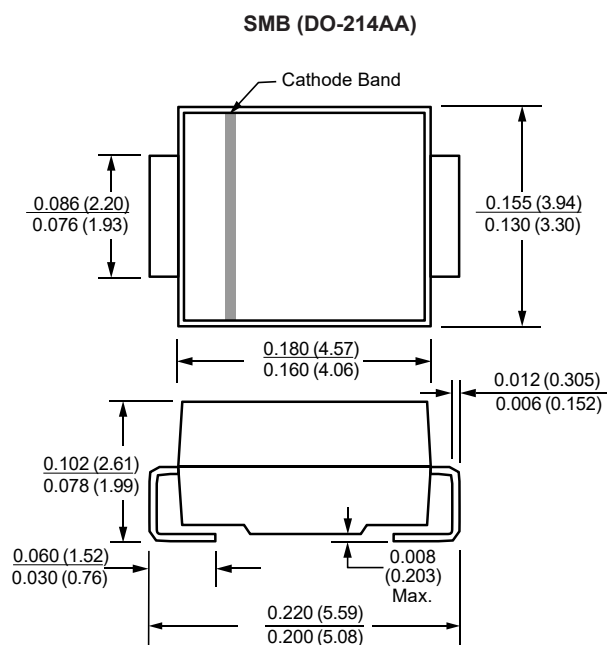


Fig.6- Typical Transient Thermal Impedance



PACKAGE OUTLINE

Plastic surface mounted package; 2 leads



Ordering Information (Example)

PREFERED P/N	MINIMUM PACKAGE(pcs)	INNER BOX QUANTITY(pcs)	OUTER CARTON QUANTITY(pcs)	DELIVERY MODE
SS52B THRU SS520B	3000	6000	48000	13" reel

Marking

Type number	Marking code
SS52B	SS52
SS54B	SS54
SS56B	SS56
SS58B	SS58
SS510B	SS510
SS512B	SS512
SS515B	SS515
SS520B	SS520