

40V N-Channel Enhancement Mode MOSFET

Description

The SX10N04SI uses advanced trench technology to provide excellent RDS(ON), low gate charge and operation with gate voltages as low as 4.5V. This device is suitable for use as a Battery protection or in other Switching application.

General Features

V_{DS} =40V I_D =10A

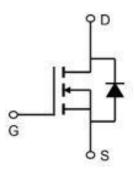
 $R_{DS(ON)}$ < 22m Ω @ Vgs=10V

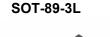
Application

Automative lighting

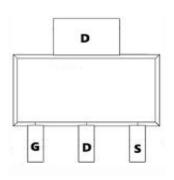
Load switch

Uninterruptible power supply









Absolute Maximum Ratings (TC=25℃unless otherwise noted)

| Symbol | Parameter | Rating | Units |
|--------------------|--|------------|------------|
| VDS | Drain-Source Voltage | 40 | V |
| VGS | Gate-Source Voltage | ±20 | V |
| l o@Ta=25°C | Continuous Drain Current ¹ | 10 | А |
| l o@Ta=70°C | Continuous Drain Current ¹ | 6.7 | А |
| IDM | Pulsed Drain Current ² | 50 | А |
| EAS | Single Pulse Avalanche Energy ³ | 31 | mJ |
| IAS | Avalanche Current | 25 | А |
| Pb@Ta=25°C | Total Power Dissipation ⁴ | 1.9 | W |
| TSTG | Storage Temperature Range | -55 to 150 | $^{\circ}$ |
| TJ | Operating Junction Temperature Range | -55 to 150 | $^{\circ}$ |
| ReJA | Thermal Resistance Junction-ambient ¹ | 125 | °C/W |
| Rejc | Thermal Resistance Junction-Case ¹ | 15 | °C/W |



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Electrical Characteristics@T_j=25°C(unless otherwise specified)

| Symbol | Parameter | Conditions | Min. | Тур. | Max. | Unit | |
|----------------------|--|--|------|-------|------|--------|--|
| BVpss | Drain-Source Breakdown Voltage | Vgs=0V , Ib=250uA | 40 | 44 | | ٧ | |
| △BVɒss/△Tɹ | BVDSS Temperature Coefficient | Reference to 25℃, I _D =1mA | | 0.032 | | V/°C | |
| Rds(on) | Static Drain-Source On-Resistance ² | Vgs=10V , Ip=7A | | 18 | 22 | mΩ | |
| TOS(ON) | Static Dialif-Source Off-Nesistance | Vgs=4.5V , ID=6A | | 23 | 30 | 11122 | |
| VGS(th) | Gate Threshold Voltage | \/\/ | 1.0 | 1.4 | 2.5 | V | |
| $\triangle V$ GS(th) | V _{GS(th)} Temperature Coefficient | Vgs=Vps , Ip =250uA | | -4.8 | | mV/℃ | |
| | Dunin Course Lookson Cumont | V _{DS} =32V , V _{GS} =0V , T _J =25°C | | | 1 | | |
| loss | Drain-Source Leakage Current | V _{DS} =32V , V _{GS} =0V , T _J =55°C | | | 5 | ⊢ uA │ | |
| lgss | Gate-Source Leakage Current | Vgs=±20V , Vps=0V | | | ±100 | nA | |
| gfs | Forward Transconductance | VDS=5V , ID=7A | | 32 | | S | |
| Rg | Gate Resistance | V _{DS} =0V , V _{GS} =0V , f=1MHz | | 2.1 | | Ω | |
| Qg | Total Gate Charge (4.5V) | | | 9.8 | | | |
| Qgs | Gate-Source Charge | Vds=32V , Vgs=4.5V , Id=7A | | 2.8 | | nC | |
| Qgd | Gate-Drain Charge | | | 3.9 | | | |
| T _{d(on)} | Turn-On Delay Time | | | 2.8 | | | |
| Tr | Rise Time | V _{DD} =20V , V _{GS} =10V , R _G =3.3Ω | | 40.4 | | ns | |
| T _d (off) | Turn-Off Delay Time | lb=7A | | 22.8 | | | |
| Tf | Fall Time | | | 6.4 | | | |
| Ciss | Input Capacitance | | | 1013 | | | |
| Coss | Output Capacitance | V _{DS} =15V , V _{GS} =0V , f=1MHz | | 107 | | pF | |
| Crss | Reverse Transfer Capacitance | | | 76 | | | |
| ls | Continuous Source Current ^{1,5} | V _G =V _D =0V , Force Current | | | 8.4 | Α | |
| Іѕм | Pulsed Source Current ^{2,5} | V 0V 1 40 T 05°C | | | 50 | Α | |
| VsD | Diode Forward Voltage ² | - Vgs=0V , Is=1A , Tյ=25℃ | | | 1 | V | |
| trr | Reverse Recovery Time | I _F =7A , dI/dt=100A/μs , | | 10 | | nS | |
| Qrr | Reverse Recovery Charge | | | 3.3 | | nC | |
| | | 1 | | 1 | | | |

Note

- 1 、The data tested by surface mounted on a 1 inch 2 FR-4 board with 2OZ copper.
- 2 $\,{}_{\sim}$ The data tested by pulsed , pulse width ${}^{\leq}$ 300us , duty cycle ${}^{\leq}$ 2%
- 3 . The power dissipation is limited by $150\,^\circ\!\mathrm{C}$ junction temperature
- 4 、The data is theoretically the same as I D and I DM , in real applications , should be limited by total power dissipation.

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Typical Characteristics

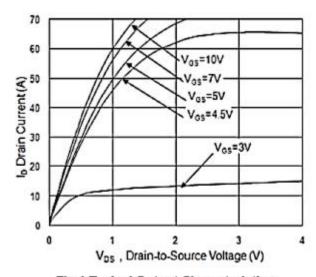


Fig.1 Typical Output Characteristics

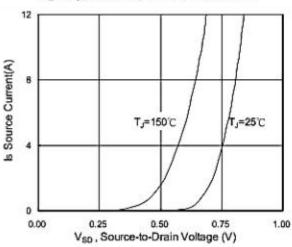


Fig.3 Forward Characteristics of Reverse

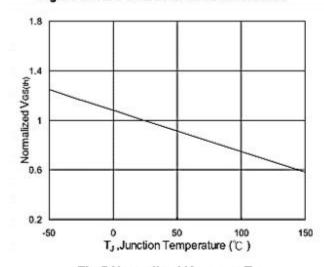


Fig.5 Normalized V_{GS(th)} vs. T_J

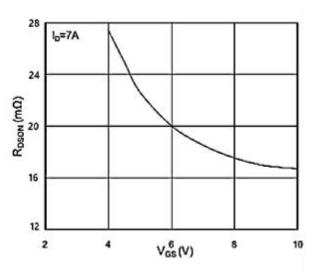


Fig.2 On-Resistance vs. G-S Voltage

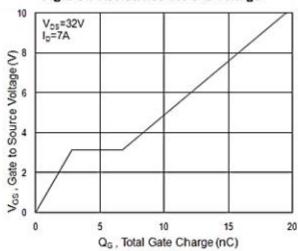


Fig.4 Gate-Charge Characteristics

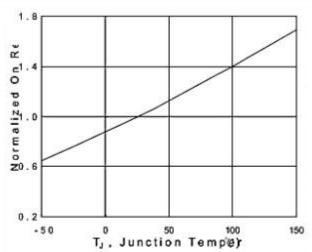
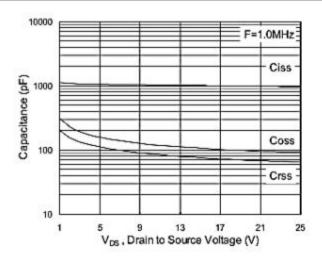


Fig.6 Normalized RDSON vs. TJ





Typical Characteristics



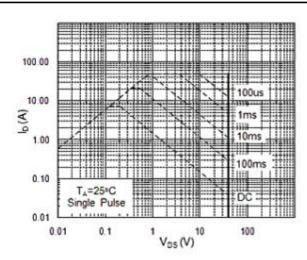


Fig.7 Capacitance

Fig.8 Safe Operating Area

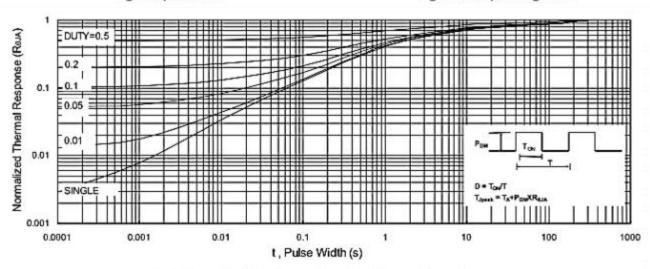


Fig.9 Normalized Maximum Transient Thermal Impedance

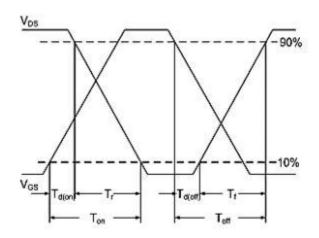


Fig.10 Switching Time Waveform

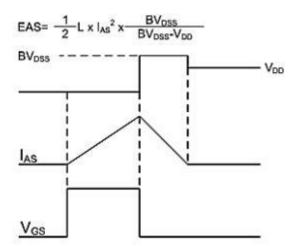
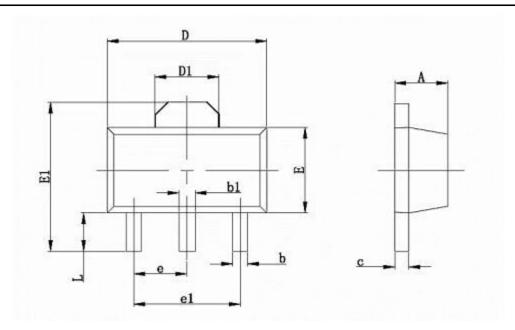


Fig.11 Unclamped Inductive Switching Waveform



Package Mechanical Data:SOT89-3L



| Cumbal | Dimensions | In Millimeters | Dimension | s In Inches |
|--------|------------|----------------|-----------|-------------|
| Symbol | Min | Max | Min | Max |
| Α | 1.400 | 1.600 | 0.055 | 0.063 |
| b | 0.350 | 0.520 | 0.013 | 0.197 |
| b1 | 0.400 | 0.580 | 0.016 | 0.023 |
| С | 0.350 | 0.440 | 0.014 | 0.017 |
| D | 4.400 | 4.600 | 0.173 | 0.181 |
| D1 | 1.550 REF | | 0.061 | REF |
| E | 2.350 | 2.550 | 0.091 | 0.102 |
| E1 | 3.940 | 4.250 | 0.155 | 0.167 |
| е | 1.500 TYP | | 0.06 | OTYP |
| e1 | 3.000 TYP | | 0.118TYP | |
| L | 0.900 | 1.100 | 0.035 | 0.047 |

Package Marking and Ordering Information

| _ | and the state of t | | | | | |
|---|--|----------|---------|----------|--|--|
| | Product ID | Pack | Marking | Qty(PCS) | | |
| | TAPING | SOT89-3L | | 3000 | | |

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