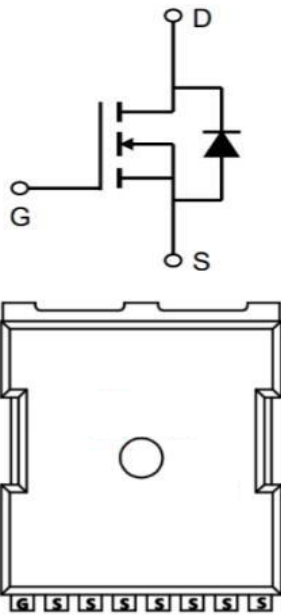


Description

The SX300N05TLG5 uses advanced technology to provide excellent $R_{DS(ON)}$, low gate charge and operation with gate voltages as low as 4.5V. This device is suitable for use as a Battery protection or in other Switching application.



General Features

$V_{DS} = 45V$ $I_D = 300A$

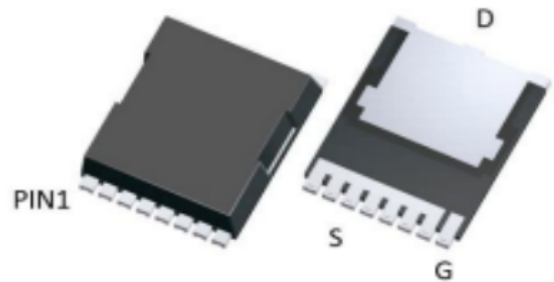
$R_{DS(ON)} < 1.3m\Omega$ @ $V_{GS}=10V$

Application

BMS

BLDC

UPS



Absolute Maximum Ratings ($T_c=25^\circ C$ unless otherwise noted)

Symbol	Parameter	Max.	Units
VDSS	Drain-Source Voltage	45	V
VGSS	Gate-Source Voltage	± 20	V
$I_{D@TC=25^\circ C}$	Continuous Drain Current, $V_{GS} @ 10V$	300	A
$I_{D@TC=100^\circ C}$	Continuous Drain Current, $V_{GS} @ 10V$	210	A
IDM	Pulsed Drain Current	900	A
EAS	Single Pulsed Avalanche Energy	510	mJ
IAS	Avalanche Current	70	A
$PD@TC=25^\circ C$	Power Dissipation	230	W
$R_{\theta JA}$	Thermal Resistance Junction-Ambient ¹	35	$^\circ C/W$
$R_{\theta JC}$	Thermal Resistance, Junction to Case	1.9	$^\circ C/W$
T_J	Operating Junction Temperature Range	-55 to 175	$^\circ C$
TSTG	Storage Temperature Range	-55 to 175	$^\circ C$

Electrical Characteristics (T_J=25 °C, unless otherwise noted)

Symbol	Parameter	Test Condition	Min.	Typ.	Max.	Units
V(BR)DSS	Drain-Source Breakdown Voltage	V _{GS} =0V, I _D =250μA	45	48	-	V
IDSS	Zero Gate Voltage Drain Current	V _{DS} =40V, V _{GS} =0V,	-	-	1.0	μA
IGSS	Gate to Body Leakage Current	V _{DS} =0V, V _{GS} = ±20V	-	-	±100	nA
VGS(th)	Gate Threshold Voltage	V _{DS} =V _{GS} , I _D =250μA	1.0	1.8	2.5	V
RDS(on)	Static Drain-Source on-Resistance	V _{GS} =10V, I _D =30A	-	1.0	1.3	mΩ
		V _{GS} =4.5V, I _D =20A	-	1.7	2.5	mΩ
Ciss	Input Capacitance	V _{DS} =20V, V _{GS} =0V, f=1.0MHz	-	8300	-	pF
Coss	Output Capacitance		-	1510	-	pF
Crss	Reverse Transfer Capacitance		-	130	-	pF
Qg	Total Gate Charge	V _{DS} =20V, I _D =85A, V _{GS} =10V	-	127	-	nC
Qgs	Gate-Source Charge		-	35	-	nC
Qgd	Gate-Drain("Miller") Charge		-	26	-	nC
td(on)	Turn-on Delay Time	V _{DD} =20V, I _D =85A, R _G =1.6Ω, V _{GS} =10V	-	22.5	-	ns
tr	Turn-on Rise Time		-	6.7	-	ns
td(off)	Turn-off Delay Time		-	80.3	-	ns
tr	Turn-off Fall Time		-	26.9	-	ns
IS	Maximum Continuous Drain to Source Diode Forward Current		-	-	300	A
ISM	Maximum Pulsed Drain to Source Diode Forward Current		-	-	750	A
VSD	Drain to Source Diode Forward Voltage	V _{GS} =0V, I _S =30A	-	-	1.2	V
trr	Body Diode Reverse Recovery Time	T _J =25°C, I _F =I _S , dI/dt=100A/μs	-	100	-	ns
Qrr	Body Diode Reverse Recovery Charge		-	163	-	nC

Note :

- 1、The data tested by surface mounted on a 1 inch 2 FR-4 board with 2OZ copper.
- 2、The data tested by pulsed , pulse width ≤ 300us , duty cycle ≤ 2%
- 3、The EAS data shows Max. rating . The test condition is VDD =32V,VGS =10V,L=0.1mH,IAS =70A
- 4、The power dissipation is limited by 150°C junction temperature
- 5、The data is theoretically the same as I D and I DM , in real applications , should be limited by total power dissipation.

Typical Characteristics

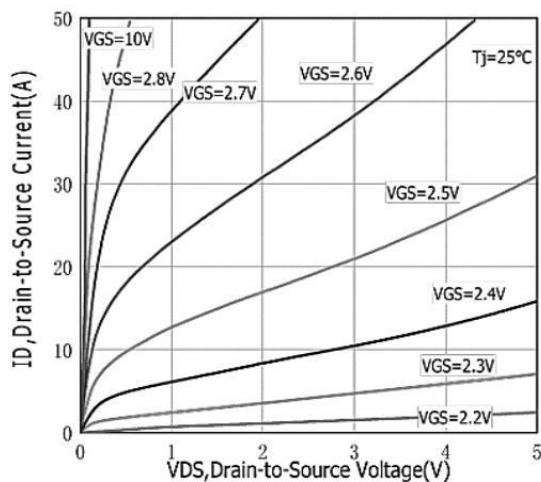


Figure.1 Typical Output Characteristics

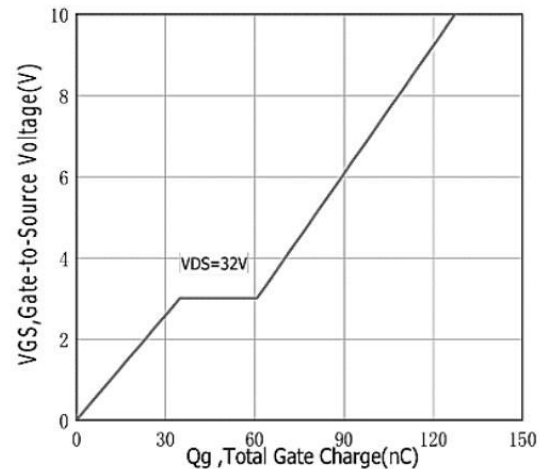


Figure.2 Typical Gate Charge vs Gate to Source Voltage

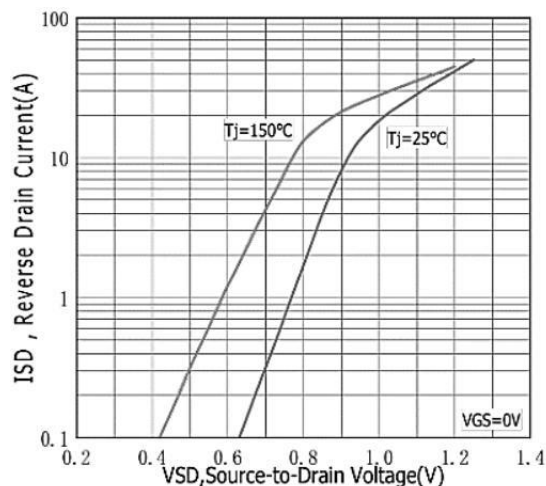


Figure.3 Typical Body Diode Transfer Characteristics

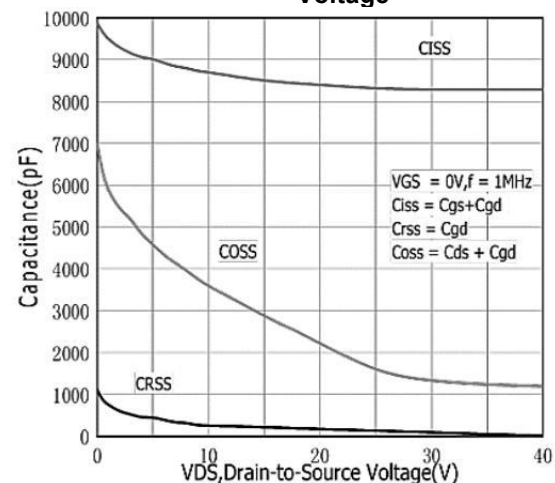


Figure 4: Body Diode Characteristics

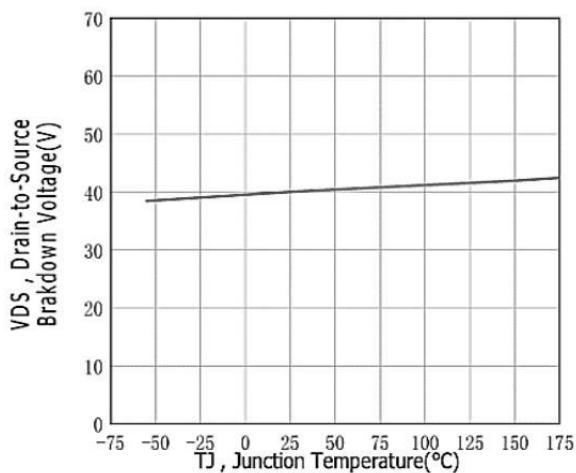


Figure.5 Typical Breakdown Voltage vs Junction Temperature

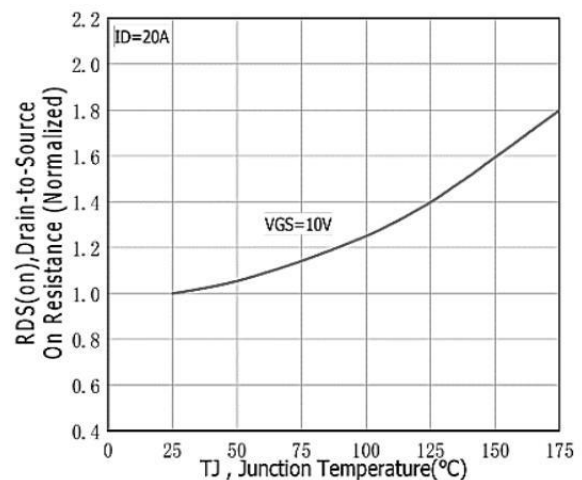


Figure 6: Capacitance Characteristics

Typical Characteristics

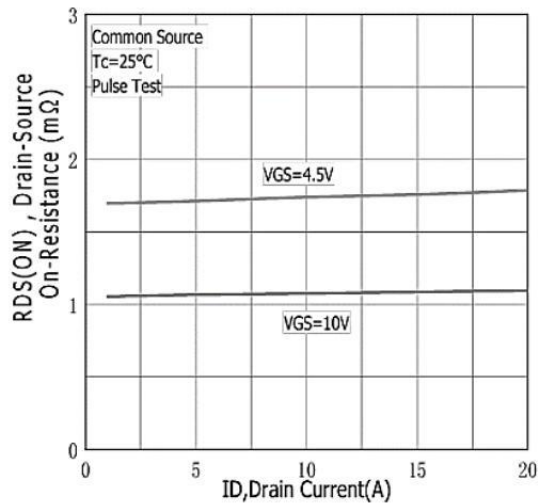


Figure.7 Typical Drain to Source ON Resistance vs Drain Current

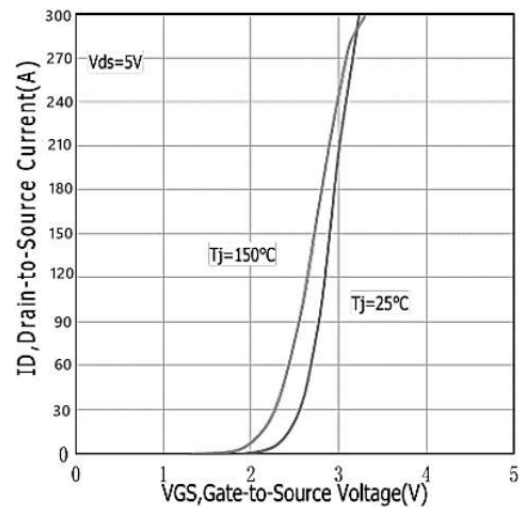


Figure.10 Typical Transfer Characteristics

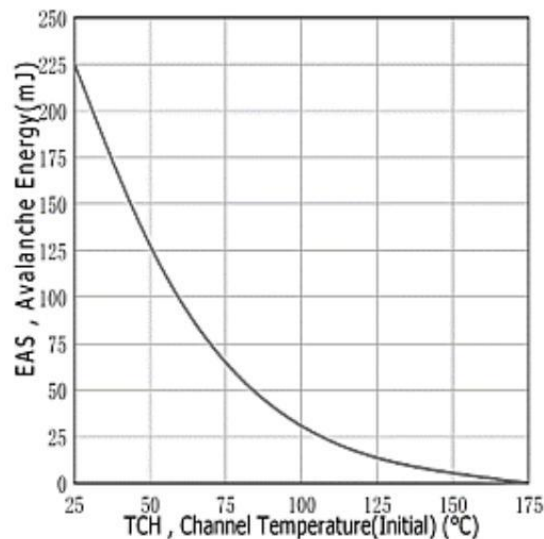


Figure.9 Maximum EAS vs Channel Temperature

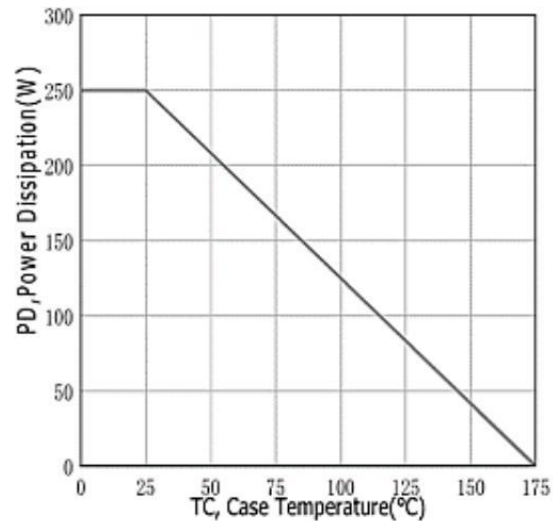


Figure.12 Maximum Power Dissipation vs Case Temperature

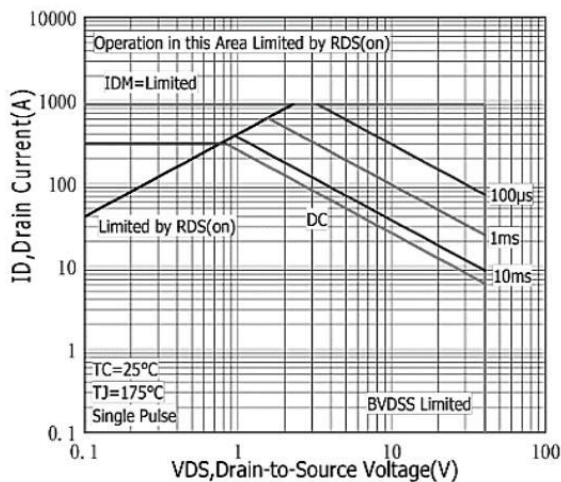


Figure 11: Maximum Safe Operating Area

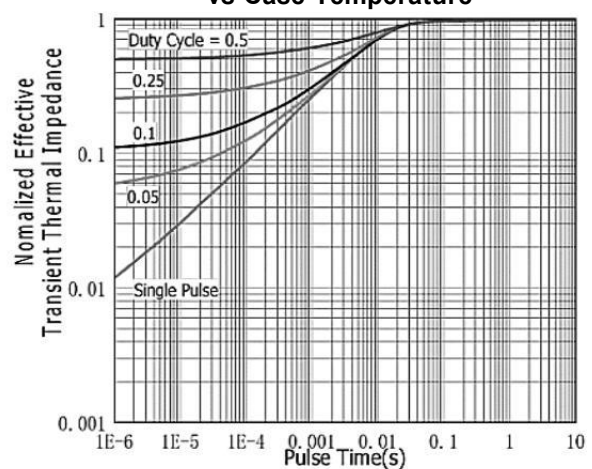
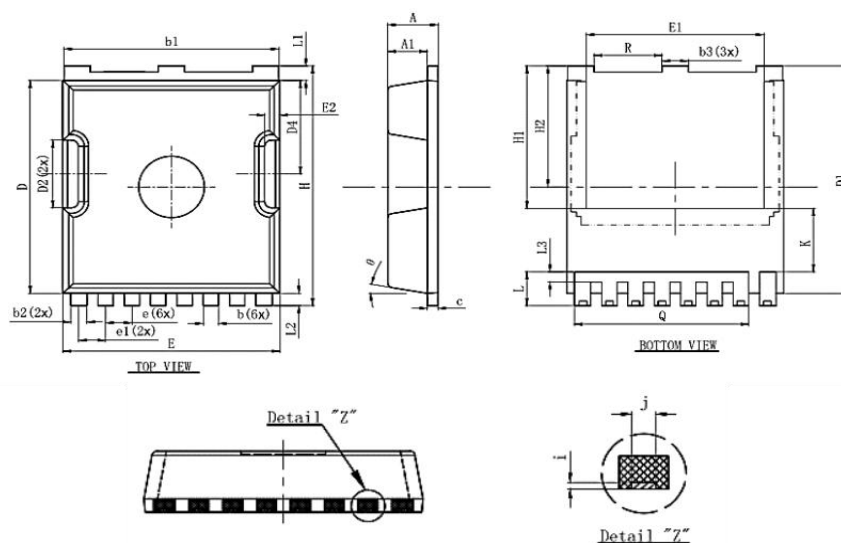


Figure.12: Maximum Effective Transient Thermal Impedance, Junction-to-Cas

Package Mechanical Data-TOLLA-8-XZ Single



Symbol	Dimensions In Millimeters		
	Min.	Nom	Max.
A	2.2	2.3	2.4
A1	1.7	1.8	1.9
b	0.6	0.7	0.8
b1	9.7	9.8	9.9
b2	0.65	0.75	0.85
b3	1.1	1.2	1.3
C	0.4	0.5	0.6
D	10.3	10.4	10.5
D1	11.0	11.1	11.2
D2	3.2	3.3	3.4
D4	4.47	4.57	4.67
E	9.8	9.9	10.0
E1	8.0	8.1	8.2
E2	0.5	0.6	0.7
e	1.200 (BSC)		
e1	1.225 (BSC)		
H	11.6	11.7	11.8
H1	6.95BSC		
H2	5.9BSC		
i	0.1REF		
j	0.350REF		
K	3.100REF		
L	1.55	1.65	1.75
L1	0.6	0.7	0.8
L2	0.5	0.6	0.7
L3	0.4	0.5	0.6
Q	7.95REF		
R	3.0	3.1	3.2
θ	10°REG		

Package Marking and Ordering Information

Product ID	Pack	Marking	Qty(PCS)
TAPING	TOLLA-8L		2000