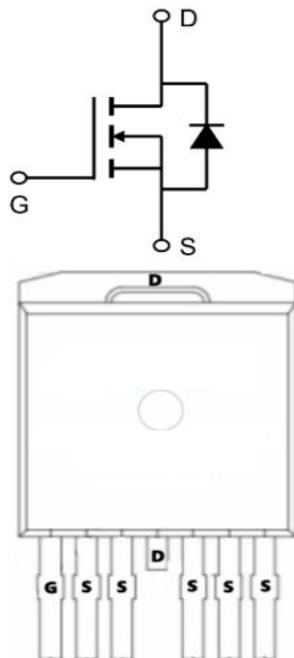


**Description**

The SX280N10T6 uses advanced technology to provide excellent  $R_{DS(ON)}$ , low gate charge and operation with gate voltages as low as 10V. This device is suitable for use as a Battery protection or in other Switching application.

**General Features**

$V_{DS} = 100V$   $I_D = 280A$

$R_{DS(ON)} < 2.8m\Omega$  @  $V_{GS}=10V$

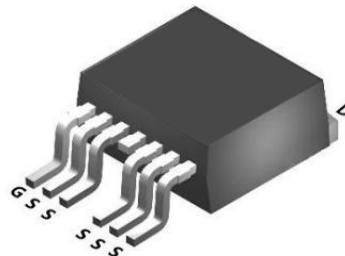
**Application**

DC/DC Converter

LED Backlighting

Power Management Switches

TO-263-6L

**Absolute Maximum Ratings ( $T_c=25^\circ C$  unless otherwise noted)**

Symbol	Parameter	Rating	Units
$V_{DS}$	Drain-Source Voltage	100	V
$V_{GS}$	Gate-Source Voltage	$\pm 20$	V
$I_D @ T_c=25^\circ C$	Continuous Drain Current, $V_{GS} @ 10V$	280	A
$I_D @ T_c=100^\circ C$	Continuous Drain Current, $V_{GS} @ 10V$	183	A
$I_{DM}$	Pulsed Drain Current	900	A
$E_{AS}$	Single Pulse Avalanche Energy	1028	mJ
$I_{AS}$	Avalanche Current	54	A
$P_D @ T_c=25^\circ C$	Total Power Dissipation <sup>4</sup>	379	W
$T_{STG}$	Storage Temperature Range	-55 to 175	°C
$T_J$	Operating Junction Temperature Range	-55 to 175	°C
$R_{eJA}$	Thermal Resistance Junction-Ambient	0.33	°C/W
$R_{eJC}$	Thermal Resistance Junction-Case	60	°C/W

**Electrical Characteristics ( $T_c=25^\circ\text{C}$  unless otherwise noted)**

Symbol	Parameter	Test Conditions	Min.	Typ.	Max.	Unit
V(BR)DSS	Drain-Source Breakdown Voltage	$V_{GS}=0\text{V}$ , $I_D=250\mu\text{A}$	100	107	-	V
IGSS	Gate-body Leakage current	$V_{DS}=0\text{V}$ , $V_{GS} = \pm 20\text{V}$	-	-	$\pm 100$	nA
IDSS	Zero Gate Voltage Drain Current $T_J=25^\circ\text{C}$	$V_{DS}=100\text{V}$ , $V_{GS}=0\text{V}$	-	-	1	$\mu\text{A}$
IDSS	Zero Gate Voltage Drain Current $T_J=100^\circ\text{C}$		-	-	100	
VGS(th)	Gate-Threshold Voltage	$V_{DS}=V_{GS}$ , $I_D=250\mu\text{A}$	2	3	4	V
RDS(on)	Drain-Source on-Resistance <sup>4</sup>	$V_{GS}=10\text{V}$ , $I_D=20\text{A}$	-	2.0	2.8	$\text{m}\Omega$
gfs	Forward Transconductance <sup>4</sup>	$V_{DS}=10\text{V}$ , $I_D=20\text{A}$	-	76	-	S
Ciss	Input Capacitance	$V_{DS}=50\text{V}$ , $V_{GS}=0\text{V}$ , $f=1\text{MHz}$	-	9030	-	$\text{pF}$
Coss	Output Capacitance		-	1505	-	
Crss	Reverse Transfer Capacitance		-	40	-	
R <sub>g</sub>	Gate Resistance	$f=1\text{MHz}$	-	2.3	-	$\Omega$
Q <sub>g</sub>	Total Gate Charge	$V_{GS} = 10\text{V}$ , $V_{DS} = 50\text{V}$ , $I_D=20\text{A}$	-	150	-	$\text{nC}$
Q <sub>gs</sub>	Gate-Source Charge		-	32.5	-	
Q <sub>gd</sub>	Gate-Drain Charge		-	49	-	
td(on)	Turn-on Delay Time	$V_{GS} = 10\text{V}$ , $V_{DD} = 50\text{V}$ , $R_G = 3\Omega$ , $I_D= 20\text{A}$	-	27	-	$\text{ns}$
t <sub>r</sub>	Rise Time		-	78.5	-	
td(off)	Turn-off Delay Time		-	110	-	
t <sub>f</sub>	Fall Time		-	86	-	
trr	Body Diode Reverse Recovery Time	$I_F = 20\text{A}$ , $dI/dt=100\text{A}/\mu\text{s}$	-	88	-	$\text{ns}$
Q <sub>rr</sub>	Body Diode Reverse Recovery Charge		-	220	-	$\text{nC}$
VSD	Diode Forward Voltage <sup>4</sup>	$I_D = 20\text{A}$ , $V_{GS}=0\text{V}$	-	-	1.2	V
IS	Continuous Source Current $T_c=25^\circ\text{C}$		-	-	220	A

**Notes:**

- 1、The data tested by surface mounted on a 1 inch<sup>2</sup> FR-4 board with 2OZ copper.
- 2、The data tested by pulsed , pulse width  $\leq 300\mu\text{s}$  , duty cycle  $\leq 2\%$
- 3、The EAS data shows Max. rating . The test condition is  $V_{DD}=50\text{V}$ ,  $V_{GS}=10\text{V}$ ,  $L=0.5\text{mH}$ ,  $I_{AS}=54\text{A}$
- 4、The power dissipation is limited by  $150^\circ\text{C}$ junction temperature
- 5、The data is theoretically the same as  $I_D$  and  $I_{DM}$  , in real applications , should be limited by total power dissipation.

## Typical Characteristics

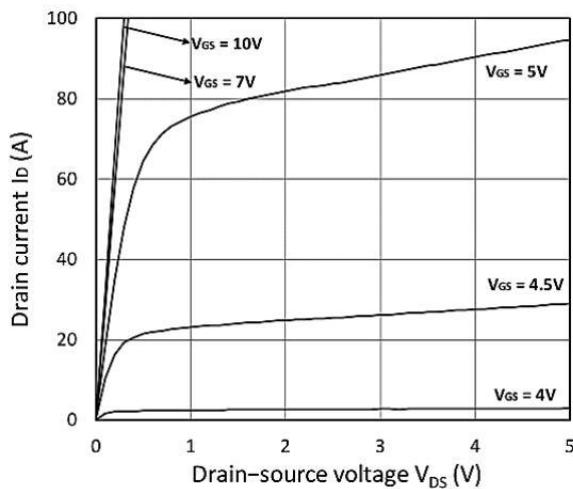


Figure 1. Output Characteristics

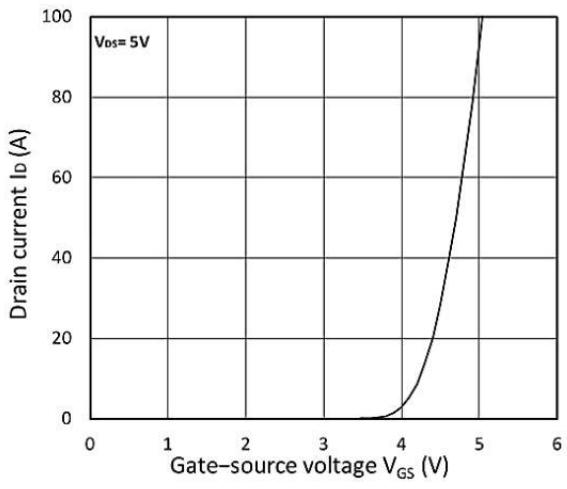


Figure 2. Transfer Characteristics

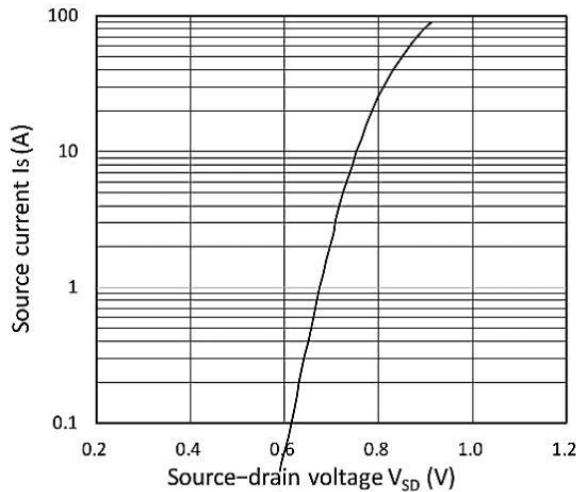


Figure 3. Forward Characteristics of Reverse

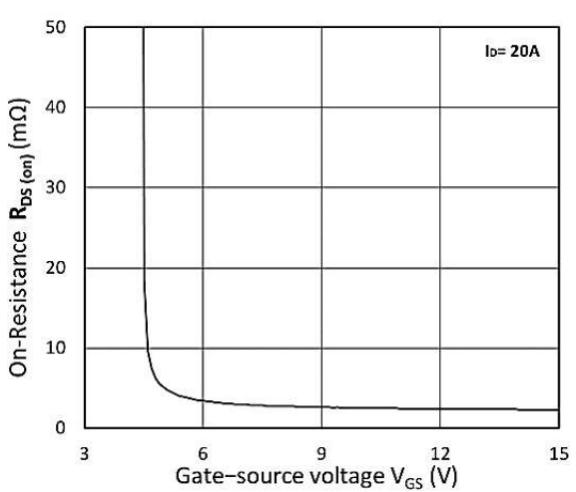


Figure 4. RDS(ON) vs. VGS

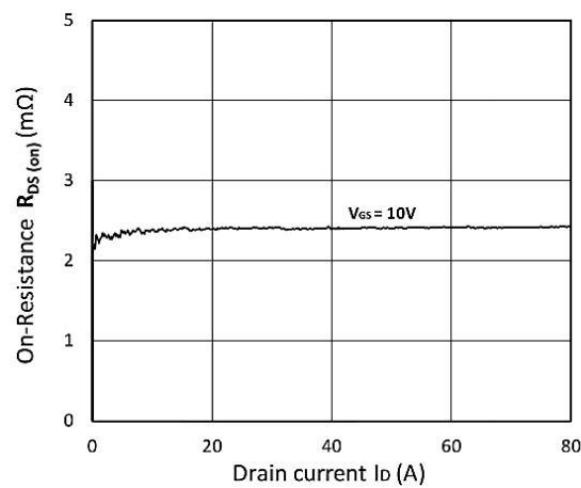


Figure 5. RDS(ON) vs. ID

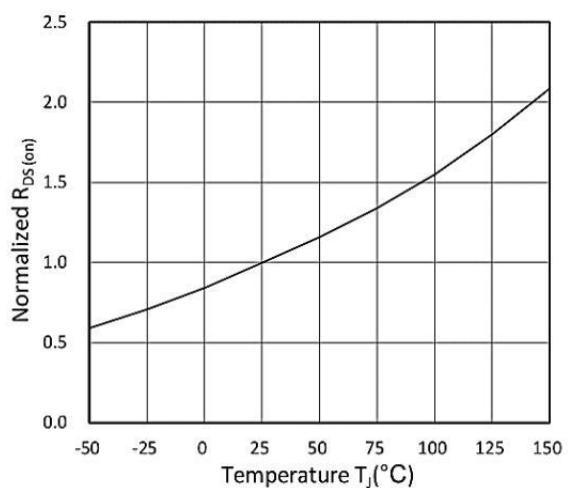
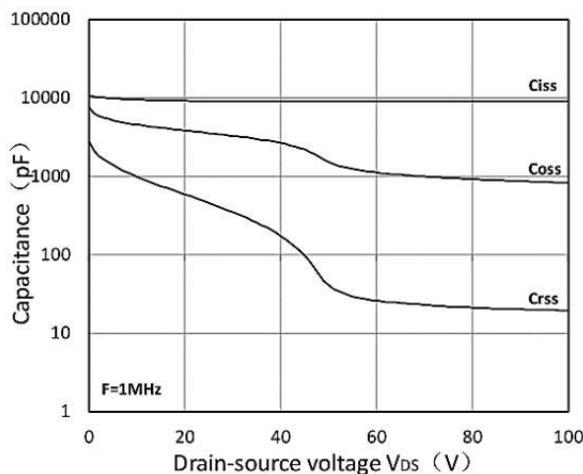
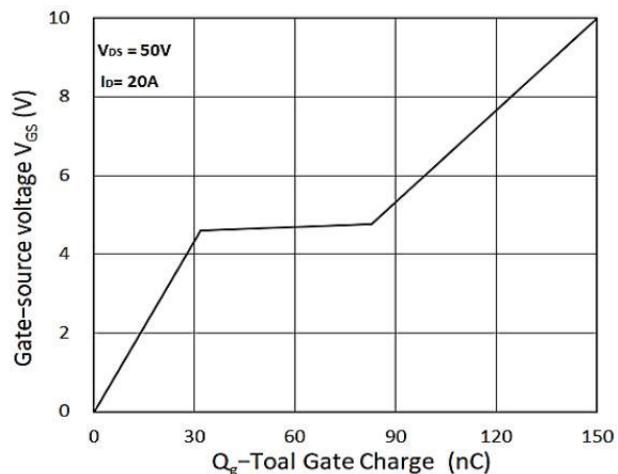


Figure 6. Normalized RDS(on) vs. Temperature

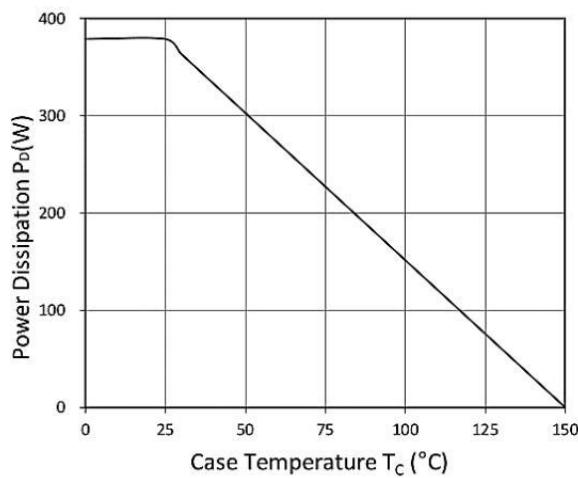
## Typical Characteristics



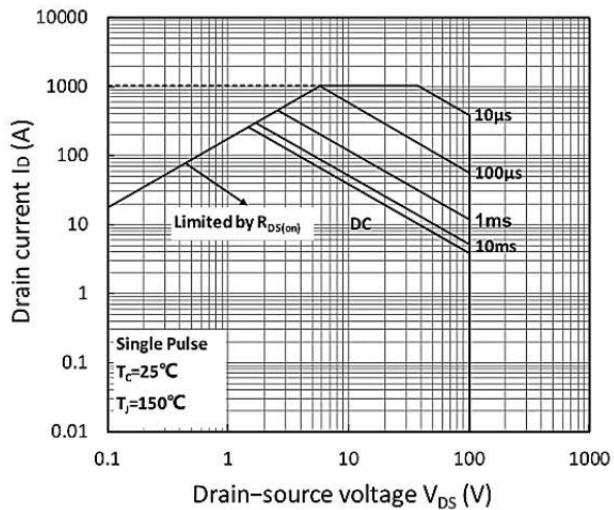
**Figure 7. Capacitance Characteristics**



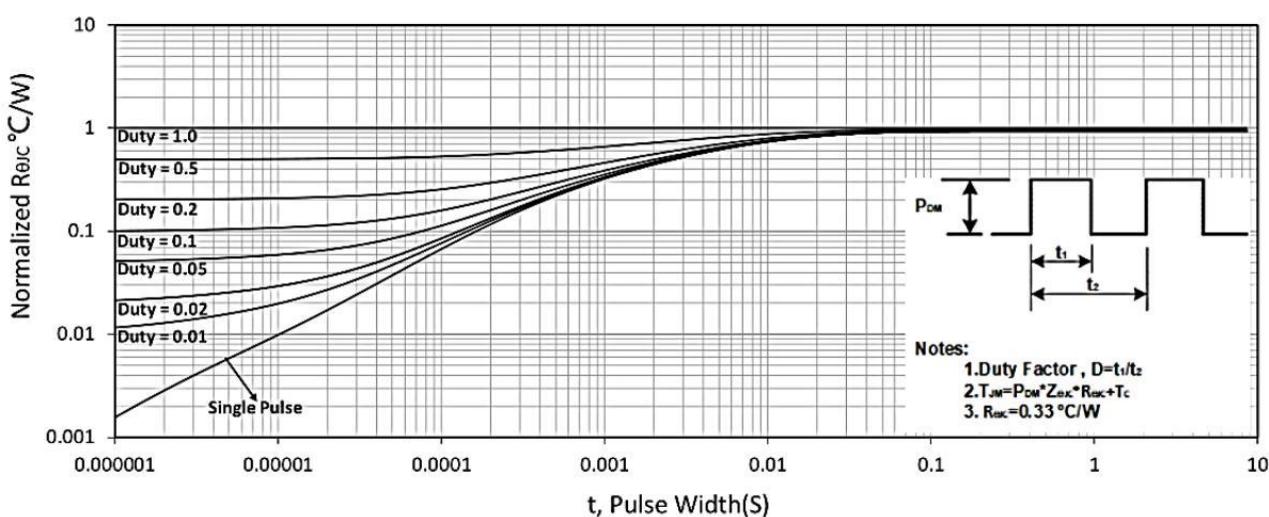
**Figure 8. Gate Charge Characteristics**



**Figure 9. Power Dissipation**

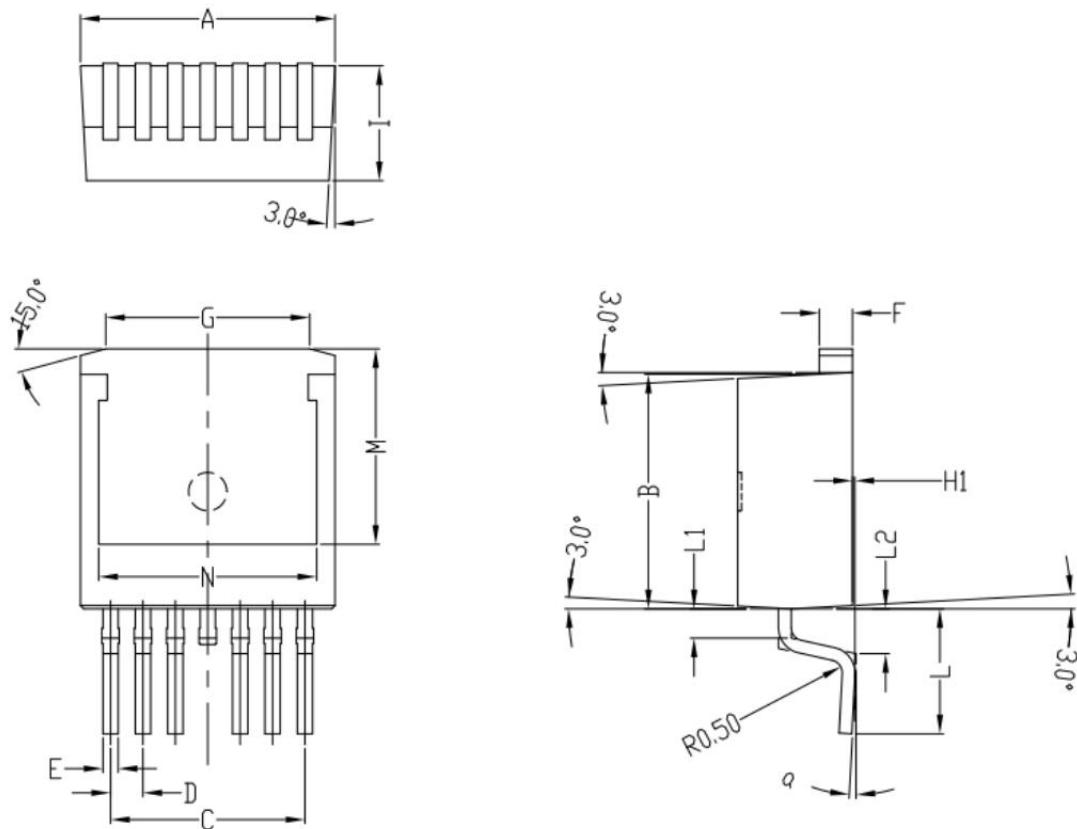


**Figure 10. Safe Operating Area**



**Figure 11. Normalized Maximum Transient Thermal Impedance**

## Package Mechanical Data-TO263-6L



Symbol	Common mm		
	Mim	Nom	Max
A	9.88	9.98	10.08
B	9.09	9.19	9.29
C	7.54	7.62	7.70
D	1.23	1.27	1.31
E	0.55	0.6	0.65
F	1.27	1.30	1.33
G	7.7	8	8.3
H1	-0.1	+0.10	+0.2
I	4.42	4.50	4.58
L	4.60	4.90	5.20
L1	1.05	1.15	1.25
L2	1.66	1.76	1.86
a	-7°	0°	7°
N	8.25	8.55	8.85
M	7.36	7.66	7.96

### Package Marking and Ordering Information

Product ID	Pack	Marking	Qty(PCS)
TAPING	TO-263-6L		800