

BMW60N165UC1

N-Channel Power MOSFET

600 V, 21 A, 165 mΩ



bestirpower

Description

BMW60N165UC1 is power MOSFET using bestirpower's advanced super junction technology that can realize very low on resistance and gate charge.

It will provide much high efficiency by using optimized charge coupling technology. These user friendly devices give an advantage of Low EMI to designers as well as low switching loss.

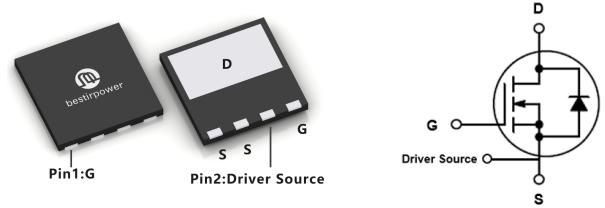
Features

$BV_{DSS} @ T_{J,max}$	I_D	$R_{DS(on),max}$	$Q_{g,typ}$
650 V	21 A	165 mΩ	40 nC

- Ultra-fast body diode.
- Extremely low losses due to very low FOM $R_{dson} \cdot Q_g$ and E_{oss} .
- Very high commutation ruggedness.

Applications

- AC/DC power supply.
- PC power.
- Telecom/Sever.
- Solar inverter.



Absolute Maximum Ratings ($T_c = 25^\circ C$ unless otherwise noted)

Symbol	Parameter		Value	Unit
V_{DSS}	Drain to Source Voltage ¹⁾		600	V
V_{GSS}	Gate to Source Voltage ²⁾		± 30	V
I_D	Drain Current		21	A
	Continuous ($T_c = 125^\circ C$)	11		
I_{DM}	Drain Current	Pulsed ($T_c = 25^\circ C$)	60	A
E_{AS}	Single Pulsed Avalanche Energy ³⁾		320	mJ
I_{AR}	Repetitive Avalanche Energy		3.6	A
dV/dt	MOSFET dV/dt		50	V/ns
	Peak Diode Recovery dV/dt ⁴⁾		50	
P_{tot}	Power Dissipation	($T_c = 25^\circ C$)	152	W
T_J, T_{STG}	Operating and Storage Temperature Range		-55 to 150	°C
T_{sold}	Soldering temperature, w波 soldering only allowed at leads		260	°C

1) Limited by T_j max. Maximum duty cycle $D=0.75$.

2) Pulse width t_p limited by T_j, max .

3) $V_{DD}=50V$, $R_G=25\Omega$, Starting $T_j=25^\circ C$.

4) $V_{DClink}=400V$; $V_{DS,peak} < V(BR)_{DSS}$; identical low side and high side switch with identical R_G .

Thermal Characteristics

Symbol	Parameter	Value	Unit
$R_{\theta_{JC}}$	Thermal Resistance, Junction to Case, Max.	0.82	°C/W
$R_{\theta_{JA}}$	Thermal Resistance, Junction to Ambient, Max.	62	

Package Marking and Ordering Information

Part Number	Top Marking	Package	Packing Method	Quantity
BML60N165UC1	BML60N165UC1	DFN8*8	Tape & Reel	5000 units

Electrical Characteristics ($T_C = 25^\circ C$ unless otherwise noted)

Symbol	Parameter	Test Conditions	Min	Typ	Max	Unit
Off Characteristics						
BV_{DSS}	Drain-Source Breakdown Voltage	$V_{GS} = 0 V$, $I_D = 1 mA$	600	-	-	V
I_{DSS}	Zero Gate Voltage Drain Current	$V_{DS} = 600 V$, $V_{GS} = 0 V$, $T_j = 25^\circ C$	-	-	10	μA
I_{GSS}	Gate-Source Leakage Current	$V_{GS} = \pm 30 V$, $V_{DS} = 0 V$	-	-	± 100	nA
On Characteristics						
$V_{GS(th)}$	Gate Threshold Voltage	$V_{DS} = V_{GS}$, $I_D = 250 \mu A$	3.0	4.0	5.0	V
$R_{DS(on)}$	Static Drain to Source On Resistance	$V_{GS} = 1.0 V$, $I_D = 7 A$, $T_j = 25^\circ C$	-	150	165	$m\Omega$
Dynamic Characteristics						
C_{iss}	Input Capacitance	$V_{DS} = 50 V$, $V_{GS} = 0 V$, $f = 250 kHz$	-	1670	-	pF
C_{oss}	Output Capacitance		-	68	-	pF
C_{rss}	Reverse transfer capacitance		-	4.3	-	pF
$C_{o(er)}$	Energy Related Output Capacitance ¹⁾	$V_{DS} = 0 V$ to 400 V, $V_{GS} = 0 V$	-	53	-	pF
$C_{o(tr)}$	Time Related Output Capacitance ²⁾		-	268	-	pF
$Q_{g(tot)}$	Total Gate Charge at 10 V		-	40	-	nC
Q_{gs}	Gate to Source Charge	$V_{DD} = 400 V$, $I_D = 7 A$, $V_{GS} = 0$ to 10 V	-	9	-	nC
Q_{gd}	Gate to Drain "Miller" Charge		-	17	-	nC
R_G	Gate Resistance		-	4	-	Ω
Switching Characteristics						
$t_{d(on)}$	Turn-On Delay Time	$V_{DD} = 400 V$, $I_D = 7 A$, $V_{GS} = 10 V$	-	11	-	ns
t_r	Turn-On Rise Time		-	13	-	ns
$t_{d(off)}$	Turn-Off Delay Time		-	59	-	ns
t_f	Turn-Off Fall Time		-	5	-	ns
Source-Drain Diode Characteristics						
I_S	Maximum Continuous Diode Forward Current	$V_{GS} = 0 V$, $I_F = 7 A$, $T_r = 25^\circ C$	-	-	21	A
I_{SM}	Maximum Pulsed Diode Forward Current		-	-	60	A
V_{SD}	Diode Forward Voltage		-	0.88	-	V
t_{rr}	Reverse Recovery Time		-	145	-	ns
Q_{rr}	Reverse Recovery Charge		-	1	-	μC
I_{imm}	Peak reverse recovery current		-	10	-	A

1) $C_{o(er)}$ is a fixed capacitance that gives the same stored energy as C_{oss} while V_{DS} is rising from 0 to 400V.2) $C_{o(tr)}$ is a fixed capacitance that gives the same charging time as C_{oss} while V_{DS} is rising from 0 to 400V.

Typical Performance Characteristics

Figure 1. Power dissipation

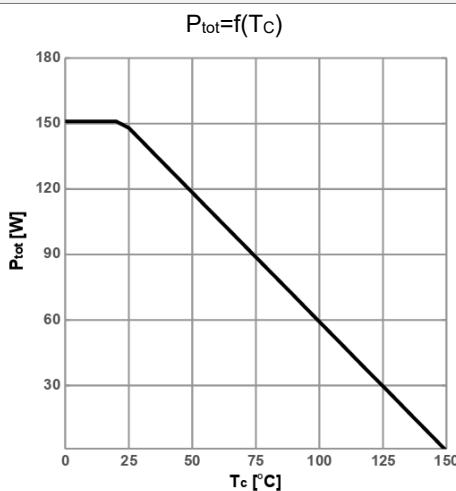


Figure 2. Max. transient thermal impedance

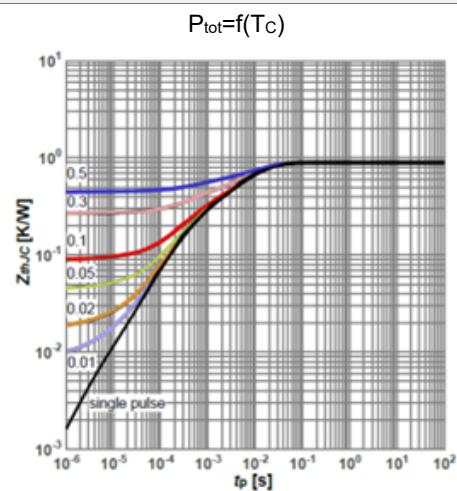


Figure 3. Safe operating area

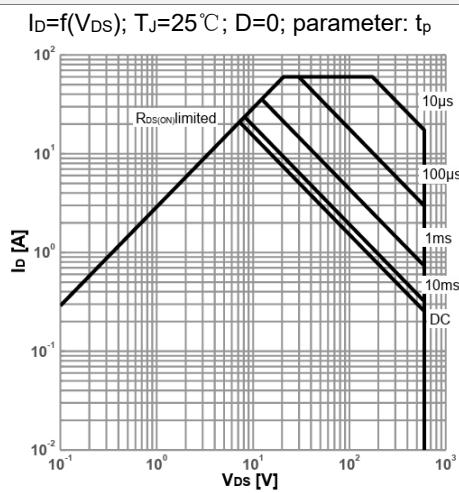


Figure 4. Typ. output characteristics

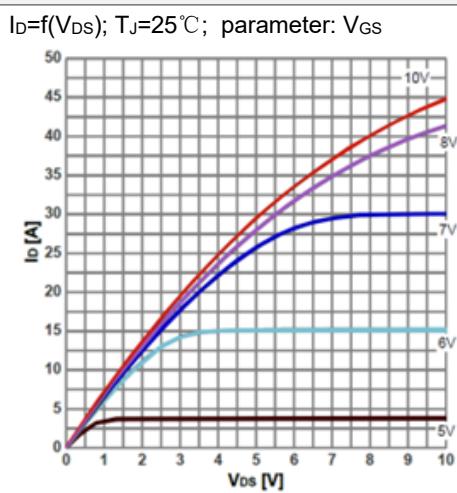


Figure 5. Typ. output Characteristics

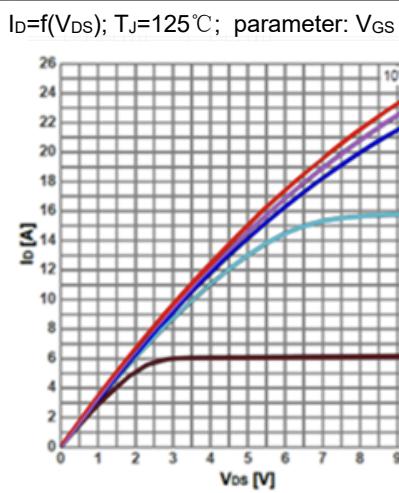
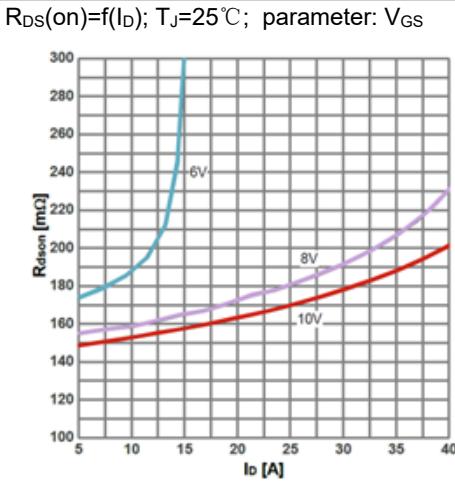


Figure 6. Typ. drain-souce on-state resistance



Typical Performance Characteristics

Figure 7. Drain-source on-state resistance

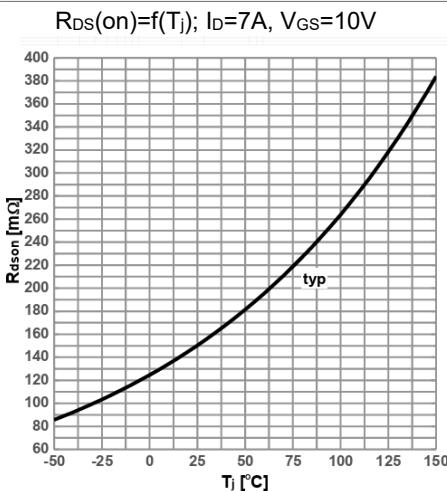


Figure 8. Typ. transfer characteristics

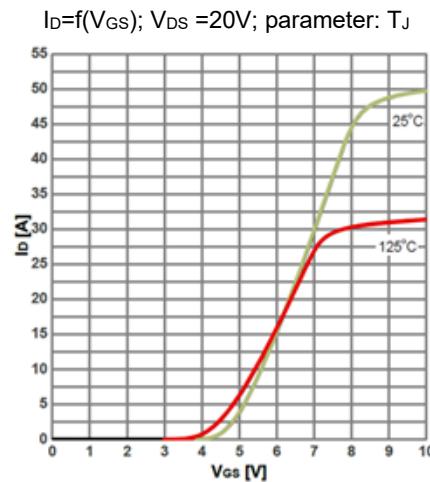


Figure 9. Typ. gate charge

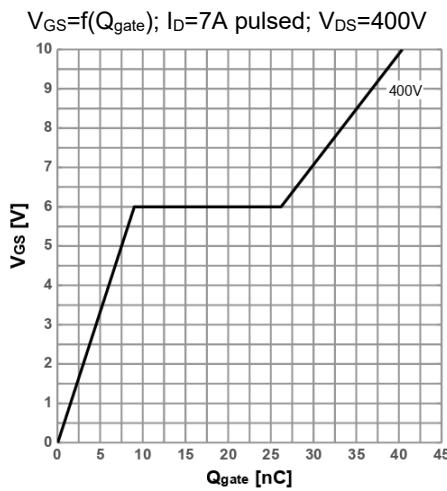


Figure 10. Foward characteristics of reverse diode

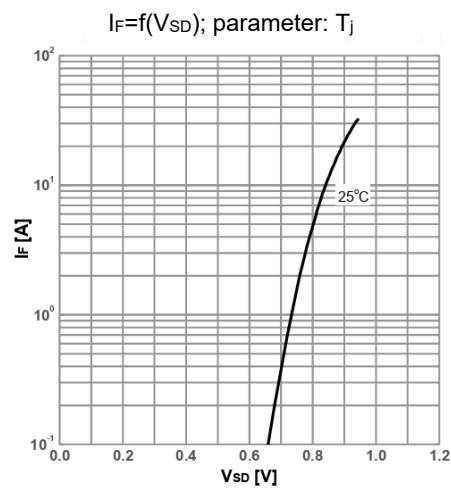


Figure 11. Drain-source breakdown coltage

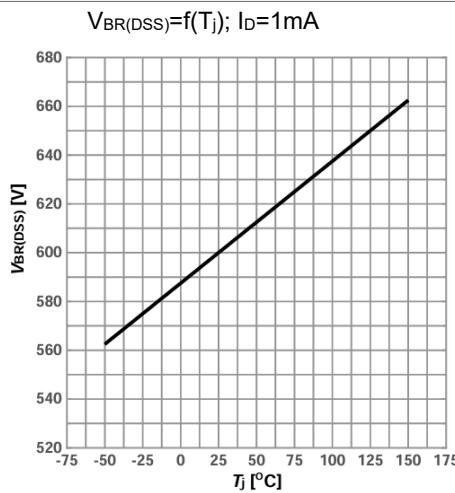


Figure 12. Typ. capacitances

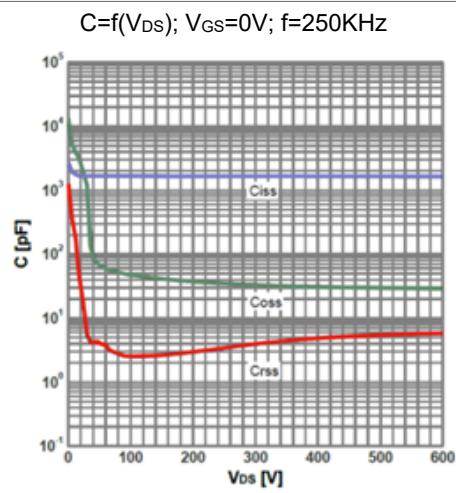
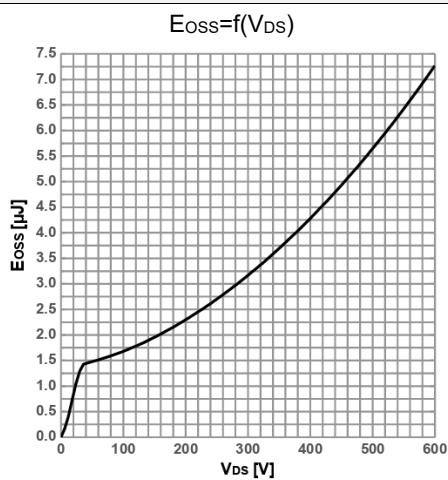


Figure 13. Typ. Coss stored energy

Test Circuits

Figure 14. Diode Characteristics

Test circuit for diode characteristics and Diode recovery waveform

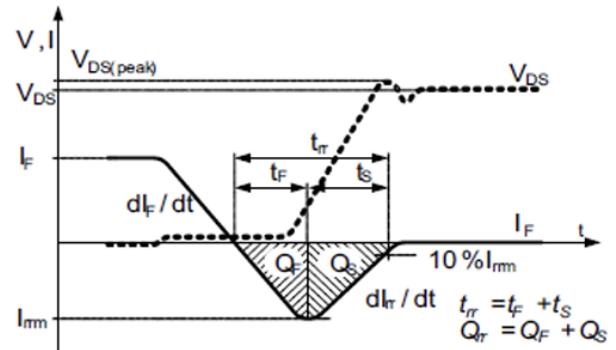
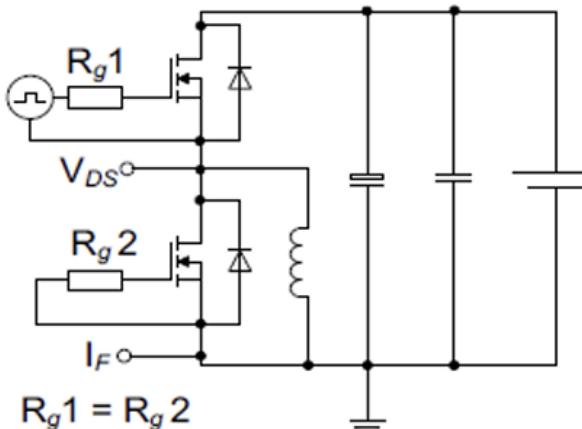


Figure 15. Switching Times

Switching times test circuit for inductive load and Switching times waveform

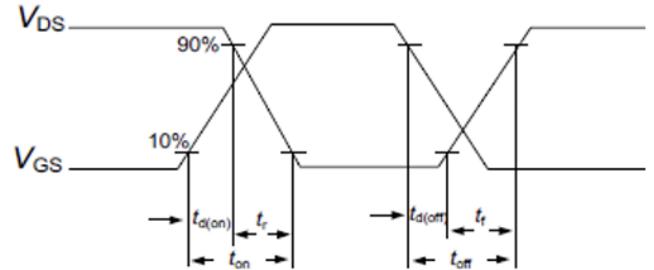
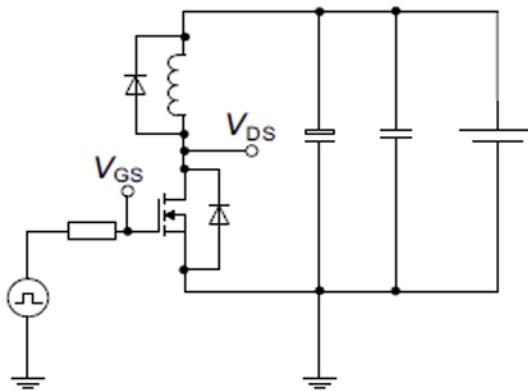
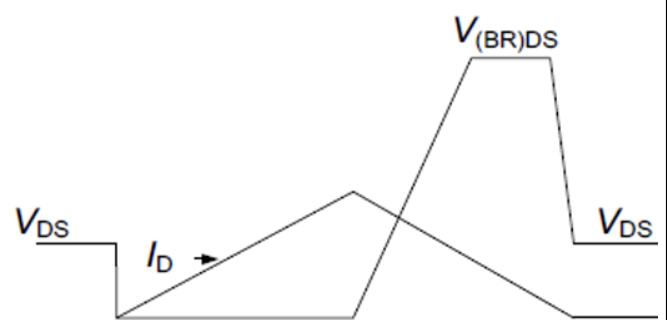
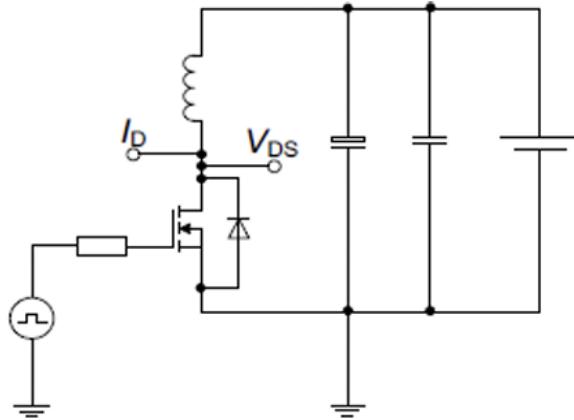


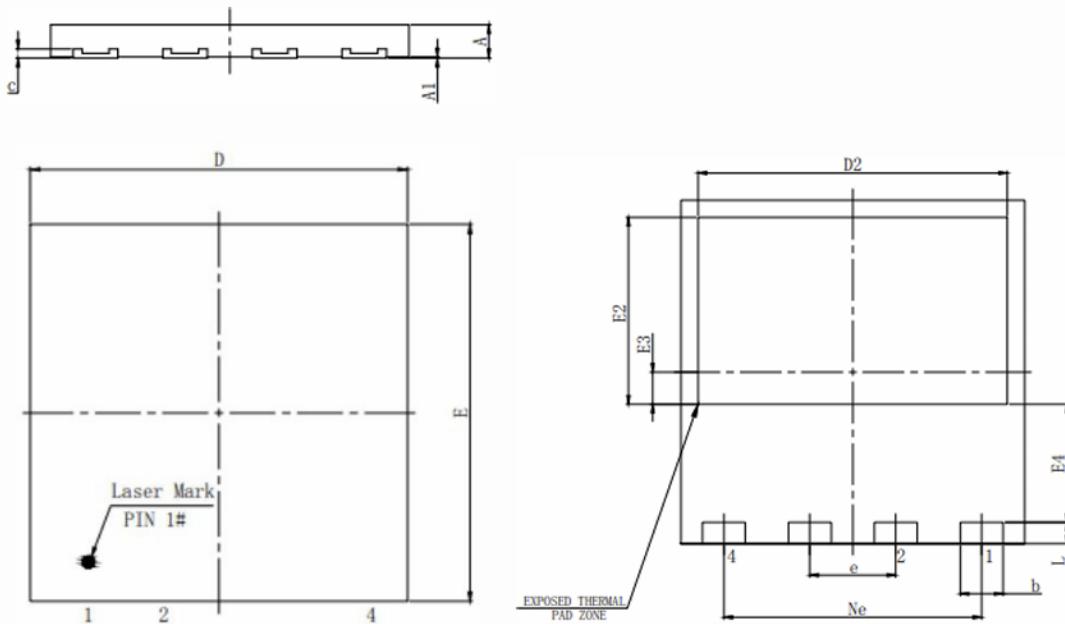
Figure 16. Unclamped Inductive Load

Unclamped inductive load test circuit and Unclamped inductive waveform



Package Outlines

DFN 8*8



Symbol	Dimensions In Millimeters		
	Min	Nom	Max
D	7.90	8.00	8.10
E	7.90	8.00	8.10
D2	7.10	7.20	7.30
E2	4.25	4.35	4.45
e		2.00BSC	
E3		0.75REF	
E4		2.75REF	
Ne		6.00BSC	
b	0.95	1.00	1.05
A	0.70	0.75	0.80
c		0.203REF	
A1	0	/	0.050
L	0.40	0.50	0.55

Disclaimer

Bestirpower reserve the right to make changes, corrections, enhancements, modifications, and improvements to Bestirpower products and/or to this document at any time without notice.

The information given in this document shall in no event be regarded as a guarantee of conditions or characteristics. Bestirpower does not assume any liability for infringement of patents, copyrights, or other intellectual property rights of third parties by or arising from the use of Products or technical information described in this document.

This document is the property of Bestirpower Co., LTD., and not allowed to copy or transformed to other format if not under the authority approval.

© 2024 bestirpower – All rights Reserved