

### APPLICATION

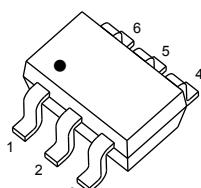
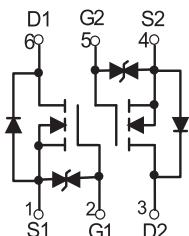
Load Switch for Portable Devices

DC/DC Converter

### FEATURE

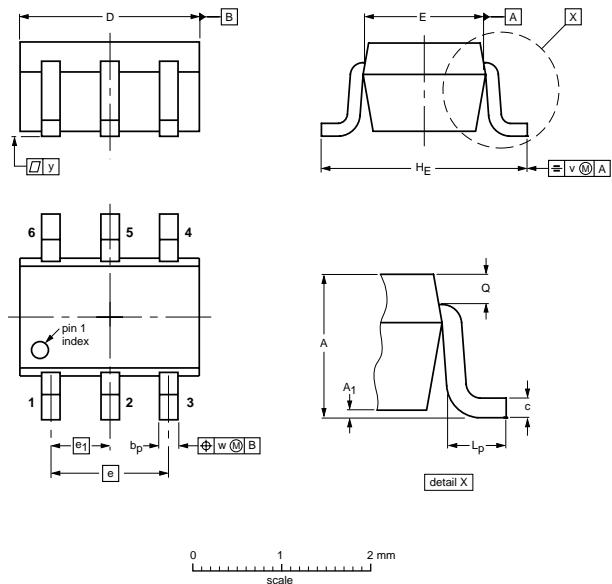
- High density cell design for Low  $R_{DS(on)}$
- Voltage controlled small signal switch
- Rugged and reliable
- High saturation current capability
- ESD protected

### MARKING 72K



SOT-363

### SOT-363



DIMENSIONS (mm are the original dimensions)

UNIT	A	$A_1$ max	$b_p$	c	D	E	e	$e_1$	$H_E$	$L_p$	Q	v	w	y
mm	1.1 0.8	0.1 0.20	0.30 0.20	0.25 0.10	2.2 1.8	1.35 1.15	1.3	0.65	2.2 2.0	0.45 0.15	0.25 0.15	0.2	0.2	0.1

### MOSFET MAXIMUM RATINGS ( $T_a = 25^\circ\text{C}$ unless otherwise noted)

Symbol	Parameter	Value	Unit
$V_{DS}$	Drain-Source voltage	60	V
$V_{GS}$	Gate-Source voltage	$\pm 20$	V
$I_D$	Drain Current	300	mA
$P_D$	Power Dissipation	0.15	W
$T_J$	Junction Temperature	150	$^\circ\text{C}$
$T_{stg}$	Storage Temperature	-55-150	$^\circ\text{C}$
$R_{\theta JA}$	Thermal Resistance from Junction to Ambient	833	$^\circ\text{C}/\text{W}$

# 2N7002KDW

**T<sub>a</sub>=25 °C unless otherwise specified**

Parameter	Symbol	Test Condition	Min	Typ	Max	Units
<b>Static Characteristics</b>						
Drain-Source Breakdown Voltage	V <sub>DS</sub>	V <sub>GS</sub> = 0V, I <sub>D</sub> =250µA	60			V
Gate Threshold Voltage*	V <sub>GS(th)</sub>	V <sub>DS</sub> =V <sub>GS</sub> , I <sub>D</sub> =1mA	1	1.3	2.5	V
Zero Gate Voltage Drain Current	I <sub>DSS</sub>	V <sub>DS</sub> =48V, V <sub>GS</sub> = 0V			1	µA
Gate –Source leakage current	I <sub>GSS1</sub>	V <sub>GS</sub> =±20V, V <sub>DS</sub> = 0V			±10	µA
Drain-Source On-Resistance*	R <sub>DS(on)</sub>	V <sub>GS</sub> = 4.5V, I <sub>D</sub> =200mA		1.1	5.3	Ω
		V <sub>GS</sub> =10V, I <sub>D</sub> =500mA		0.9	5	Ω
Diode Forward Voltage	V <sub>SD</sub>	V <sub>GS</sub> =0V, I <sub>S</sub> =300mA			1.5	V
Recovered charge	Q <sub>r</sub>	V <sub>GS</sub> =0V, I <sub>S</sub> =300mA, V <sub>R</sub> =25V, dI <sub>S</sub> /dt=-100A/µs		30		nC
<b>Dynamic Characteristics**</b>						
Input Capacitance	C <sub>iss</sub>	V <sub>DS</sub> =10V, V <sub>GS</sub> =0V, f =1MHz			40	pF
Output Capacitance	C <sub>oss</sub>				30	pF
Reverse Transfer Capacitance	C <sub>rss</sub>				10	pF
<b>Switching Characteristics**</b>						
Turn-On Delay Time	t <sub>d(on)</sub>	V <sub>GS</sub> =10V, V <sub>DD</sub> =50V, R <sub>G</sub> =50Ω, R <sub>GS</sub> =50Ω, R <sub>L</sub> =250Ω			10	ns
Turn-Off Delay Time	t <sub>d(off)</sub>				15	ns
Reverse recovery Time	t <sub>rr</sub>	V <sub>GS</sub> =0V, I <sub>S</sub> =300mA, V <sub>R</sub> =25V, dI <sub>S</sub> /dt=-100A/µs		30		ns
<b>GATE-SOURCE ZENER DIODE</b>						
Gate-Source Breakdown Voltage	BV <sub>GSO</sub>	I <sub>GS</sub> =±1mA (Open Drain)	±21.5		±30	V

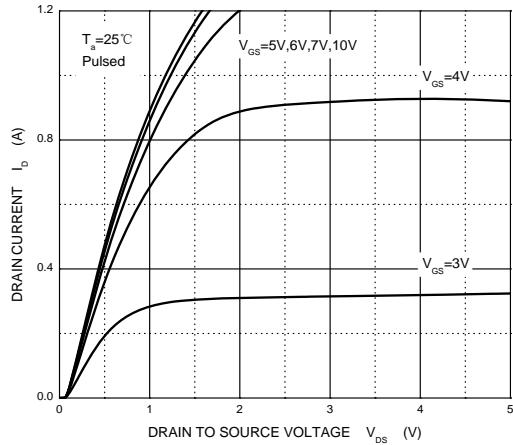
**Notes :**

\*Pulse Test : Pulse Width ≤300µs, Duty Cycle ≤2%.

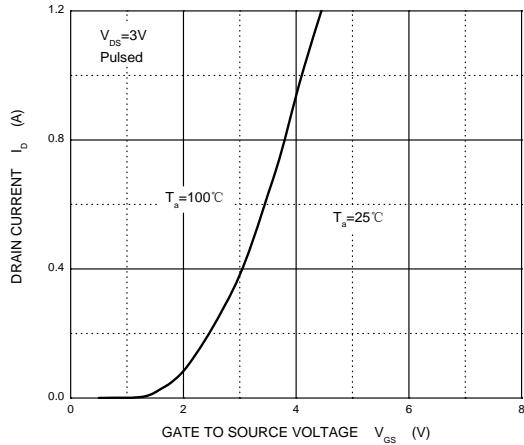
\*\*These parameters have no way to verify.

## RATING AND CHARACTERISTIC CURVES (2N7002KDW)

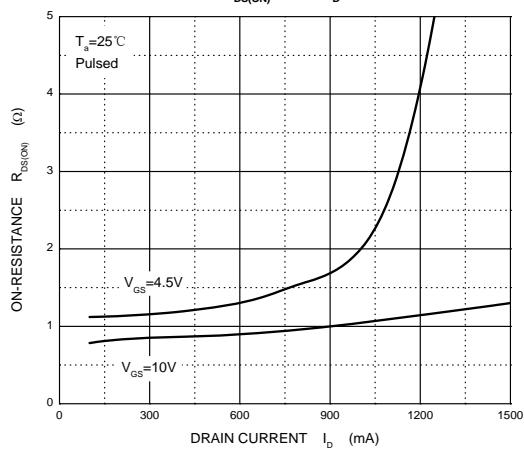
**Output Characteristics**



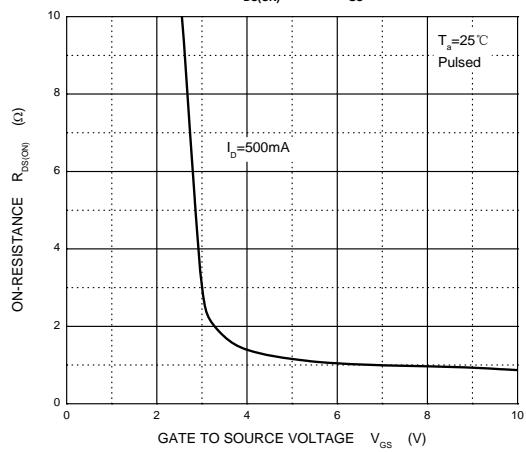
**Transfer Characteristics**



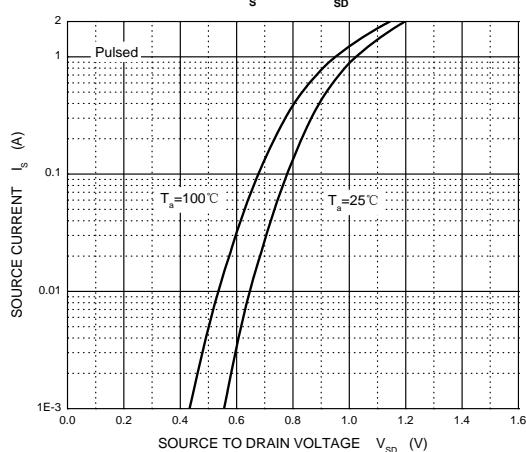
$R_{DS(ON)}$  —  $I_D$



$R_{DS(ON)}$  —  $V_{GS}$



$I_S$  —  $V_{SD}$



**Threshold Voltage**

