

Description

The 2N7002KW is N-Channel enhancement MOS Field Effect Transistor. Uses advanced trenchtechnology and design to provide excellent RoSROW, with low gate charge. Device is suitable for use in DC-DC conversion, power switch and charging circuit.

General Features

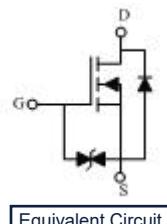
- High density cell design for Low $R_{DS(on)}$
- Voltage controlled small signal switch
- Rugged and reliable
- High saturation current capability
- ESD protected



SOT-323

Applications

- Load Switch for Portable Devices
- DC/DC Converter



Equivalent Circuit

Ordering information

Product ID	Pack	Naming rule	Marking	Qty(PCS)
2N7002KW	SOT-323	 产品名称 product name	K72	3000
			72K	

Absolute Maximum Ratings ($T_A=25^\circ\text{C}$ unless otherwise noted)

Symbol	Parameter	Rating	Units
V_{DSS}	Drain-Source voltage	60	V
V_{GS}	Gate-Source Voltage	± 20	V
I_D	Drain Current-Continuous	0.34	A
I_{DM}	Drain Current -Pulsed(note1)	0.8	A
P_D	Power Dissipation	0.2	W
$R_{\theta JA}$	Thermal Resistance from Junction to Ambient	625	$^\circ\text{C}/\text{W}$
T_J, T_{stg}	Operation Junction and Storage Temperature Range	-55~+150	$^\circ\text{C}$

Electrical Characteristics ($T_A=25^\circ C$, unless otherwise noted)

Symbol	Parameter	Conditions	Min.	Typ.	Max.	Unit
STATIC CHARACTERISTICS						
$V_{(BR)DSS}$	Drain-Source Breakdown Voltage	$V_{GS}=0V, I_D=250\mu A$	60	---	---	V
$V_{GS(th)}$	GateThreshold Voltage (note 2)	$V_{DS}=V_{GS}, I_D=1mA$	1	1.6	2.5	V
I_{DSS}	Zero Gate Voltage Drain Current	$V_{DS}=48V, V_{GS}=0V$	---	---	1	μA
I_{GSS}	Gate -Source leakage current	$V_{GS}=\pm 20V, V_{DS}=0V$	---	---	± 10	μA
$R_{DS(on)}$	Drain-Source On-Resistance (note 2)	$V_{GS}= 4.5V, I_D=200mA$	---	1.1	2	Ω
		$V_{GS}= 10V, I_D=500mA$	---	0.9	2.5	
DYNAMIC PARAMETERS (note 3)						
C_{iss}	Input Capacitance	$V_{DS}=10V, V_{GS}=0V, f=1MHz$	---	---	40	pF
C_{oss}	Output Capacitance		---	---	30	
C_{rss}	Reverse Transfer Capacitance		---	---	10	
SWITCHING PARAMETERS(note 3)						
$T_{d(on)}$	Turn-On Delay Time	$V_{GS}=10V, V_{DD}=50V, R_G=50\Omega, R_{GS}=50\Omega, R_L=250\Omega$	---	---	10	ns
T_r	Rise Time		---	---	15	
T_{rr}	Reverse recovery Time	$V_{GS}=0V, I_S=300mA, V_R=25V, dI/dt=-100A/\mu S$	---	30	---	nC
Q_r	Recovered charge	$V_{GS}=0V, I_S=300mA, V_R=25V, dI/dt=-100A/\mu S$	---	30	---	
GATE-SOURCE ZENER DIODE						
BV_{GSO}	Gate-Source Breakdown Voltage	$I_{GS}=\pm 1mA$ (Open Drain)	± 21.5	---	± 30	V
DRAIN-SOURCE DIODE						
V_{SD}	Diode Forward Voltage(note 2)	$V_{GS}=0V, I_S=300mA$	---	---	1.5	V
I_S	Continuous Diode Forward Current		---	---	0.2	A
I_{SM}	Pulsed Diode Forward Current(note1)		---	---	0.53	A

Notes :

- 1.Repetitive ratingPluse width limited by junction temperature.
- 2.Pulse Test : Pulse width300 μs , duty cycle%.
- 3.Guaranteed by design, not subject to production testing.

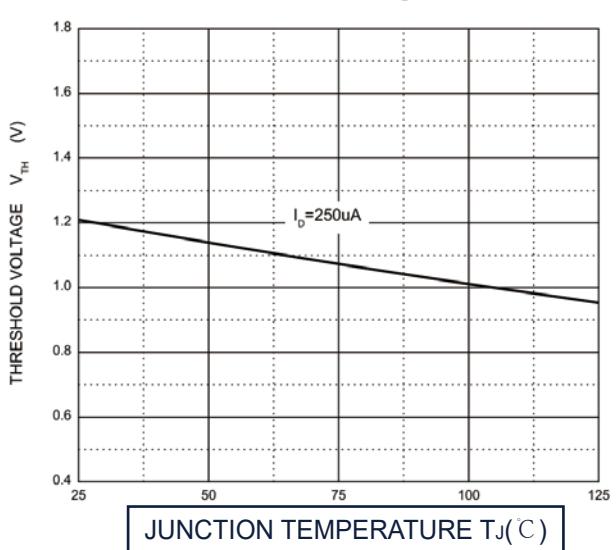
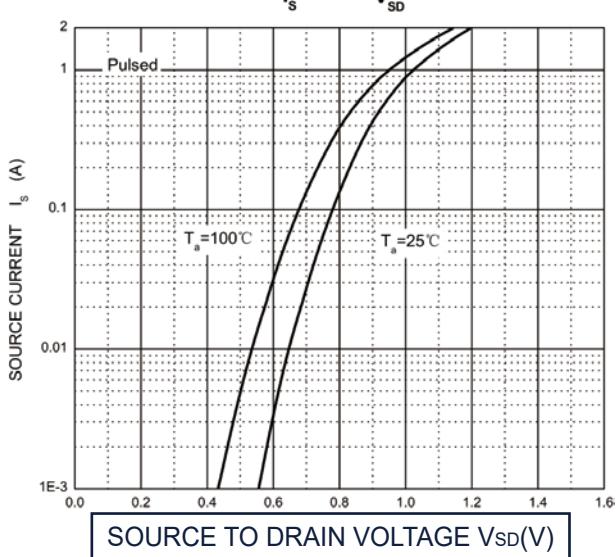
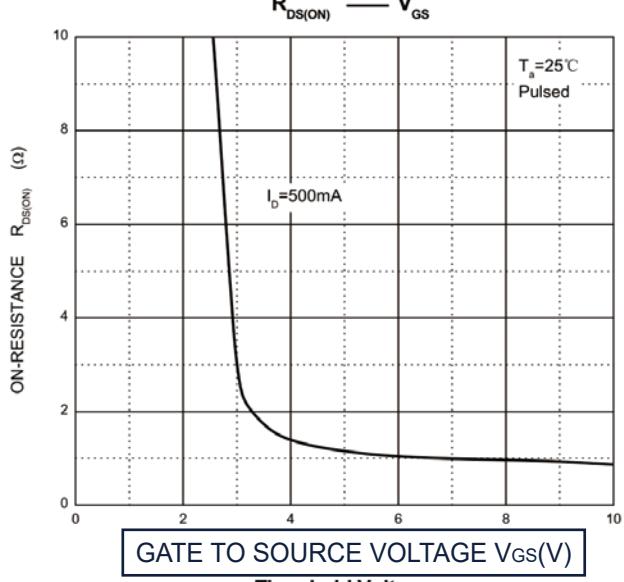
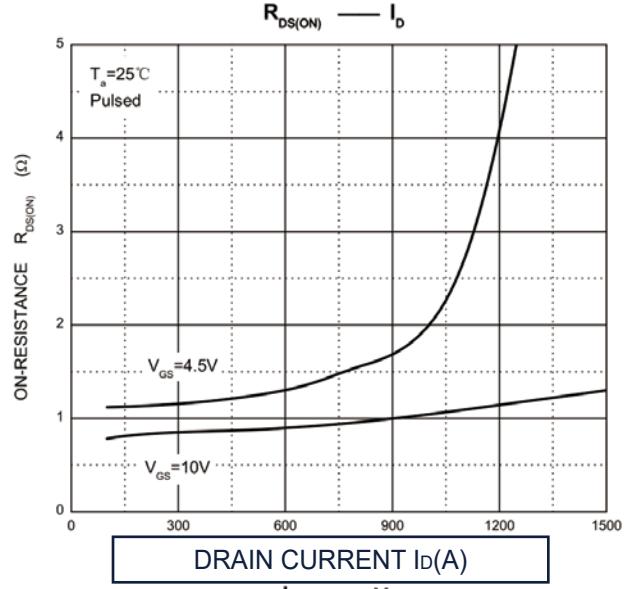
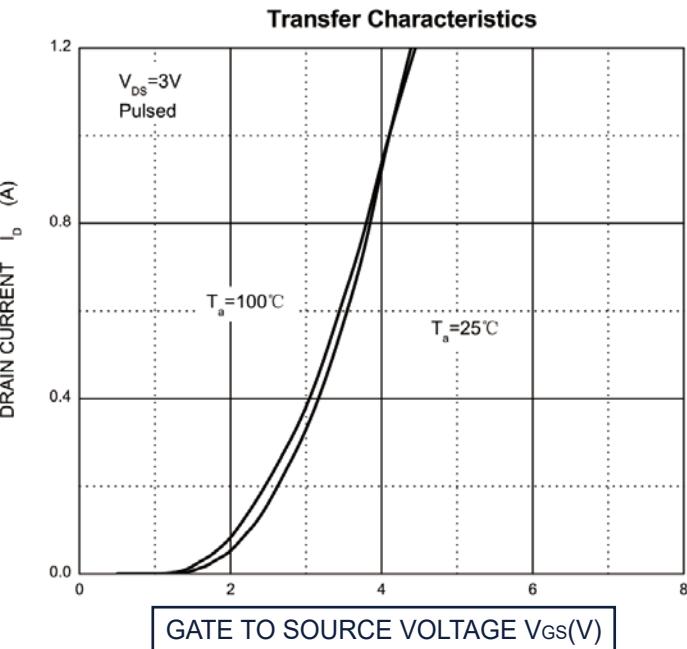
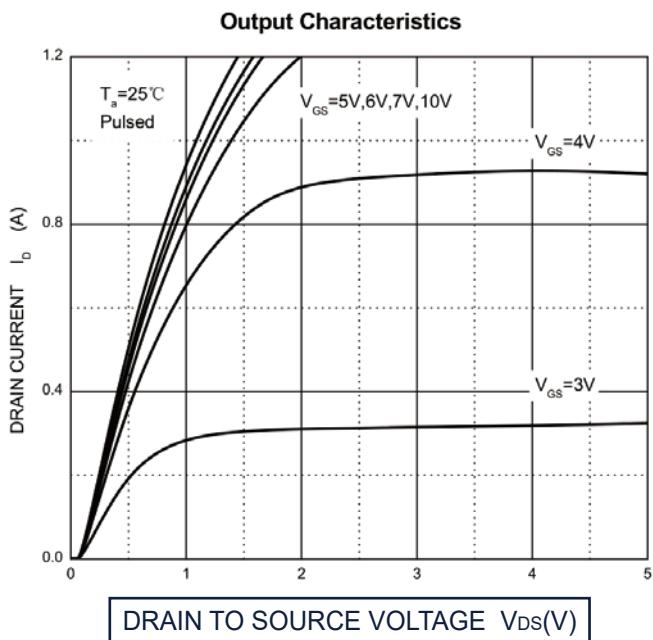


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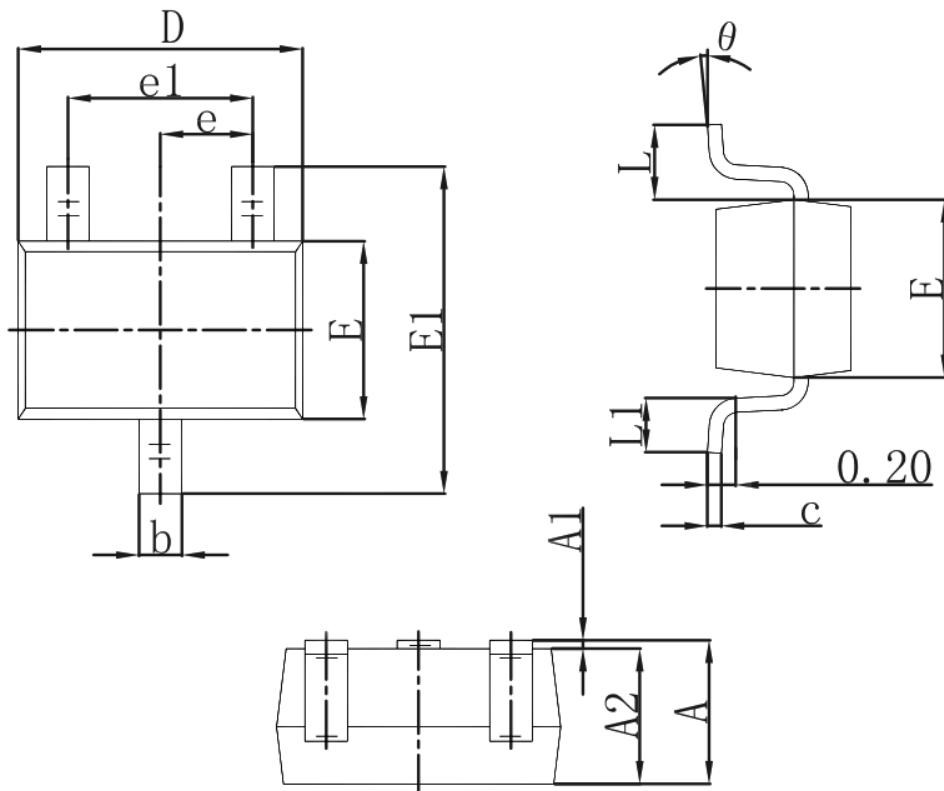
TL-2N7002KW

SOT-323 60V N-Channel Enhancement Mode MOSFET

Typical Characteristics



SOT-323 Package Outline Dimensions



Symbol	Dimensions in Millimeters		Dimensions In Inches	
	Min	Max	Min	Max
A	0.900	1.100	0.035	0.043
A1	0.000	0.100	0.000	0.004
A2	0.900	1.000	0.035	0.039
b	0.200	0.400	0.008	0.016
c	0.080	0.150	0.003	0.006
D	2.000	2.200	0.079	0.087
E	1.150	1.350	0.045	0.053
E1	1.350	2.450	0.085	0.096
e	0.650 TYP		0.026 TYP	
e1	1.200	1.400	0.047	0.055
L	0.525 REF		0.021 REF	
L1	0.260	0.460	0.010	0.018
θ	0°	8°	0°	8°