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TPM6052PK3

P-Channel Enhancement Mode Field Effect Transistor

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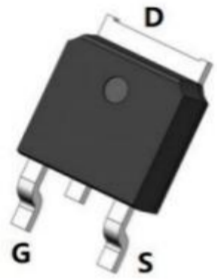
Product Summary

- V_{DS} -60 V
- I_{DS} -50A
- $R_{DS(ON)}$ (at V_{GS}= -10V) <20mΩ(Typ)

Application

- Reverse Battery protection
- Load switch
- Power management
- PWM Application

Package and Pin Configuration



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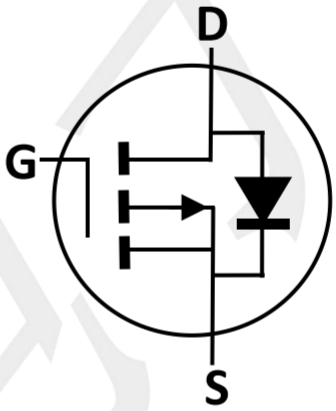
Circuit diagram



Equivalent Circuit

Absolute Maximum Ratings (T_C=25℃unless otherwise noted)

PARAMETER	SYMBOL	LIMIT	UNIT
Drain-Source Voltage	V_{DS}	-60	V
Gate-Source Voltage	V_{GS}	±20	V
Continuous Drain Current ,T_C=25℃	I_D	-50	A
Continuous Drain Current ,T_C=100℃	I_D	-35	A
Pulsed Drain Current (note1)	I_{DM}	-150	A
Maximum Power Dissipation	P_D	95	W
Operating Junction Temperature Range	T_J	150	℃
Storage Temperature Range	T_{stg}	-55 to +150	℃

Thermal Characteristic

PARAMETER		Symbol	Value	Unit
Thermal Resistance from Junction to Ambient(t≤10s)	PCB Mount (note2)	$R_{\theta JA}$	20	°C/W
Maximum Junction-to-Case		$R_{\theta JC}$	1.31	°C/W

notes 1. Repetitive Rating: Pulse width limited by maximum junction temperature.

2 . When mounted on 1" square PCB (FR4 material).

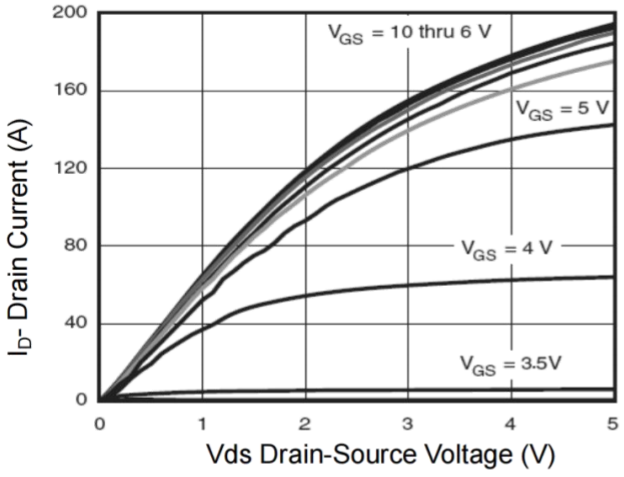
Electrical Characteristics (T_A=25℃unless otherwise noted)

PARAMETER	CONDITIONS	SYMBOL	MIN	TYP	MAX	UNIT
Static						
Drain-Source Breakdown Voltage	V_{GS}=0V, I_D=-250μA	BV_{DSS}	-60	--	--	V
Gate-Source Threshold Voltage	V_{DS}=V_{GS} , I_D= -250μA	$V_{GS(th)}$	-1.0	-1.8	-2.5	V
Gate-Source Leakage	V_{DS}=0V, V_{GS}= ±20V	I_{GSS}	--	--	±100	nA
Zero Gate Voltage Drain Current	V_{DS}= -60V, V_{GS}=0V	I_{DSS}	--	-0.1	-1	μA
	V_{DS}= -60V, T_J=55°C		--	-1	-5	μA
Drain-Source On-State Resistance (Note 1)	V_{GS}= -10V, I_D= -20A	R_{DS}(on)	--	20	28	mΩ
	V_{GS}= -4.5V, I_D= -10A		--	30	38	
Forward Transconductance (Note 2)	V_{DS}= -5V, I_D= -10A	g_{fs}	--	25	--	S
Dynamic (Note 2)						
Total Gate Charge (Note 3)	V_{DS} = -30V, I_D = -20A, V_{GS} = -10V	Q_g	--	75	--	nC
Gate-Source Charge (Note 3)		Q_{gs}	--	16	--	
Gate-Drain Charge (Note 3)		Q_{gd}	--	19	--	
Input Capacitance	V_{DS} = -25V, V_{GS} = 0V, F= 1.0MHz	C_{iss}	--	6560	--	pF
Output Capacitance		C_{oss}	--	719	--	
Reverse Transfer Capacitance		C_{rss}	--	535	--	
Switching						
Turn-On Delay Time (Note 3)	V_{DS} = -30V, RL = 1.5Ω, V_{GS} = -10V, R_{GEN} = 3Ω	$t_{d(on)}$	--	15	--	nS
Rise Time (Note 3)		t_r	--	17	--	
Turn-Off Delay Time (Note 3)		$t_{d(off)}$	--	40	--	
Fall Time (Note 3)		t_f	--	45	--	
Source-Drain Diode Ratings and Characteristics (Note 2)						
Forward Voltage	V_{GS} = 0V, I_{SD} = -10A	V_{SD}	--	-0.85	-1.4	V
Continuous Source Current	Integral reverse diode in the MOSFET	I_S	--	--	-50	A
Pulsed Current (Note 1)		I_{SM}	--	--	-150	A

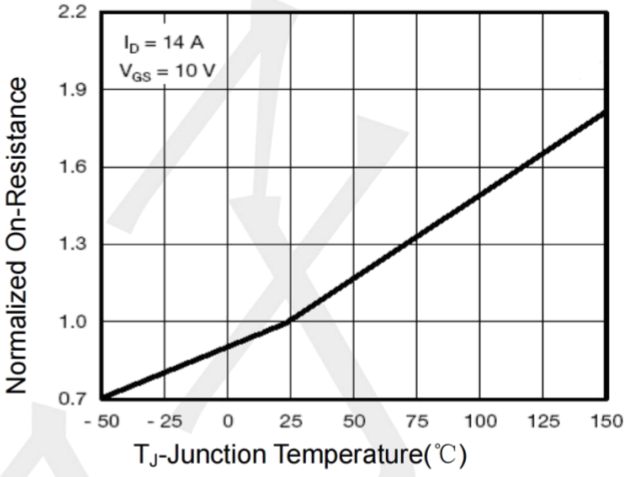
Notes:

1. Pulse test; pulse width ≤ 300 μS, duty cycle ≤ 2%.
2. Guaranteed by design, not subject to production testing.
3. Independent of operating temperature

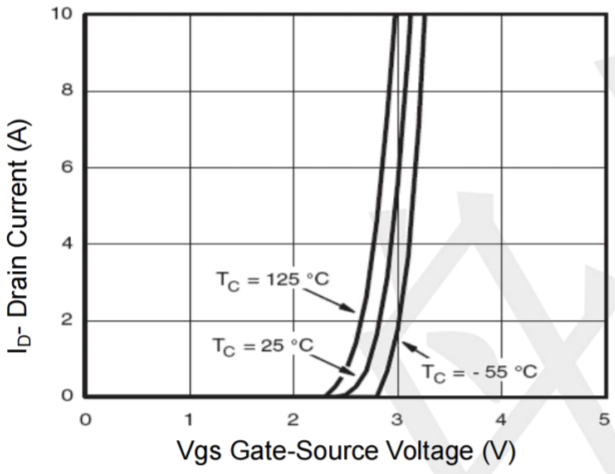
TYPICAL CHARACTERISTICS (25 °C, unless otherwise noted)



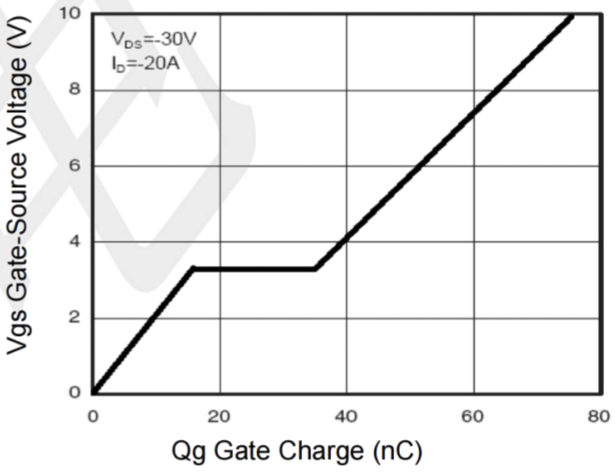
Output Characteristics



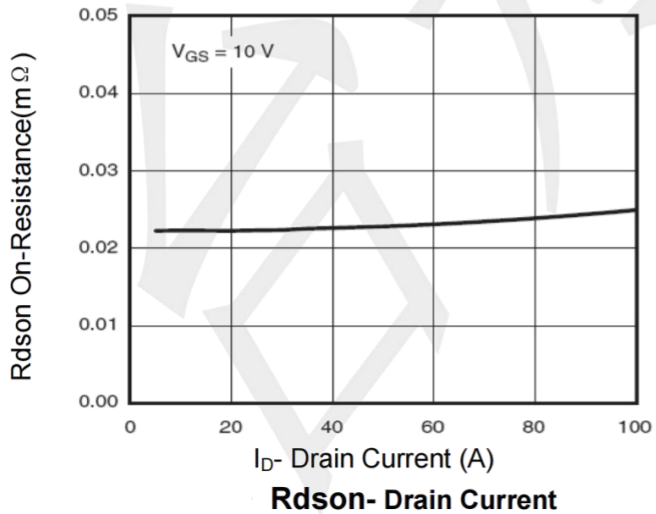
Rdson-Junction Temperature



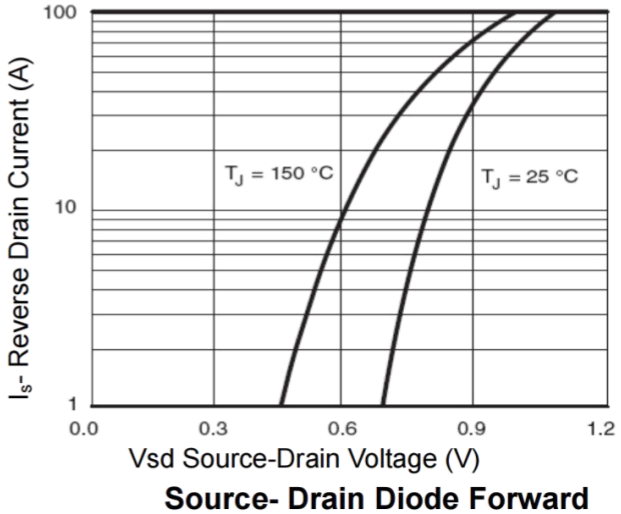
Transfer Characteristics



Gate Charge



Rdson- Drain Current



Source- Drain Diode Forward

Package Outline Dimensions (unit: mm)

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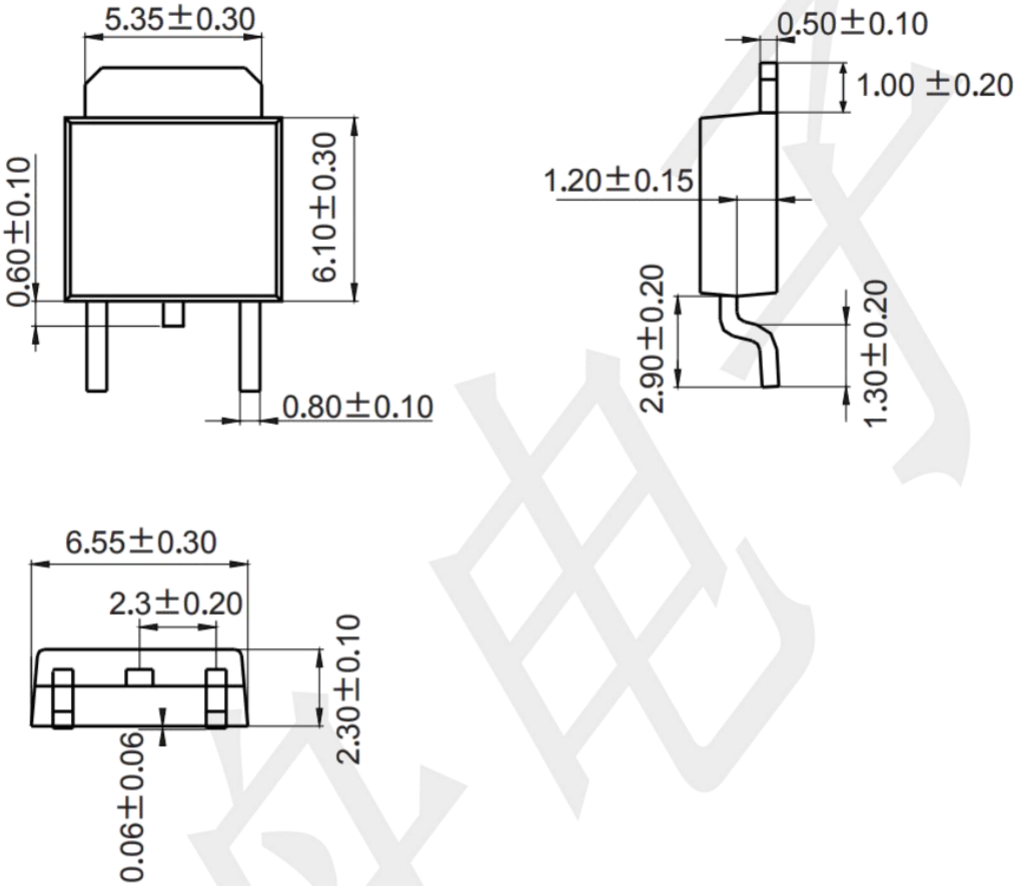

Mounting Pad Layout (unit: mm)