JLHF450B120E62E7DN

L62 PACK module with Gen7 IGBT and Emitter Controlled 7 diode

RoHS

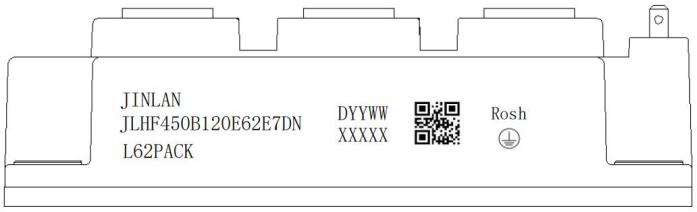
Features

- 1200V Trench Stop IGBTs
- $T_{vj\ op}$=150℃
- V_{CEsat} with positive temperature coefficient
- 10μs short circuit capability
- For higher switching frequencies up to 12kHz
- Standard housing
- 4 kV AC 1 min insulation
- Low inductance case
- Insulated copper baseplate using DBC technology
- High creepage and clearance distances

L62 Pack

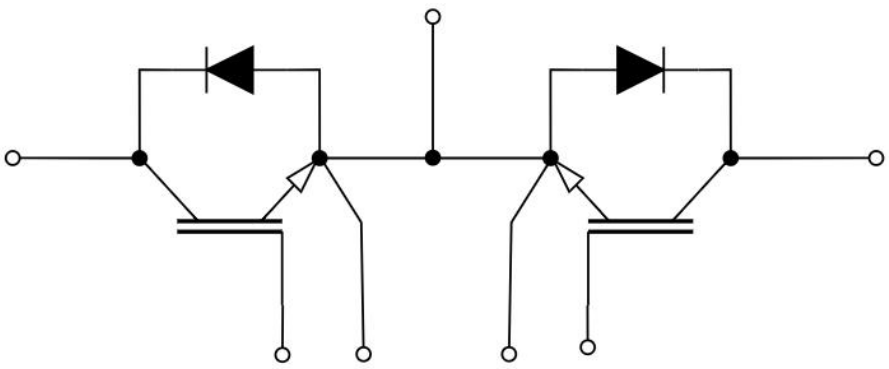
Typical Applications

- Matrix Inverter
- Bidirectional switch



JINLAN	= Company Name
JLHF450B120E62E7DN	= Specific Device Code
YYWW	= Year and Work Week Code
XXXXX	= Serial Number
QR code	= Custom Assembly Information

Description



Package Insulation coordination

Parameter	Symbol	Note or test condition	Values	Unit
Isolation test voltage	V_{ISOL}	RMS,f=50Hz,t=60s	4.0	kV
Internal isolation		basic insulation(class 1,IEC 61140)	Al_2O_3	
Creepage distance	d_{creep}	terminal to heatsink	29.0	mm
Creepage distance	d_{creep}	terminal to terminal	23.0	mm
Clearance	d_{clear}	terminal to heatsink	23.0	mm
Clearance	d_{clear}	terminal to terminal	11.0	mm
Comparative tracking index (electrical)	CTI		>500	
RTI Elec.	RTI	housing	140	℃

Package Characteristic values

Parameter	Symbol	Note or test condition		Values			Unit
				Min.	Typ.	Max.	
Stray Inductance	L_{CE}			--	20	--	nH
Module Lead Resistance, Terminal to Chip	$R_{CC'+EE'}$	T_C=25°C, per switch		--	0.5	--	mΩ
Storage Temperature Range	T_{STG}			-40		125	℃
M	Mounting torque for module mounting	-Mounting according to valid application note	M5, Screw	3.0	--	6.0	Nm
M	Terminal connection torque	-Mounting according to valid application note	M6, Screw	2.5	--	5.0	Nm
Weight	G			--	340	--	g

IGBT

Absolute Maximum Ratings (Tc = 25°C unless otherwise noted)

Symbol	Description	Note or test condition		Value	Unit
V_{CES}	Collector-Emitter Voltage	T_{vj} = 25 °C		1200	V
I_{CDC}	Continuous DC collector current	$T_{vj\ max}$ = 175 °C	T_C = 90 °C	450	A
I_{CRM}	Repetitive peak collector current	t_p limited by $T_{vj\ op}$		900	A
V_{GES}	Gate-emitter peak voltage			±30	V

Characteristics (Tc = 25°C unless otherwise noted)

Symbol	Parameter	Test Condition		Min	Typ	Max	Unit
$V_{CE(sat)}$	Collector−Emitter Saturation Voltage	I_C =450 A, V_{GE} = 15 V	T_{vj} = 25 °C	--	1.45	1.95	V
			T_{vj} = 150 °C	--	1.70	--	
			T_{vj} = 175 °C	--	1.75	--	
$V_{GE(TH)}$	Gate−Emitter Threshold Voltage	V_{GE} = V_{CE}, I_C = 5 mA, T_{vj} = 25°C		5.0	5.8	6.5	V
I_{CES}	Collector−Emitter Cutoff Current	V_{GE} = 0 V, V_{CE} =1200V, T_{vj}= 25°C		--	--	100	uA
I_{GES}	Gate-Emitter Leakage Current	V_{GE} = ±30V, V_{CE} = 0 V, T_{vj} = 25°C		--	--	±100	nA
R_{Gint}	Internal Gate Resistance	T_{vj} = 25 °C		--	1.34	--	Ω
C_{ies}	Input Capacitance	f = 100 kHz, T_{vj} = 25 °C, V_{CE} = 25 V, V_{GE} = 0 V		--	55.4	--	nF
C_{oes}	Out Capacitance			--	1.3	--	nF
C_{res}	Reverse Transfer Capacitance			--	0.19	--	nF
Q_G	Gate Charge	V_{GE} = ±15 V, V_{CC} = 600 V		--	1.80	--	μC
$t_{d(on)}$	Turn−On Delay Time	I_C = 450A, V_{CC} = 600 V, V_{GE} = 15 /-5V, R_G = 1.0 Ω	T_{vj} = 25 °C	--	0.331	--	μS
			T_{vj} = 150 °C	--	0.375	--	
			T_{vj} = 175 °C	--	0.380	--	
t_r	Rise Time	I_C =450A, V_{CC} = 600 V, V_{GE} = 15 /-5V, R_G = 1.0 Ω	T_{vj} = 25 °C	--	0.060	--	μS
			T_{vj} = 150 °C	--	0.072	--	
			T_{vj} = 175 °C	--	0.073	--	
$t_{d(off)}$	Turn−off Delay Time	I_C = 450 A, V_{CC} = 600 V, V_{GE} = 15 /-5V, R_{Goff} = 1.0 Ω	T_{vj} = 25 °C	--	0.672	--	μS
			T_{vj} = 150 °C	--	0.766	--	
			T_{vj} = 175 °C	--	0.782	--	
t_f	Fall Time	I_C = 450 A, V_{CC} = 600 V, V_{GE} = 15 /-5V, R_G = 1.0 Ω	T_{vj} = 25 °C	--	0.082	--	μS
			T_{vj} = 150 °C	--	0.183	--	
			T_{vj} = 175 °C	--	0.186	--	
E_{on}	Turn−On Switching Loss per Pulse	I_C =450A, V_{CC} = 600 V, V_{GE} = 15 /-5V, R_G = 1.0 Ω ($T_{vj\ max}$= 175 °C)	T_{vj} = 25 °C	--	10.14	--	mJ
			T_{vj} = 150 °C	--	20.21	--	
			T_{vj} = 175 °C	--	22.49	--	
E_{off}	Turn Off Switching Loss per Pulse	I_C = 450A, V_{CC} = 600 V, V_{GE} = 15 /-5V, R_G = 1.0 Ω ($T_{vj\ max}$= 175 °C)	T_{vj} = 25 °C	--	44.05	--	mJ
			T_{vj} = 150 °C	--	58.68	--	
			T_{vj} = 175 °C	--	60.63	--	

I_{SC}	SC Data	$V_{GE} \le 15$ V, V_{CC} = 800 V, $V_{CEmax}=V_{CES}-L_{sCE}.di/dt$	$t_P \le 10$ µs, T_{vj}=150 °C	--	1630	--	A
			$t_P \le 10$ µs, T_{vj}=175 °C	--	1533	--	
R_{thJC}	Thermal resistance	Junction-to-Case (per IGBT)		--	0.0787	0.088	K/W
$T_{vj\ op}$	Temperature under switching conditions			-40	--	175[1)]	℃

[1)] $T_{vj\ op}$ > 175℃ is only allowed for operation at overload conditions. For detailed specifications please refer to AN 2018-14.

Diode

Absolute Maximum Ratings (Tc = 25°C unless otherwise noted)

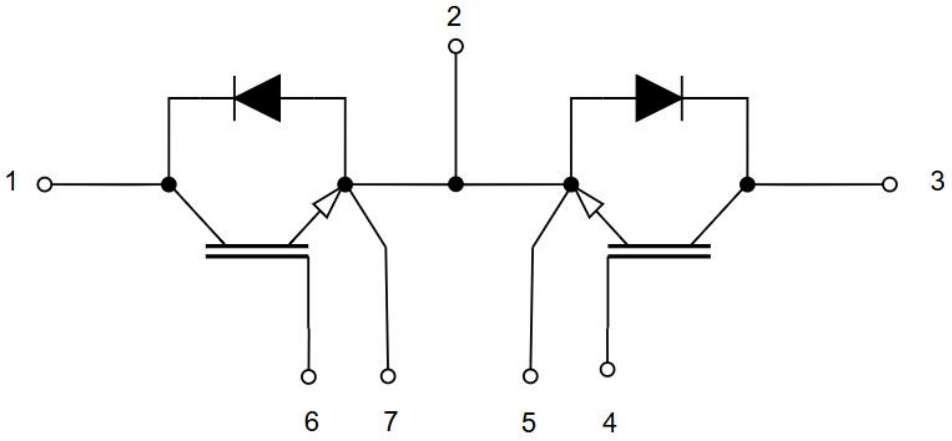
Symbol	Description	Note or test condition	Value	Unit
V_{RRM}	Repetitive peak reverse voltage	T_{vj} = 25 °C	1200	V
I_F	Continuous DC forward current		450	A
I_{FRM}	Repetitive peak forward current	t_P = 1 ms	900	A

Characteristics （Tc=25℃ unless otherwise noted）

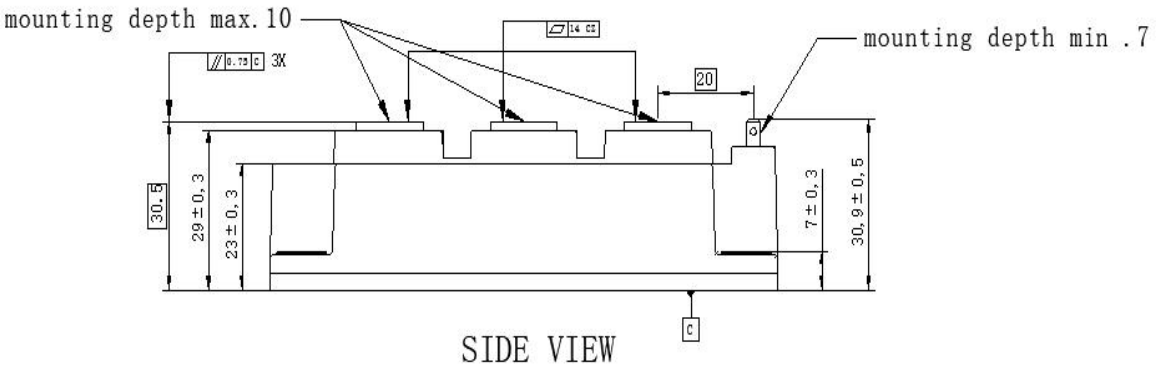
Symbol	Parameter	Test Condition		Value			Unit
				Min	Typ	Max	
V_F	Diode Forward Voltage	I_F = 450 A, V_{GE} = 0 V	T_{vj} = 25 °C	--	2.00	2.80	V
			T_{vj} = 150 °C	--	1.95	--	
			T_{vj} = 175 °C	--	1.85	--	
Q_r	Recovered Charge	I_C = 450 A, V_{CC} = 600 V, V_{GE} = 15 /-5V, R_G = 1.0 Ω ($T_{vj\ max}$= 175 °C)	T_{vj} = 25 °C	--	7.58	--	µC
			T_{vj} = 150 °C	--	24.97	--	
			T_{vj} = 175 °C	--	30.52	--	
I_{RM}	Peak Reverse Recovery Current	I_C = 450A, V_{CC} = 600 V, V_{GE} = 15 /-5V, R_G = 1.0 Ω ($T_{vj\ max}$= 175 °C)	T_{vj} = 25 °C	--	259	--	A
			T_{vj} = 150 °C	--	325	--	
			T_{vj} = 175 °C	--	342	--	
E_{rec}	Reverse recovery energy	I_C = 450 A, V_{CC} = 600 V, V_{GE} = 15 /-5V, R_G = 1.0 Ω ($T_{vj\ max}$= 175 °C)	T_{vj} = 25 °C	--	8.14	--	mJ
			T_{vj} = 150 °C	--	22.2	--	
			T_{vj} = 175 °C	--	25.9	--	
R_{thJC}	Thermal resistance, junction to case	per diode		--	0.103	0.116	K/W
$T_{vj\ op}$	Temperature under switching conditions			-40	--	175[2)]	℃

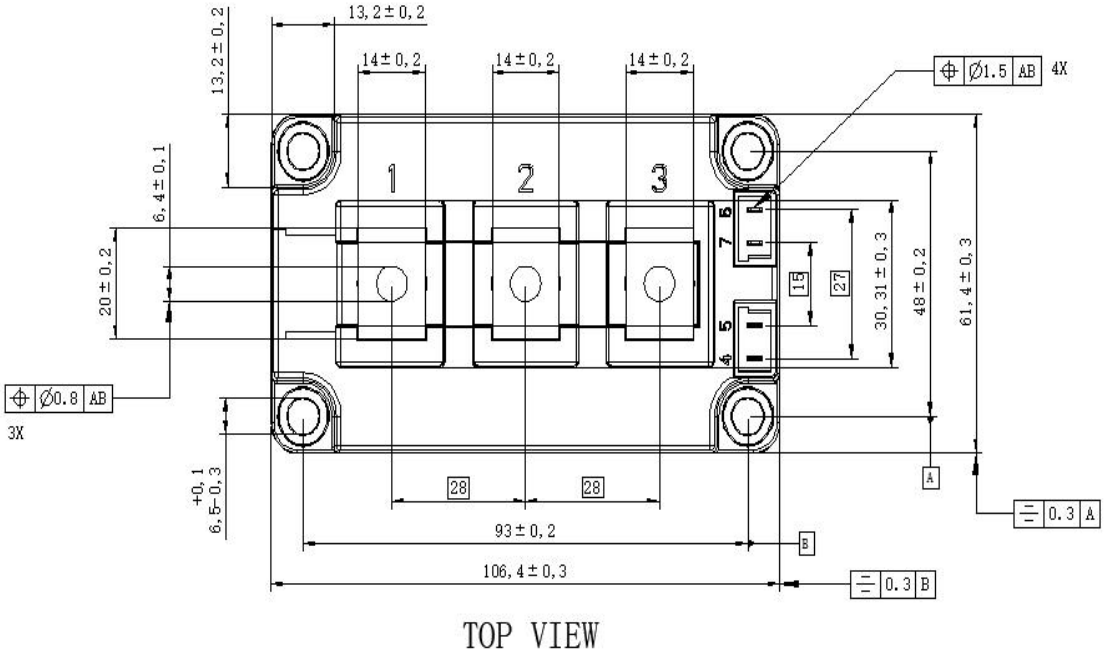
[2)] $T_{vj\ op}$ > 175℃ is only allowed for operation at overload conditions. For detailed specifications please refer to AN 2018-14.

CIRCUIT DIAGRAM



PACKAGE DIMENSION





REVISION HISTORY

Document version	Date of release	Description of changes
Rev.00	2024-05-31	Preliminary Data

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