JLPI50B120RE2F7SN

LE2 PACK module with Gen.7 Trench/Fieldstop IGBT and Emitter Controlled diode and NTC

RoHS

Features

• Electrical features

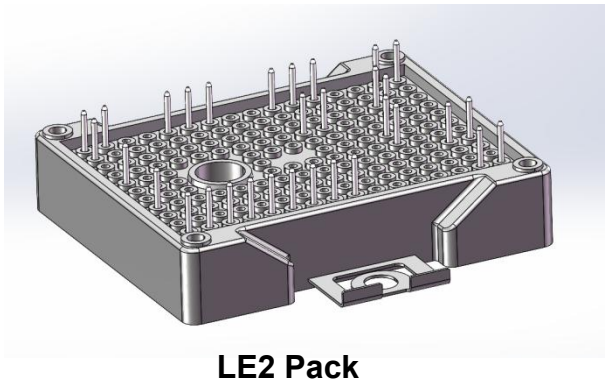
- V_{CES} = 1200 V
- $I_{C\ nom}$ = 50 A / I_{CRM} = 100 A
- Low V_{CEsat}

-10μs short circuit capability

- Overload operation up to 175°C

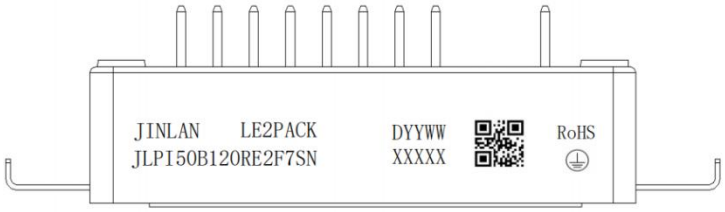
• Mechanical features

- Integrated NTC temperature sensor
- Isolated heatsink using DBC technology
- Low inductance case
- Solder contact technology



LE2 Pack

MARKING DIAGRAM



JINLAN	= Company Name
JLPI50B120RE2F7SN	= Specific Device Code
YYWW	= Year and Work Week Code
XXXXX	= Serial Number
QR code	= Custom Assembly Information

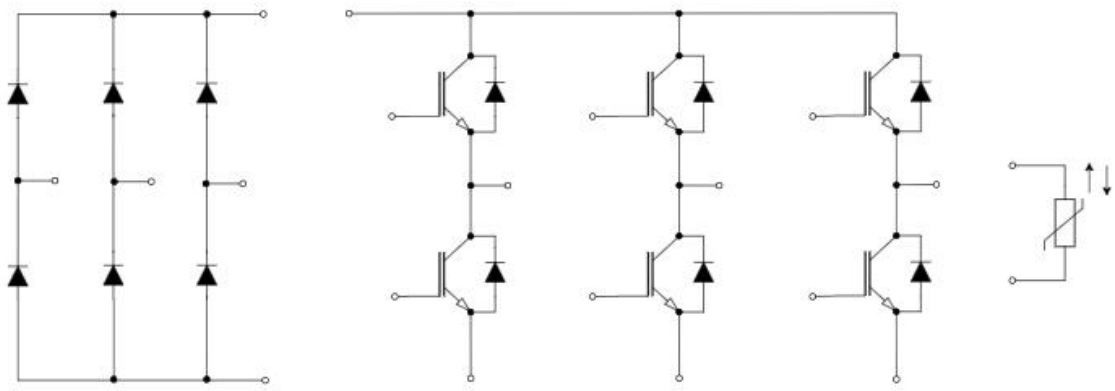
Typical Applications

• Auxiliary inverters

• Motor drives

• Servo drives

Description



Package Insulation coordination

Parameter	Symbol	Note or test condition	Values	Unit
Isolation test voltage	V_{ISOL}	RMS,f=50Hz,t=60s	2.5	kV
Internal isolation		basic insulation(class 1,IEC 61140)	Al_2O_3	
Creepage distance	d_{creep}	terminal to heatsink	11.5	mm
Clearance	d_{clear}	terminal to heatsink	10	mm
Comparative tracking index (electrical)	CTI		>200	
RTI Elec.	RTI	housing	140	℃

Package Characteristic values

Parameter	Symbol	Note or test condition	Values			Unit
			Min.	Typ.	Max.	
Stray Inductance	LCE		--	35	--	nH
Module Lead Resistance, Terminal to Chip	$R_{AA'+CC'}$	T_C=25℃, per switch	--	6.0	--	mΩ
Module Lead Resistance, Terminal to Chip	$R_{CC'+EE'}$	T_C=25℃, per switch	--	5.0	--	mΩ
Storage Temperature Range	T_{STG}		-40		125	℃
Mounting Torque	F	M5, Screw	40		80	N
Weight	G		--	39	--	g

IGBT, Inverter

Absolute Maximum Ratings (Tc = 25°C unless otherwise noted)

Symbol	Description	Value	Unit
V_{CES}	Collector-Emitter Voltage	1200	V
V_{GES}	Gate-Emitter Voltage	±30	V
I_C	Collector Current @ T_C=25℃	100	A
	Collector Current @ T_C=80℃	50	A
I_{CM}	Pulsed Collector Current，t_p=1s	100	A
Tjmax	Maximum Junction Temperature	175	℃

Characteristics (Tc = 25°C unless otherwise noted)

Symbol	Parameter	Test Condition	Min	Typ	Max	Unit
$V_{CE(sat)}$	Collector−Emitter Saturation Voltage	V_{GE} = 15 V, I_C = 50 A, T_{vj} = 25°C	--	1.80	2.20	V
		V_{GE} = 15 V, I_C = 50 A, T_{vj} = 125°C	--	1.95	--	
		V_{GE} = 15 V, I_C = 50 A, T_{vj} = 150°C	--	2.00	--	
$V_{GE(TH)}$	Gate−Emitter Threshold Voltage	V_{GE} = V_{CE} , I_C = 1 mA,T_{vj} = 25°C	5.0	6.0	7.0	V
I_{CES}	Collector−Emitter Cutoff Current	V_{GE} = 0 V, V_{CE} =V_{CES} ,T_{vj} = 25°C	--	--	200	μA
I_{GES}	Gate-Emitter Leakage Current	V_{GE} = V_{GES} , V_{CE} = 0 V， T_{vj} = 25°C	--	--	200	nA
RGint	Internal Gate Resistance		--	1.27	--	Ω
Cies	Input Capacitance	V_{CE}=25V,f=1MHz, V_{GE}=0V	--	5.07	--	nF
Cres	Reverse Transfer		--	0.13	--	nF
Q_G	Gate Charge	V_{GE}=15V, I_C=50A	--	0.19	--	μC
td(on)	Turn−On Delay Time	V_{CC} = 600 V, I_C = 50 A R_{Gon} = 10Ω,R_{Goff} = 25Ω ,V_{GE} =±15V Inductive Load T_{vj} = 25°C	--	110	--	ns
tr	Rise Time		--	30	--	
td(off)	Turn−off Delay Time		--	180	--	
tf	Fall Time		--	190	--	
Eon	Turn−On Switching Loss per Pulse		--	2.83	--	mJ
Eoff	Turn Off Switching Loss per Pulse		--	4.42	--	
td(on)	Turn−On Delay Time	V_{CC} = 600 V, I_C = 50 A R_{Gon} = 10Ω,R_{Goff} = 25Ω ,V_{GE} =±15V Inductive Load T_{vj} = 125°C	--	120	--	ns
tr	Rise Time		--	30	--	
td(off)	Turn−off Delay Time		--	230	--	
tf	Fall Time		--	340	--	
Eon	Turn−on Switching Loss per Pulse		--	4.16	--	mJ
Eoff	Turn Off Switching Loss per Pulse		--	5.24	--	
td(on)	Turn−On Delay Time	V_{CC} = 600 V, I_C = 50 A R_{Gon} = 10Ω,R_{Goff} = 25Ω ,V_{GE} =±15V Inductive Load T_{vj} = 150°C	--	110	--	ns
tr	Rise Time		--	40	--	
td(off)	Turn−off Delay Time		--	240	--	
tf	Fall Time		--	330	--	
Eon	Turn−on Switching Loss per Pulse		--	4.87	--	mJ
Eoff	Turn Off Switching Loss per Pulse		--	5.49	--	
ISC	SC Data	t_P≤10μs,V_{GE} ≤15V, T_{vj}=150℃,V_{CC}=800V, V_{CEmax}=V_{CES} -L_{sCE}*di/dt	--	130	--	A
RthJC	Thermal resistance	Junction-to-Case (per IGBT)	--	0.45	--	K/W
$T_{vj\ op}$		Temperature under switching conditions	-40		175[1)]	℃

[1)] T_{vjop} > 150℃ is only allowed for operation at overload conditions. For detailed specifications please refer to AN 2018-14.

Diode, Inverter

Absolute Maximum Ratings (Tc = 25°C unless otherwise noted)

Symbol	Description	Value	Unit
V_{RRM}	Repetitive Peak Reverse Voltage	1200	V
I_F	Diode Continuous Forward Current	50	A
I_{FM}	Diode Maximum Forward Current t_p=1ms	150	A

Characteristics (Tc=25℃ unless otherwise noted)

Symbol	Parameter	Test Condition		Min	Typ	Max	Unit
V_F	Diode Forward Voltage	I_F=50A	T_j=25°C	--	1.80	2.70	V
			T_j=175°C	--	1.75	--	
Q_r	Recovered Charge	I_F=50A,R_g=25Ω, T_j=25°C		--	1.06	--	μC
I_{RM}	Peak Reverse Recovery Current			--	39.5	--	A
T_{rr}	Reverse Recovery Time			--	126	--	ns
E_{rec}	Reverse Recovery Energy			--	0.44	--	mJ
Q_r	Recovered Charge	I_F=50A,R_g=25Ω, T_j=175°C		--	TBD	--	μC
I_{RM}	Peak Reverse Recovery Current			--	TBD	--	A
T_{rr}	Reverse Recovery Time			--	TBD	--	ns
E_{rec}	Reverse Recovery Energy			--	TBD	--	mJ
R_{thJC}	Thermal resistance	Junction-to-Case (per diode)		--	0.6	--	K/W
$T_{vj\ op}$		Temperature under switching conditions		-40		175[2)]	℃

[2)] $T_{vj\ op}$ > 150℃ is only allowed for operation at overload conditions. For detailed specifications please refer to AN 2018-14.

Diode, Rectifier

Absolute Maximum Ratings (Tc = 25°C unless otherwise noted)

Symbol	Description	Value	Unit
V_{RRM}	Repetitive Peak Reverse Voltage	1600	V
I_F	Diode Continuous Forward Current	50	A
I_{FM}	Diode Maximum Forward Current t_p=1ms	100	A

Characteristics (Tc=25℃ unless otherwise noted)

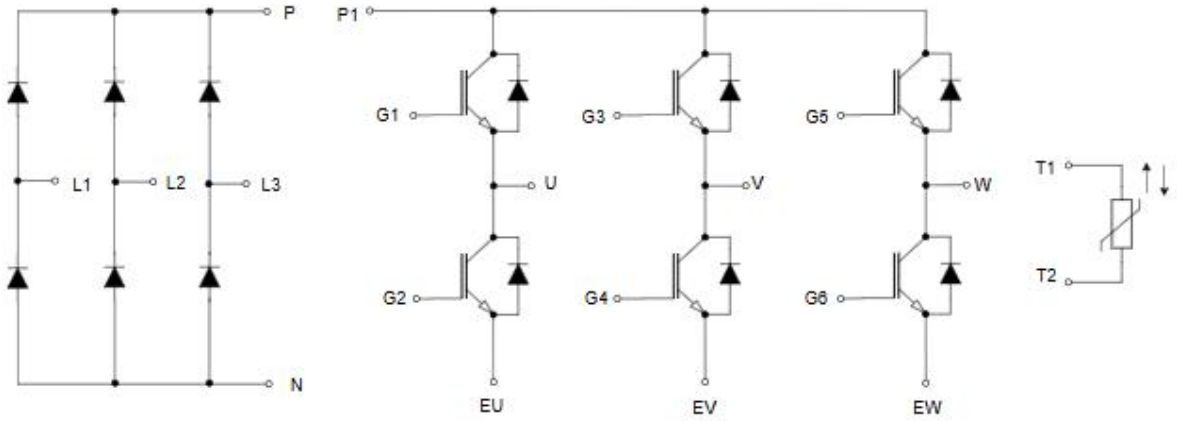
Symbol	Parameter	Test Condition	Min	Typ	Max	Unit
V_F	Diode Forward Voltage	I_F = 50 A, T_j = 150 °C	--	1.0	--	V
I_R	Reverse Current	T_j=175 °C, V_R=1600V	--	3	--	mA
R_{thJC}	Thermal resistance	Junction-to-Case (per diode)		0.53		K/W
$T_{vj\ op}$		Temperature under switching conditions	-40		175[3)]	℃

[3)] $T_{vj\ op}$ > 150℃ is only allowed for operation at overload conditions. For detailed specifications please refer to AN 2018-14.

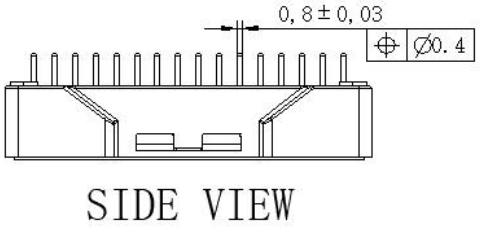
NTC Characteristics (Tc = 25°C unless otherwise noted)

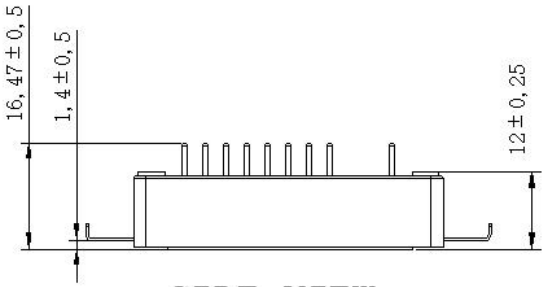
Symbol	Parameter	Test Condition	Min	Typ	Max	Unit
R_{25}	Rated Resistance		--	5.0	--	kΩ
ΔR/R	Deviation of R100	T_C=100 ℃,R_{100}=493.3Ω	-5	--	5	%
P_{25}	Power Dissipation		--	--	20.0	mW
$B_{25/50}$	B-value	$R_2=R_{25}\exp[B_{25/50}(1/T_2- 1/(298.15K))]$	--	3375	--	K
$B_{25/80}$	B-value	$R_2=R_{25}\exp[B_{25/80}(1/T_2- 1/(298.15K))]$	--	3411	--	K
$B_{25/100}$	B-value	$R_2=R_{25}\exp[B_{25/100}(1/T_2- 1/(298.15K))]$	--	3433	--	K

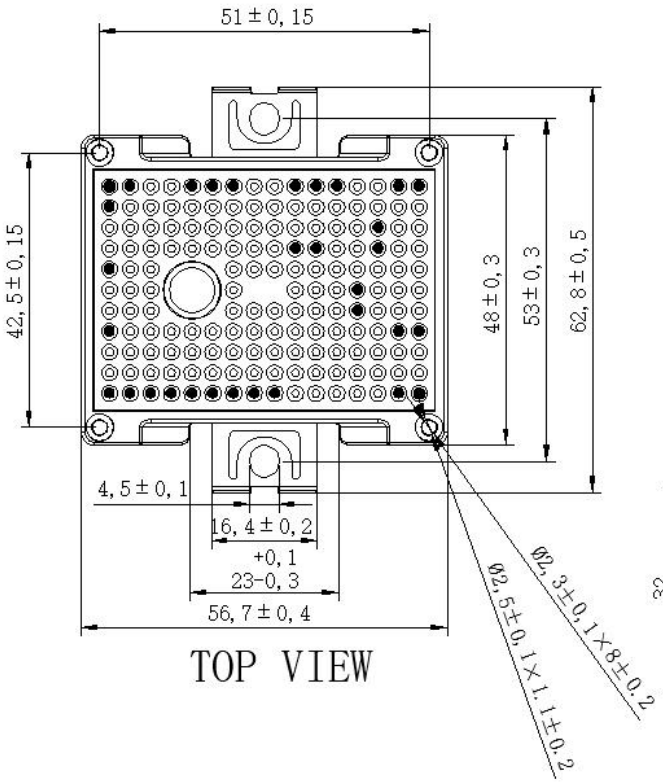
CIRCUIT DIAGRAM

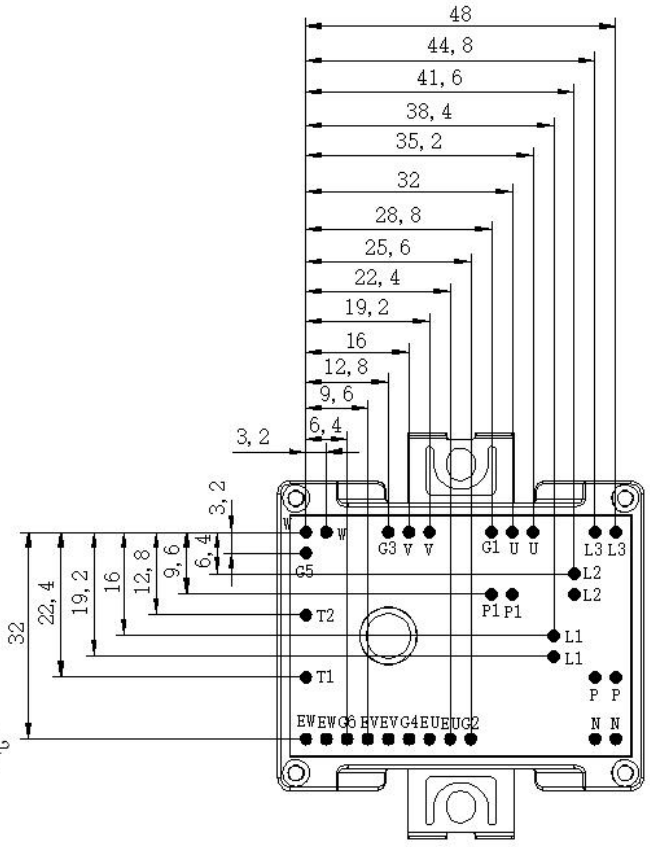


PACKAGE DIMENSION









REVISION HISTORY

Document version	Date of release	Description of changes
Rev.00	2024-12-11	Preview
Rev.01	2025-01-07	Features content modifications

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