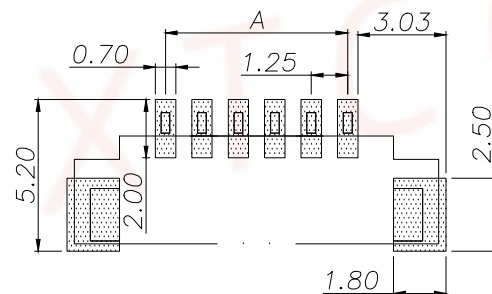
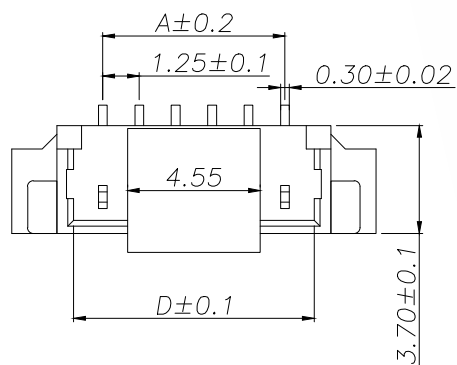
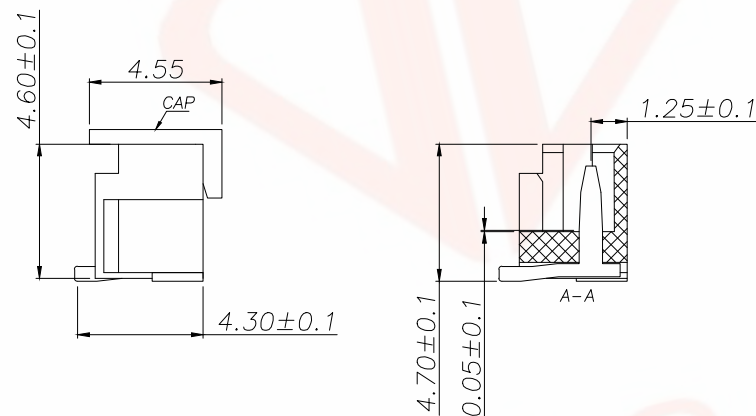
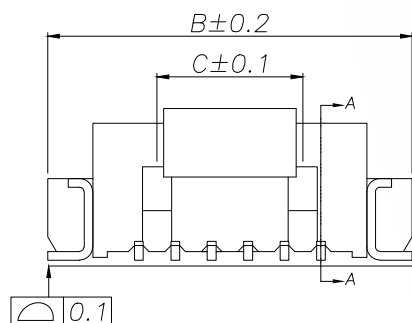


1. 塑件表面应光洁、无毛边、无明显收缩、缺陷、裂纹等现象。
2. 锁扣窗口: 2~3PIN为1个, ≥ 4 PIN为2个。
3. 不同PIN位吸盖加入/取出力有差异, 实属正常现象。



P.C.B LAYOUT

REV.	ECN NO.	DESCRIPTION	DATE	DESIGN

NOTES:

- 1.BASE: LCP
- 2.PIN: Brass
- 3.Solder Tab: Brass

SPECIFICATIONS:

- 1.Spacing: 1.25
- 2.Voltage Rating: 125V AC DC
- 3.Current Rating: 1A AC DC
- 4.Insulation Resistance: 100M Ω Min
- 5.Contact Resistance: 20m Ω Max
- 6.Withstanding voltage: 500V AC 1/Minute
- 7.Temperatur Range: -25 $^{\circ}$ ~+ 85 $^{\circ}$

Part No	PIN	A	B	C	D
DMX-2PLT-43	2	1.25	7.50		3.25
DMX-3PLT-43	3	2.50	8.75		4.50
DMX-4PLT-43	4	3.75	10.00	2.50	5.75
DMX-5PLT-43	5	5.00	11.25	3.75	7.00
DMX-6PLT-43	6	6.25	12.50	5.00	8.25
DMX-7PLT-43	7	7.50	13.75	6.25	9.50
DMX-8PLT-43	8	8.75	15.00	7.50	10.75
DMX-9PLT-43	9	10.00	16.25	8.75	12.00
DMX-10PLT-43	10	11.25	17.50	10.00	13.25
DMX-11PLT-43	11	12.50	18.75	11.25	14.50
DMX-12PLT-43	12	13.75	20.00	12.50	15.75
DMX-13PLT-43	13	15.00	21.25	13.75	17.00
DMX-14PLT-43	14	16.25	22.50	15.00	18.25
DMX-15PLT-43	15	17.50	23.75	16.25	19.50
DMX-16PLT-43	16	18.75	25.00	17.50	20.75

TOLERANCE			 深圳聚兴泰电子科技有限公司 Shenzhen Juxingtai Electronic Technology Co., Ltd				
X.X	±0.30	X.*	±5°	PART NO.	TITLE:		
.XX	±0.20	X.X*	±3°	DMX-XPLT-43	WAFER 1.25 立贴		
.XXX	±0.10	X.XX*	±1°	DESIGN: Ethan	MANUFACTURE DWG		
UNITS	mm		CHED: Olvia		SCALE	SHEET	
MATERILA	— — —		APPD: Ava		N:A	1/1	