



PRODUCT DATA SHEET



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Datasheet

es Samples

Please note: Please check the JINGAO Semiconductor website to verify the updated device numbers. The most current and up-to-date ordering information can be found at www.jg-semi.cn. Please email any questions regarding the system integration to JINGAO_questions@jgsemi.com.

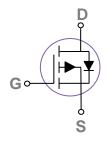


General Description

These P-Channel enhancement mode power field effect transistors are using trench DMOS technology. This advanced technology has been especially tailored to minimize on-state resistance, provide superior switching performance, and withstand high energy pulse in the avalanche and commutation mode. These devices are well suited for high efficiency fast switching applications.

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BVDSS	RDSON	ID
-30V	20m $Ω$	-9A

Features

- -30V, -9A, $RDS(ON) = 20m\Omega@VGS = -10V$
- Fast switching
- Green Device Available
- Suit for -4.5V Gate Drive Applications

Applications

- MB / VGA / Vcore
- POL Applications
- Load Switch
- LED Application

Absolute Maximum Ratings Tc=25°C unless otherwise noted

Symbol	Parameter	Rating	Units
V _{DS}	Drain-Source Voltage	-30	V
V _G s	Gate-Source Voltage	±20	V
I_	Drain Current – Continuous (T _A =25°C)	-9.0	А
lD	Drain Current – Continuous (T _A =70°C)	-4.4	А
I _{DM}	Drain Current – Pulsed1	-22	А
В	Power Dissipation (T _A =25°C)	1.25	W
P _D	Power Dissipation – Derate above 25°C	0.01	W/°C
T _{STG}	Storage Temperature Range	-55 to 150	°C
TJ	Operating Junction Temperature Range	-55 to 150	°C

Thermal Characteristics

Symbol	Symbol Parameter		Max.	Unit
R ₀ JA	R _{BJA} Thermal Resistance Junction to ambient			°C/W



Electrical Characteristics (T_J=25 °C, unless otherwise noted)

Off Characteristics

Symbol	Parameter	Conditions	Min.	Тур.	Max.	Unit
BV _{DSS}	Drain-Source Breakdown Voltage V _{GS} =0V , I _D =-250uA		-30			٧
△BV _{DSS} /△T _J	BV _{DSS} Temperature Coefficient	S Temperature Coefficient Reference to 25°C , ID=-1mA		-0.03		V/°C
I _{DSS}	Drain-Source Leakage Current	V _{DS} = - 30V , V _{GS} =0V , T _J =25°C			-1	uA
	Diain-Source Leakage Current	V _{DS} =-24V , V _{GS} =0V , T _J =125°C			-10	uA
Igss	Gate-Source Leakage Current	$V_{GS}=\pm 20V$, $V_{DS}=0V$			±100	nA

On Characteristics

R _{DS(ON)} Static Drain-Source On-Resistance	Static Drain Source On Registance	V _{GS} =-10V , I _D =-4A		20	25	mΩ
	V _{GS} =-4.5V , I _D =-2A		26	36	mΩ	
$V_{GS(th)}$	Gate Threshold Voltage		-1.0	-1.6	- 2.5	V
$\triangle V_{GS(th)}$	V _{GS(th)} Temperature Coefficient	V _{GS} =V _{DS} , I _D = - 250uA		4		mV/°C
gfs	Forward Transconductance	V _{DS} =-10V , I _D =-3A		6.8		S

Dynamic and switching Characteristics

Qg	Total Gate Charge ^{2,3}		 24.5	
Qgs	Gate-Source Charge ^{2,3}	V _{DS} =-15V , V _{GS} =-10V , I _D =-3A	 3.4	nC
Q_gd	Gate-Drain Charge ^{2, 3}		 4.2	
T _{d(on)}	Turn-On Delay Time ^{2,3}		 5.8	
Tr	Rise Time ^{2, 3}	V_{DD} =-15 V , V_{GS} =-10 V , R_{G} =6 Ω	 18.8	no
$T_{d(off)}$	Turn-Off Delay Time ^{2,3}	I _D =-1A	 46.9	ns
Tf	Fall Time ^{2, 3}		 12.3	
Ciss	Input Capacitance		 1250	
Coss	Output Capacitance	V _{DS} =-15V , V _{GS} =0V , F=1MHz	 160	pF
Crss	Reverse Transfer Capacitance		 90	

Drain-Source Diode Characteristics and Maximum Ratings

Symbol	Parameter	Conditions		Тур.	Max.	Unit
ls	Continuous Source Current	V- V- OV Force Current			-9	Α
Ism	Pulsed Source Current	V _G =V _D =0V , Force Current			-11	Α
V_{SD}	Diode Forward Voltage	V _{GS} =0V , I _S =-1A , T _J =25°C			-1.2	V

Note:

- 1. Repetitive Rating : Pulsed width limited by maximum junction temperature.
- 2. The data tested by pulsed , pulse width \leq 300us , duty cycle \leq 2%.
- 3. Essentially independent of operating temperature.



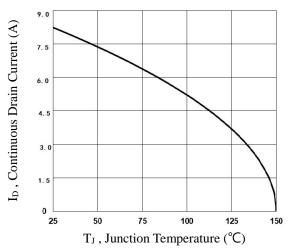


Fig.1 Continuous Drain Current vs. T_J

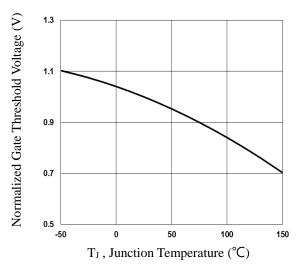


Fig.3 Normalized V_{th} vs. T_J

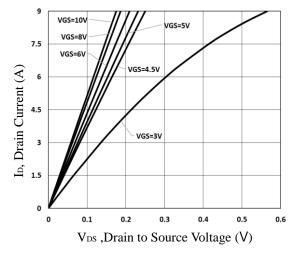


Fig.5 Typical Output Characteristics

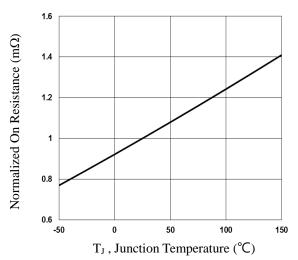


Fig.2 Normalized RDSON vs. T_J

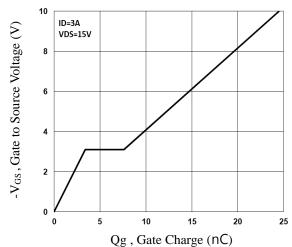


Fig.4 Gate Charge Waveform

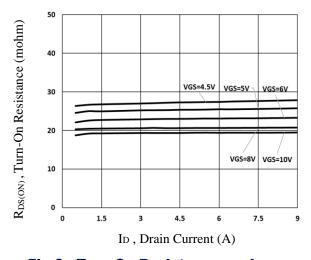


Fig.6 Turn-On Resistance vs. ID

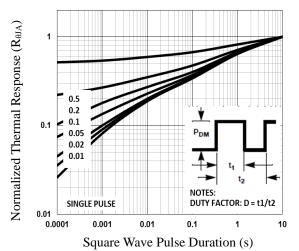


Fig.7 Normalized Transient Impedance

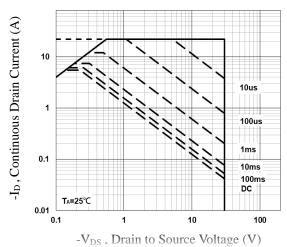


Fig.8 Maximum Safe Operation Area

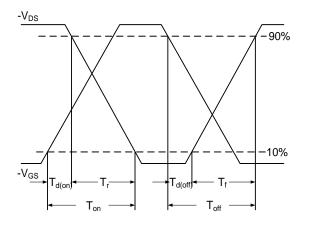


Fig.9 Switching Time Waveform

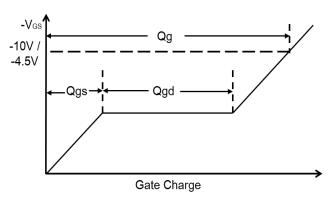
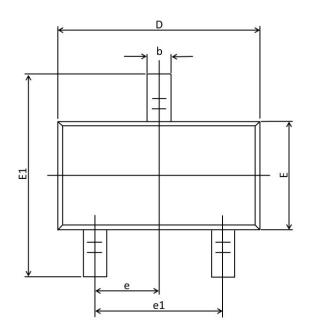
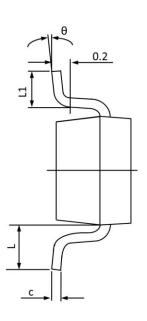


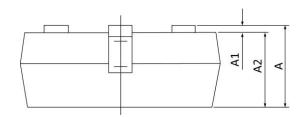
Fig.10 Gate Charge Waveform



SOT23-3 PACKAGE INFORMATION







Crombal	Dimensions I	n Millimeters	Dimension	s In Inches
Symbol	Min	Max	Min	Max
A	1.050	1.250	0.041	0.049
A1	0.000	0.100	0.000	0.004
A2	1.050	1.150	0.041	0.045
b	0.300	0.400	0.012	0.016
c	0.100	0.200	0.004	0.008
D	2.820	3.020	0.111	0.119
E	1.500	1.700	0.059	0.067
E 1	2.650	2.950	0.104	0.116
e	0.950 TYP.		0.037	TYP.
e1	1.800	2.000	0.071	0.079
L	0.700 REF. 0.02			REF.
L1	0.300	0.600	0.012	0.024
heta	0 °	8°	0 °	8°



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