



MT29C8G96MAZBADKD-5 IT

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Specs

Production			
Part Status Code	Production	NAND Density	8Gb
Width	x16	LPDDR Density	4Gb
LPDDR Density		Secondary Width	x32
RoHS	Yes	Voltage	1.7V-1.9V
Package	TFBGA	Pin Count	137-ball
Clock Rate	200 MHz	Op Temp	-40C to +85C
Pinout	JEDEC	Platform	

- **FBGA Code:** JW910
- **SPD Data:** [none](#)
- **Validated Chipsets:** [none](#)

Data Sheets (1)

137-Ball NAND Flash with LPDDR MCP

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FAQs

- » [Can I use a discrete package with the OMAP35x?](#)
- » [Do I use only Mobile LPDDR with the OMAP35x, or can I use a standard SDR/DDR/DDR2/DDR3 part?](#)
- » [Do you have any reliability data on the PoP?](#)
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