APPLICA	BLE STANI	DARD						
OPERATING TEMPERATU		E RANGE	-45°C TO +125°C(NOTES 1)	- 1	RAGE IPERATURE RANGE	-10°C TO + 60°C (NOTE		<u>?</u> )
RATING	VOLTAGE CURRENT				LICABLE	DF9#-*P-1V(22) DF9#-*P-1V(32)		
					INECTOR			
	•		SPECIFICA	TIO	NS			
l-	ГЕМ		TEST METHOD		REQUIREMENTS		QT	АТ
CONSTR	RUCTION				•			
GENERAL EX	AMINATION	VISUALLY	AND BY MEASURING INSTRUMENT.		ACCORDING TO D	DRAWING.	Х	Х
MARKING		CONFIRMED VISUALLY.						X
ELECTR	IC CHARA	CTERI	STICS		1			
CONTACT RESISTANCE					50mΩ MAX.			
INSULATIO	N	100	V DC.		500MΩ MIN.		X	
RESISTAN		1000 Be.			33314122 141114.		Х	-
VOLTAGE F	PROOF	250V AC FOR 1 min.			NO FLASHOVER	NO FLASHOVER OR BREAKDOWN.		_
MECHAI	VICAL CHA	RACTI	ERISTICS					
MECHANIC	AL	30TIMES	INSERTIONS AND EXTRACTIONS.		① CONTACT RES	SISTANCE: 50mΩ MAX.		
OPERATIO	N				② NO DAMAGE, CRACK OR LOOSENESS OF PARTS.		X	-
VIBRATION		FREQUENCY 10 TO 55 Hz, SINGLE AMPLITUDE 0.75 mm, AT 2 h, FOR 3 DIRECTIONS.			NO ELECTRICAL DISCONTINUITY OF 1µs.     NO DAMAGE, CRACK OR LOOSENESS OF PARTS.		Х	-
sноск		490 m/s <sup>2</sup> DURATION OF PULSE 11 ms AT 3 TIMES			① NO ELECTRICAL DISCONTINUITY OF 1µs.			<u> </u>
		FOR 3 D	RECTIONS.	N D A C		K OR LOOSENESS OF PARTS.	X	
DADID OLIA	NOE OF	TEMPERA	ENVIRONMENTAL CHA TURE -65→ 5 TO 35→125→ 5 TO 35°C	ARAC		TANCE: FOO MAY		_
RAPID CHANGE OF TEMPERATURE		TIME UNDER 5	30→10 TO 15→ 30→10TO15min	② INSULATION RES	TANCE: 50mΩ MAX. IISTANCE: 500 MΩ MIN. < OR LOOSENESS OF PARTS.	X	-	
DAMP HEA	T		D AT 40 ± 2 °C, 90 TO 95 %, 96 h.		① CONTACT RESIS			
(STEADY STATE)					② INSULATION RESISTANCE: 500 MΩ MIN.			-
	STANCE OF	_	MENDED TEMPERATURE PROFILE]		NO DEFORMATION	OR LOOSENESS OF PARTS.  OF CASE OF EXCESSIVE	X	_
SOLDERING		《SOLDERING AREA》  MAX250°C, 220°C FOR 60 SECONDS MAX.  《PREHEATING AREA》  150 TO 180°C 90∼120 SECONDS.  MAXIMUM TWICE ACTION IS ALLOWED UNDER THE SAME CONDITION.  [RECOMMENDED MANUAL SOLDELING CONDITION ]  SOLDERING IRON TEMPERATURE 380°C  SOLDERING TIME: WITHIN 3 SECONDS.			LOOSENESS OF THE TERMINALS.		X	_
SOLDERAB	ILITY	DURATIO	ING TEMPARATURE:245±5°C DN OF IMMERSION : ING FOR 3SECONDS			COATING OF SOLDER SHALL 1/ OF 95% OF THE SURFACE D.	Х	_
		+						

NOTE1:INCLUDING THE TEMPERATURE RISE BY CURRENT. NOTE2:STORAGEIS DEFINED AS LONG-TERM STORAGE OF UNUSED PRODUCTS. APPLY OPERATION TEMPERATURE RANGE TO PRODUCTS MOUNTED ON PCB WITHOUT POWER SUPLLY.

UNLESS OTHERWISE SPECIFIED , REFER TO JIS C 5402

0.12		TERRORE OF EOR TED , THE TER TO GOOD TOP :				
	COUNT	DESCRIPTION OF REVISIONS	DESIGNED		CHECKED	DATE
Δ	1	DIS-H-001215	AR.TAKAHASHI		TS.MIYAZAKI	06.08.02
				APPROVE	TY.OMA	04.04.02
				CHECKED	TY.OMA	04.04.02
				DESIGNED	HK.UMEHARA	04.04.01
				DRAWN	MY.NAKAMOTO	04.04.01
Note	lote QT:Qualification Test AT:Assurance Test X:Applicable Test		DRAWING NO.		ELC4-160019-11	
		SPECIFICATION SHEET	PART NO.		DF9B-*S-1V(32)	
		HIROSE ELECTRIC CO., LTD.	CODE NO.	CL540		<b>A</b> 1/1