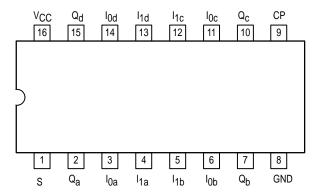


QUAD 2-PORT REGISTER

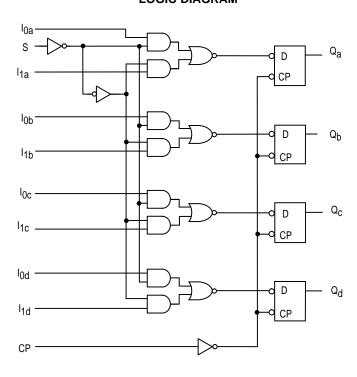
The MC54/74F399 is the logical equivalent of a guad 2-input multiplexer feeding into four edge-triggered flip flops. A common Select input determines which of the two 4-bit words is accepted. The selected data enters the flipflops on the rising edge of the clock. The MC54/74F399 is the 16-pin version of the MC54/74F398, with only the Q outputs of the flip-flops available.

- Select Inputs from Two Data Sources
- Fully Positive Edge-Triggered Operation

CONNECTION DIAGRAM (TOP VIEW)



LOGIC DIAGRAM

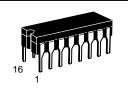


NOTE:

This diagram is provided only for the understanding of logic operations and should not be used to estimate propagation delays.

MC54/74F399

QUAD 2-PORT REGISTER FAST™ SCHOTTKY TTL



J SUFFIX CERAMIC CASE 620-09



N SUFFIX PLASTIC CASE 648-08

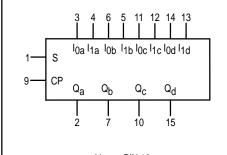


D SUFFIX SOIC CASE 751B-03

ORDERING INFORMATION

MC54FXXXJ Ceramic MC74FXXXN **Plastic** MC74FXXXD SOIC

LOGIC SYMBOL



V_{CC} = PIN 16 GND = PIN 8

FUNCTIONAL DESCRIPTION

The MC54/74F398 is a high-speed quad 2-port register. It will select four bits of data from either of two sources (Ports) under control of a common Select input (S). The selected data is transferred to a 4-bit output register synchronous with the LOW-to-HIGH transition of the Clock input (CP). The 4-bit D-

type output register is fully edge-triggered. The Data inputs (I_{0x}, I_{1x}) and Select input (S) must be stable only a setup time prior to and hold time after the LOW-to-HIGH transition of the Clock input for predictable operation.

FUNCTION TABLE

	Inputs	Output	
S	I ₀	l ₁	Q
I		Х	L
1	h	Х	Н
h	X	1	L
h	X	h	Н

H = HIGH Voltage Level

L = LOW Voltage Level

h = HIGH Voltage Level one setup time prior to the LOW-to-HIGH clock transition

I = LOW Voltage Level one setup time prior to the LOW-to-HIGH clock transition

X = Don't Care

GUARANTEED OPERATING RANGES

Symbol	Parameter		Min	Тур	Max	Unit
Vcc	Supply Voltage	54, 74	4.5	5.0	5.5	V
TA	Operating Ambient Temperature Range	54	- 55	25	125	°C
		74	0	25	70	
ІОН	Output Current — High	54, 74			- 1.0	mA
loL	Output Current — Low	54, 74		_	20	mA

DC CHARACTERISTICS OVER OPERATING TEMPERATURE RANGE (unless otherwise specified)

			Limits						
Symbol	Parameter		Min	Тур	Max	Unit	Test Conditions		
VIH	Input HIGH Voltage		2.0			V	Guaranteed Input HIGH Voltage		
V _{IL}	Input LOW Voltage				0.8	V	Guaranteed Input LOW Voltage		
VIK	Input Clamp Diode Voltage				-1.2	V	I _{IN} = -18 mA	V _{CC} = MIN	
VOH Output HIGH Voltage	Output HIGH Voltage	54, 74	2.5	3.4		V	$I_{OH} = -1.0 \text{ mA}$	V _{CC} = 4.5 V	
		74	2.7	3.4		V	$I_{OH} = -1.0 \text{ mA}$	V _{CC} = 4.75 V	
V_{OL}	Output LOW Voltage			0.35	0.5	V	I _{OL} = 20 mA	V _{CC} = MIN	
ΊΗ	Input HIGH Current				20	μΑ	V _{IN} = 2.7 V	V _{CC} = MAX	
					100	μΑ	V _{IN} = 7.0 V		
IլL	Input LOW Current				-0.6	mA	V _{IN} = 0.5 V	V _{CC} = MAX	
los	Output Short Circuit Current (Note 2)		-60		-150	mA	V _{OUT} = 0 V	V _{CC} = MAX	
ICC	Power Supply Current			22	34	mA	VCC = MAX	V _{IN} = GND CP = -	

NOTES:

- 1. For conditions shown as MIN or MAX, use the appropriate value specified under guaranteed operating ranges.
- 2. Not more than one output should be shorted at a time, nor for more than 1 second.

MC54/74F399

AC CHARACTERISTICS

		54/74F		54F		74F			
		T _A = + 25°C		T _A = −55°C to +125°C		T _A = 0°C to 70°C			
		V _{CC} = +5.0V		V_{CC} = 5.0 V \pm 10%		V _{CC} = 5.0 V ± 10%			
		C _L = 50 pF		C _L = 50 pF		C _L = 50 pF			
Symbol	Parameter	Min	Тур	Max	Min	Max	Min	Max	Unit
f _{max}	Maximum Clock Frequency	100	140		80		100		MHz
^t PLH	Propagation Delay	3.0	5.7	7.5	3.0	9.5	3.0	8.5	ns
tPHL	CP to Q	3.0	6.8	9.5	3.0	11.5	3.0	10.0	

AC OPERATING REQUIREMENTS

		54/74F		54F		74F			l	
		T _A = +25°C		$T_A = -55^{\circ}C \text{ to } + 125^{\circ}C$		T _A = 0°C to 70°C		1	l	
		V _{CC} = +5.0V		V_{CC} = 5.0 V \pm 10%		V_{CC} = 5.0 V \pm 10%			l	
Symbol	Parameter	Min	Тур	Max	Min	Max	Min	Max	Unit	l
t _S (H)	Setup Time, HIGH or LOW	3.0			4.5		3.0		ns	1
t _S (L)	I _n to CP	3.0			4.5		3.0			l
t _h (H)	Hold Time, HIGH or LOW	1.0			1.5		1.0		ns	1
t _h (L)	I _n to CP	1.0			1.5		1.0			l
t _S (H)	Setup Time, HIGH or LOW	7.5			9.5		8.5		ns	1
t _S (L)	S to CP	7.5			9.5		8.5			l
t _h (H)	Hold Time, HIGH or LOW	0			0		0		ns	1
t _h (L)	S to CP	0			0		0			١
t _W (H)	CP Pulse Width	4.0			4.0		4.0		ns	1
t _W (L)	HIGH or LOW	5.0			7.0		5.0			l

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