

V_{RRM} = 1200 V

$I_F (T_C=160^\circ\text{C})$ = 5 A

Q_c = 32 nC

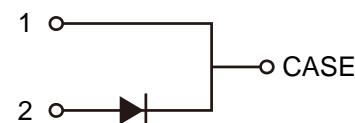
TO-252-2

CASE



Features

- Extremely low reverse current
- No reverse recovery current
- Temperature independent switching
- Positive temperature coefficient on V_F
- Excellent surge current capability
- Low capacitive charge



Benefits

- Essentially no switching losses
- System efficiency improvement over Si diodes
- Increased power density
- Enabling higher switching frequency
- Reduction of heat sink requirements
- System cost savings due to smaller magnetics
- Reduced EMI



Applications

- Switch mode power supplies (SMPS)
- Uninterruptible power supplies
- Motor drivers
- Power factor correction



Package Pin Definitions

- Pin1- Cathode
- Pin2- Anode

Package Parameters

Part Number	Marking	Package
B1D05120E	B1D05120E	TO-252-2

Maximum Ratings (T_c=25°C unless otherwise specified)

Symbol	Parameter	Test conditions	Value	Unit
V _{RRM}	Repetitive peak reverse voltage		1200	V
V _{RSM}	Non-repetitive peak reverse voltage		1200	V
I _F	Continuous forward current	T _c =25°C T _c =160°C	23 5	A
I _{FSM}	Non-Repetitive forward surge current	T _c =25°C , t _p =10ms, Half Sine Wave	60	A
∫i ² dt	i ² t value	T _c =25°C , t _p =10ms	18	A ² S
P _{tot}	Power dissipation	T _c =25°C T _c =110°C	124 53	W
T _j	Operating junction temperature		-55~175	°C
T _{stg}	Storage temperature		-55~175	°C

Thermal Characteristics

Symbol	Parameter	Value			Unit
		Min.	Typ.	Max.	
R _{th(jc)}	Thermal resistance from junction to case		1.207		K/W

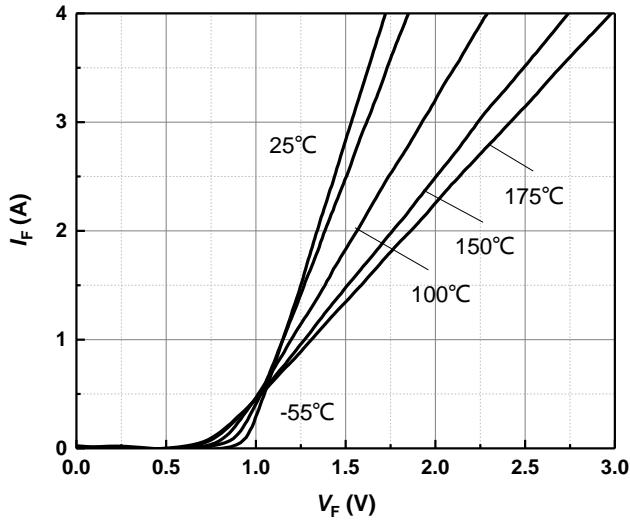
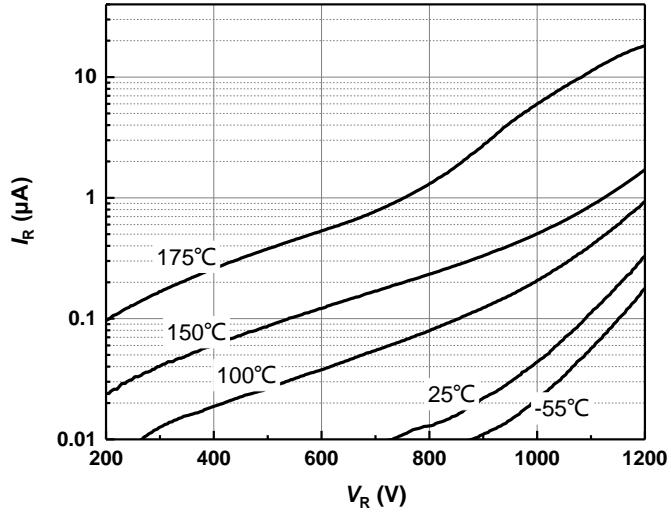
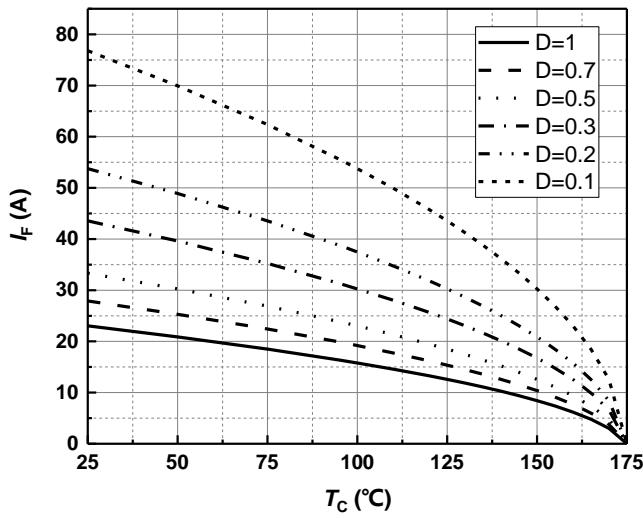
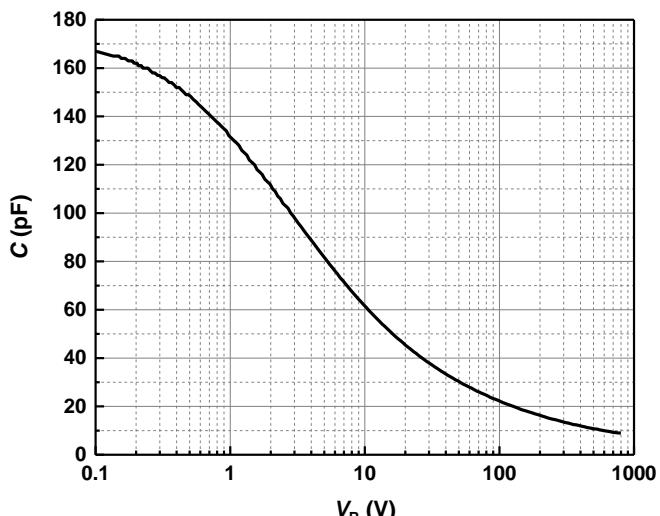
Electrical Characteristics

Static Characteristics

Symbol	Parameter	Test conditions	Value			Unit
			Min.	Typ.	Max.	
V_{DC}	DC blocking voltage	$T_j=25^\circ C$	1200			V
V_F	Diode forward voltage	$I_F=5A T_j=25^\circ C$ $I_F=5A T_j=175^\circ C$		1.40 1.78		V
I_R	Reverse current	$V_R=1200V T_j=25^\circ C$ $V_R=1200V T_j=175^\circ C$		2 10		μA

AC Characteristics

Symbol	Parameter	Test conditions	Value			Unit
			Min.	Typ.	Max.	
Q_c	Total capacitive charge	$V_R=800V T_j=25^\circ C$ $Q_c = \int_0^{V_R} C(V)dV$		32		nC
C	Total capacitance	$V_R=1V f=1MHz$ $V_R=400V f=1MHz$ $V_R=800V f=1MHz$		352 31 23		pF
E_c	Capacitance stored energy	$V_R=800V$		16		μJ

Typical Performance

Figure 1. Typical forward characteristics

Figure 2. Typical reverse current as function of reverse voltage

Figure 3. Diode forward current as function of temperature, D=duty cycle

Figure 4. Typical capacitance as function of reverse voltage, $C=f(V_R)$; $T_j=25^\circ\text{C}$; $f=1 \text{ MHz}$

Typical Performance

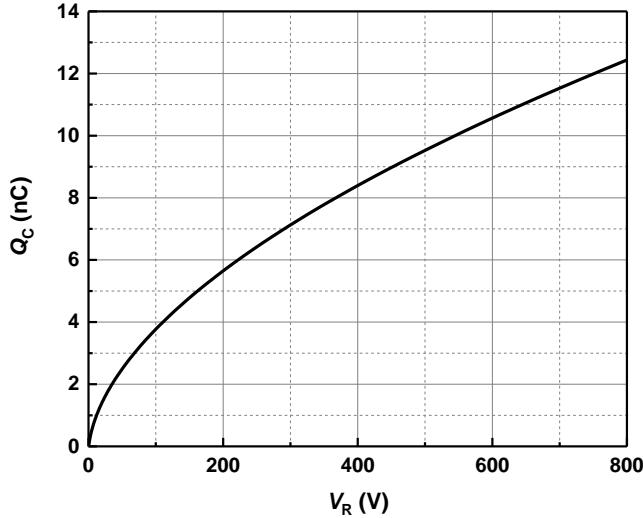


Figure 5. Typical reverse charge as function of reverse voltage

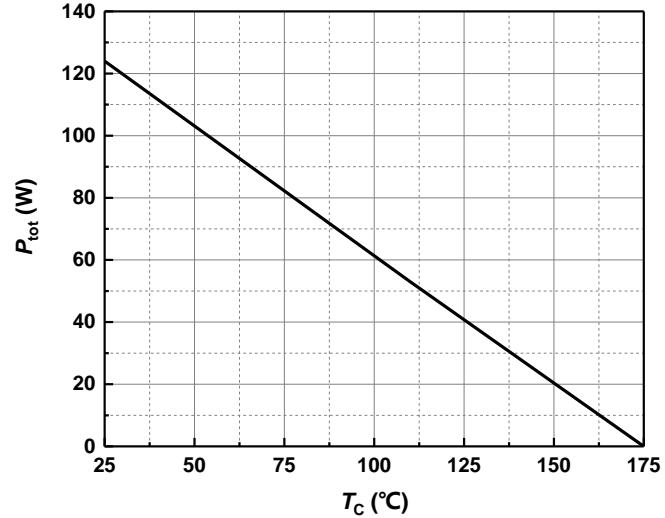


Figure 6. Power dissipation as function of case temperature

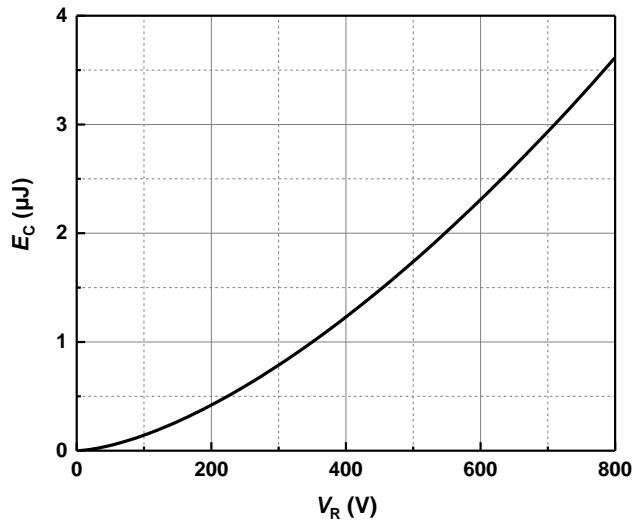


Figure 7. Capacitance stored energy

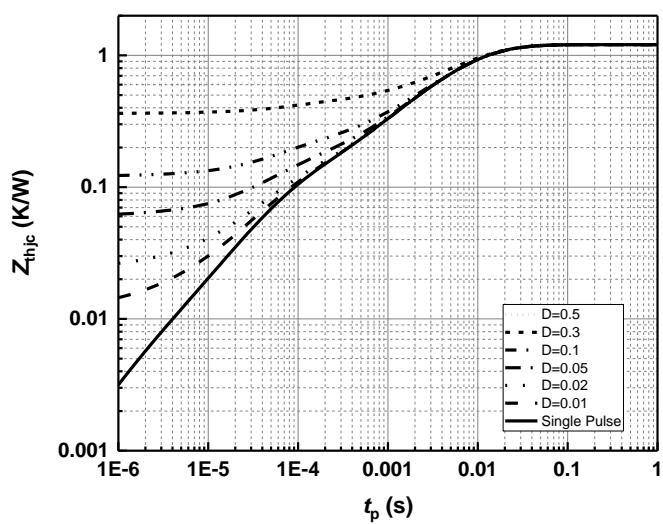
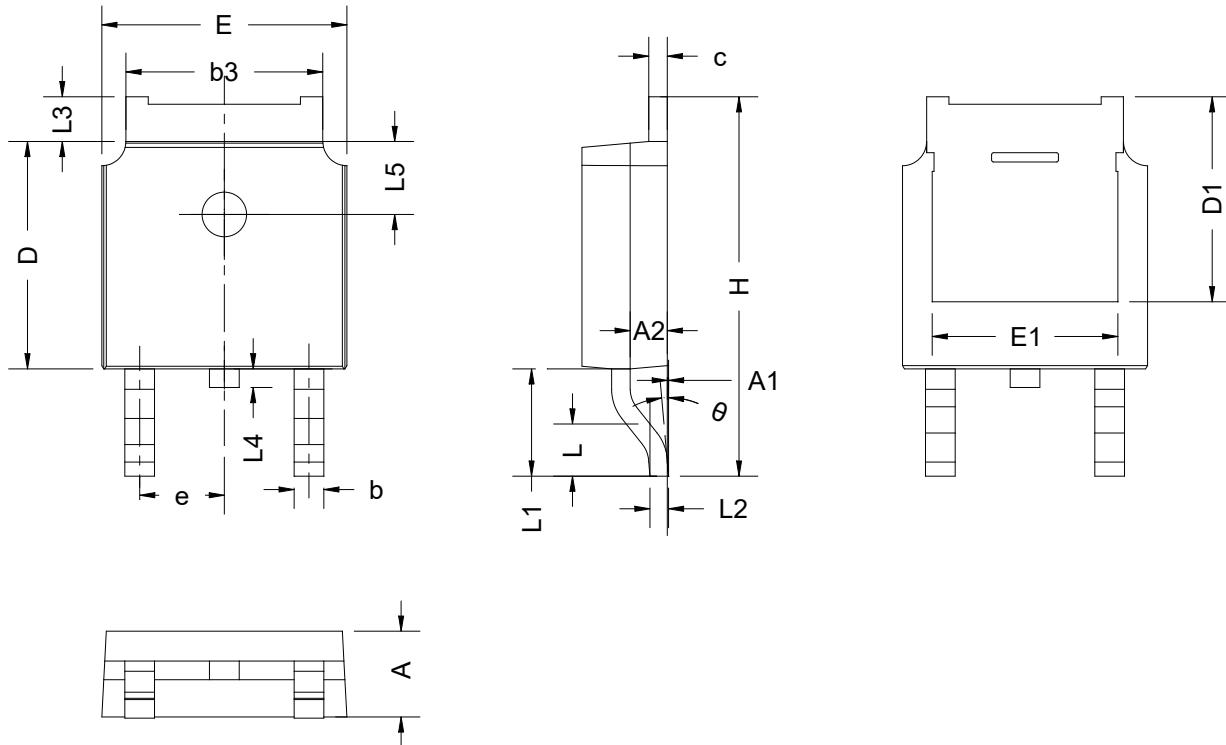


Figure 8. Max. transient thermal impedance, $Z_{thjc} = f(t)$, parameter: $D = t/T$

Package Dimensions



SYMBOL	mm		
	MIN	NOM	MAX
A	2.20	2.30	2.38
A1	0.00	-	0.20
A2	0.90	1.07	1.17
b	0.68	0.78	0.90
b3	5.23	5.33	5.46
c	0.43	0.53	0.61
D	5.98	6.10	6.22
D1	5.30 REF		
E	6.40	6.60	6.73
E1	4.63	-	-
e	2.286 BSC		
H	9.40	10.10	10.50
L	1.38	1.50	1.75
L1	2.90 REF		
L2	0.51 BSC		
L3	0.88	-	1.28
L4	0.50	-	1.00
L5	1.65	1.80	1.95
θ	0 °	-	8 °

Revision History

Document Version	Date of Release	Description of Changes
Rev. 0.1	2021-01-26	Release of the preliminary datasheet.

BASiC Semiconductor Ltd.
Shenzhen, China
© 2021 BASiC Semiconductor Ltd.
All Rights Reserved.

Information

For further information on technology, delivery terms and conditions and prices, please contact the nearest BASiC Semiconductor Office.

Disclaimer

The information given in this document shall in no event be regarded as a guarantee of conditions or characteristics. With respect to any examples or hints given herein, any typical values stated herein and/or any information regarding the application of the device, BASiC semiconductor Ltd. hereby disclaims any and all warranties and liabilities of any kind, including without limitation, warranties of non-infringement of intellectual property rights of any third party.