

Description

The SX70P03NF uses advanced trench technology to provide excellent $R_{DS(ON)}$, low gate charge and operation with gate voltages as low as 4.5V. This device is suitable for use as a Battery protection or in other Switching application.

General Features

$V_{DS} = -30V$ $I_D = -78A$

$R_{DS(ON)} < 7.5m\Omega$ @ $V_{GS} = -10V$

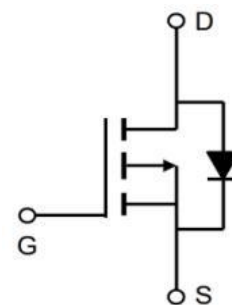
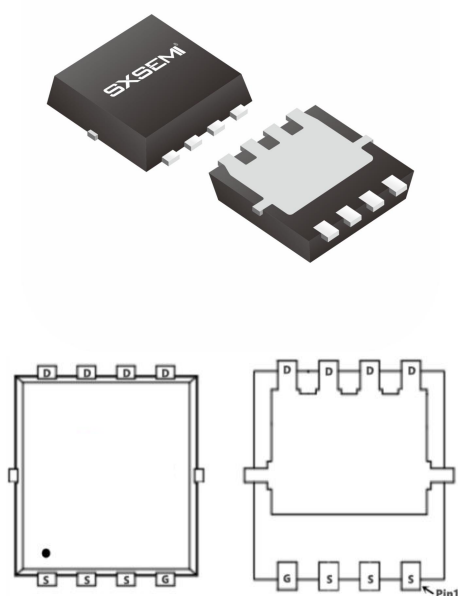
Application

Lithium battery protection

Wireless impact

Mobile phone fast charging

PDFN5*6-8L



Absolute Maximum Ratings (TC=25°C unless otherwise noted)

Symbol	Parameter	Rating	Units
V _{DS}	Drain-Source Voltage	-30	V
V _{GS}	Gate-Source Voltage	±20	V
I _D @TC=25°C	Continuous Drain Current, V _{GS} @ -10V ¹	-78	A
I _D @TC=100°C	Continuous Drain Current, V _{GS} @ -10V ¹	-57	A
I _{DM}	Pulsed Drain Current ²	-200	A
E _{AS}	Single Pulse Avalanche Energy ³	125	mJ
I _{AS}	Avalanche Current	-40	A
P _D @TC=25°C	Total Power Dissipation ⁴	69	W
T _{STG}	Storage Temperature Range	-55 to 150	°C
T _J	Operating Junction Temperature Range	-55 to 150	°C
R _{θJA}	Thermal Resistance Junction-Ambient ¹	25	°C/W
R _{θJC}	Thermal Resistance Junction-Case ¹	1.6	°C/W

Electrical Characteristics (T_J=25 °C, unless otherwise noted)

Symbol	Parameter	Conditions	Min.	Typ.	Max.	Unit
BV _{DSS}	Drain-Source Breakdown Voltage	V _{GS} =0V , I _D =-250uA	-30	-34	---	V
$\Delta BV_{DSS}/\Delta T_J$	BVDSS Temperature Coefficient	Reference to 25°C , I _D =-1mA	---	-0.0232	---	V/°C
RDS(ON)	Static Drain-Source On-Resistance	V _{GS} =-10V , I _D =-20A	---	5.2	7.5	mΩ
		V _{GS} =-4.5V , I _D =-15A	---	8.0	11	
V _{GS(th)}	Gate Threshold Voltage	V _{GS} =V _{DS} , I _D =-250uA	-1.2	-1.4	-2.5	V
$\Delta V_{GS(th)}$	V _{GS(th)} Temperature Coefficient		---	4.6	---	mV/°C
I _{DSS}	Drain-Source Leakage Current	V _{DS} =-24V , V _{GS} =0V , T _J =25°C	---	---	-1	uA
		V _{DS} =-24V , V _{GS} =0V , T _J =55°C	---	---	-5	
I _{GSS}	Gate-Source Leakage Current	V _{GS} =±20V , V _{DS} =0V	---	---	±100	nA
g _{fs}	Forward Transconductance	V _{DS} =-5V , I _D =-30A	---	30	---	S
R _g	Gate Resistance	V _{DS} =0V , V _{GS} =0V , f=1MHz	---	9.8	---	Ω
Q _g	Total Gate Charge (-4.5V)	V _{DS} =-15V , V _{GS} =-4.5V I _D =-20A	---	35	---	nC
Q _{gs}	Gate-Source Charge		---	9.9	---	
Q _{gd}	Gate-Drain Charge		---	10.5	---	
T _{d(on)}	Turn-On Delay Time	V _{DD} =-15V , V _{GS} =-10V , R _G =3.0Ω I _D =-20A	---	10.8	---	ns
T _r	Rise Time		---	13.2	---	
T _{d(off)}	Turn-Off Delay Time		---	73	---	
T _f	Fall Time		---	35	---	
C _{iss}	Input Capacitance	V _{DS} =-15V , V _{GS} =0V , f=1MHz	---	3520	---	pF
C _{oss}	Output Capacitance		---	465	---	
C _{rss}	Reverse Transfer Capacitance		---	370	---	
I _S	Continuous Source Current	V _G =V _D =0V , Force Current	---	---	-70	A
I _{SM}	Pulsed Source Current		---	---	-130	A
V _{SD}	Diode Forward Voltage	V _{GS} =0V , I _S =-1A , T _J =25°C	---	---	-1.3	V
t _{rr}	Reverse Recovery Time	I _F =-20A , dI/dt=100A/μs , T _J =25°C	---	25	---	nS
Q _{rr}	Reverse Recovery Charge		---	10	---	nC

Note :

- 1、The data tested by surface mounted on a 1 inch 2 FR-4 board with 2OZ copper.
- 2、The data tested by pulsed , pulse width .The EAS data shows Max. rating .
- 3、The power dissipation is limited by 175°C junction temperature
- 4、EAS condition: T_J=25°C , V_{DD}= -24V, V_G= -10V, R_G=7Ω, L=0.1mH, I_{AS}= -40A
- 5、The data is theoretically the same as I_D and I_{DM} , in real applications , should be limited by total power dissipation.

Typical Characteristics

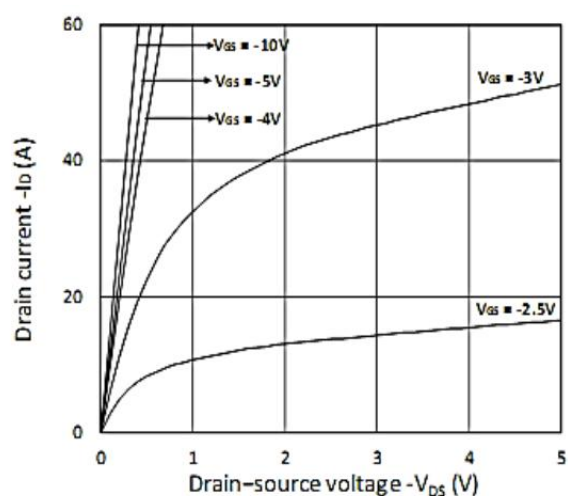


Figure 1. Output Characteristics

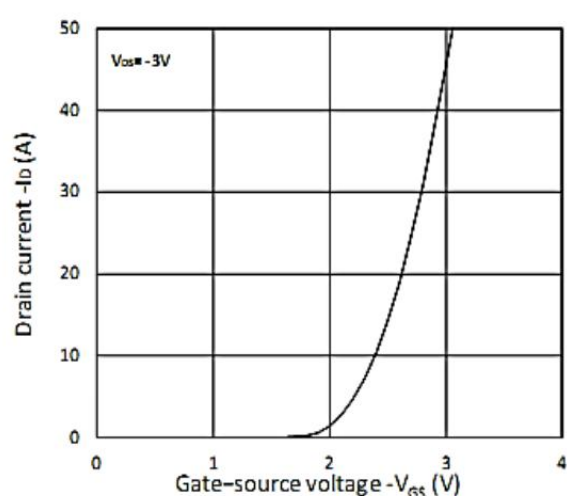


Figure 2. Transfer Characteristics

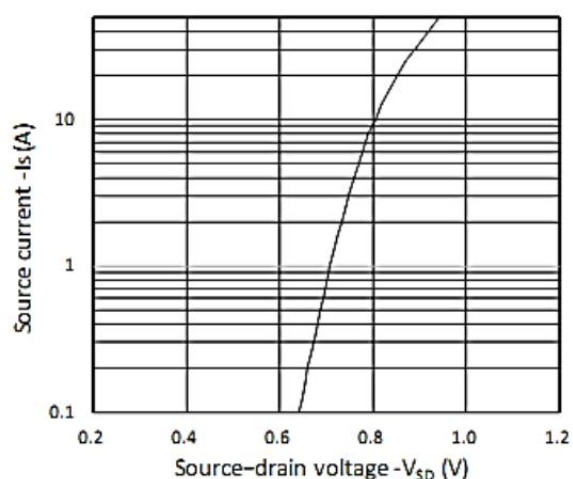


Figure 3. Forward Characteristics of Reverse

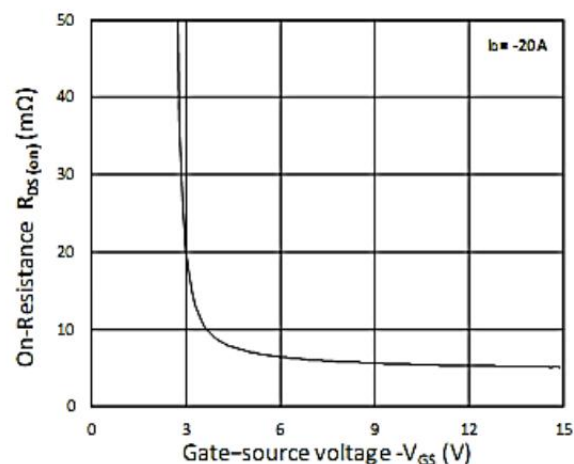


Figure 4. $R_{DS(ON)}$ vs. V_{GS}

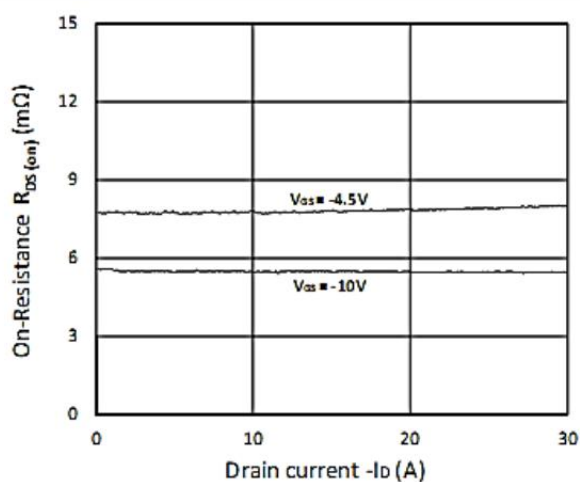


Figure 5. $R_{DS(ON)}$ vs. I_D

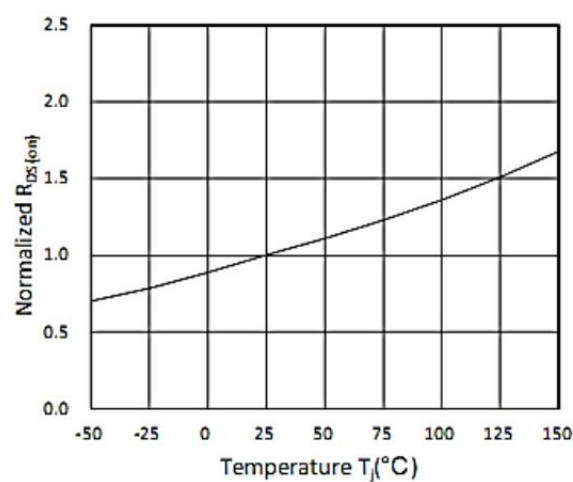


Figure 6. Normalized $R_{DS(on)}$ vs. Temperature

Typical Characteristics

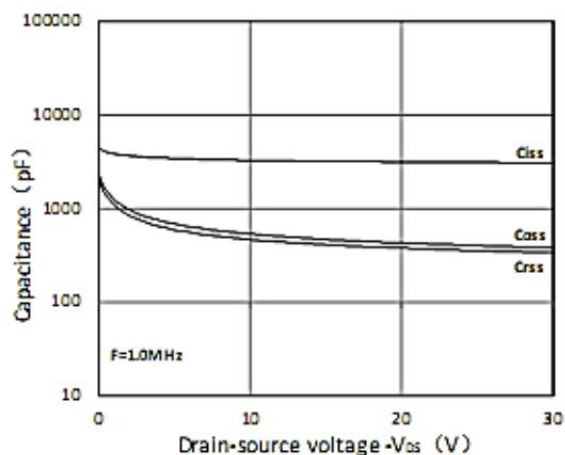


Figure 7. Capacitance Characteristics

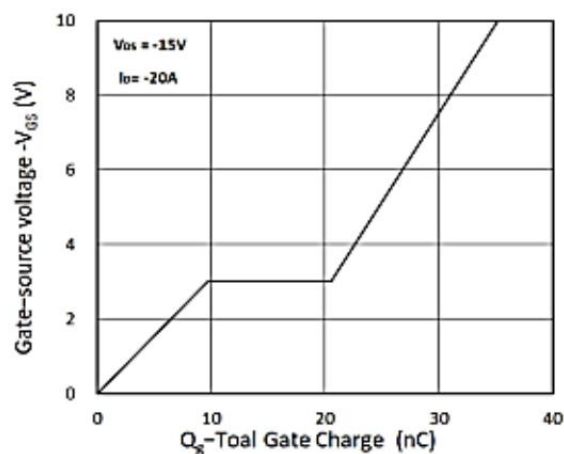


Figure 8. Gate Charge Characteristics

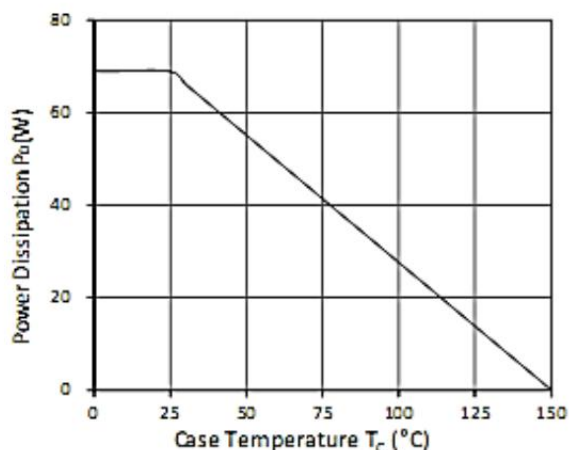


Figure 9. Power Dissipation

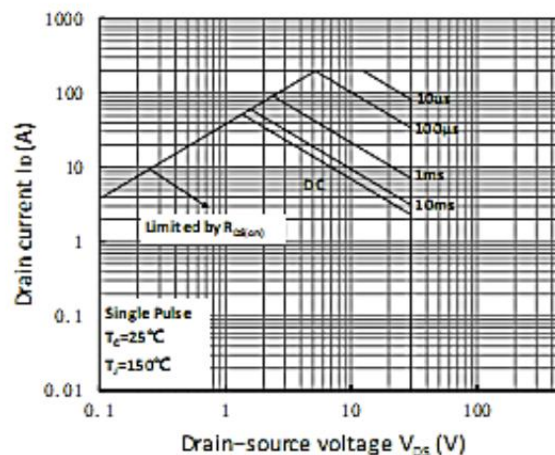


Figure 10. Safe Operating Area

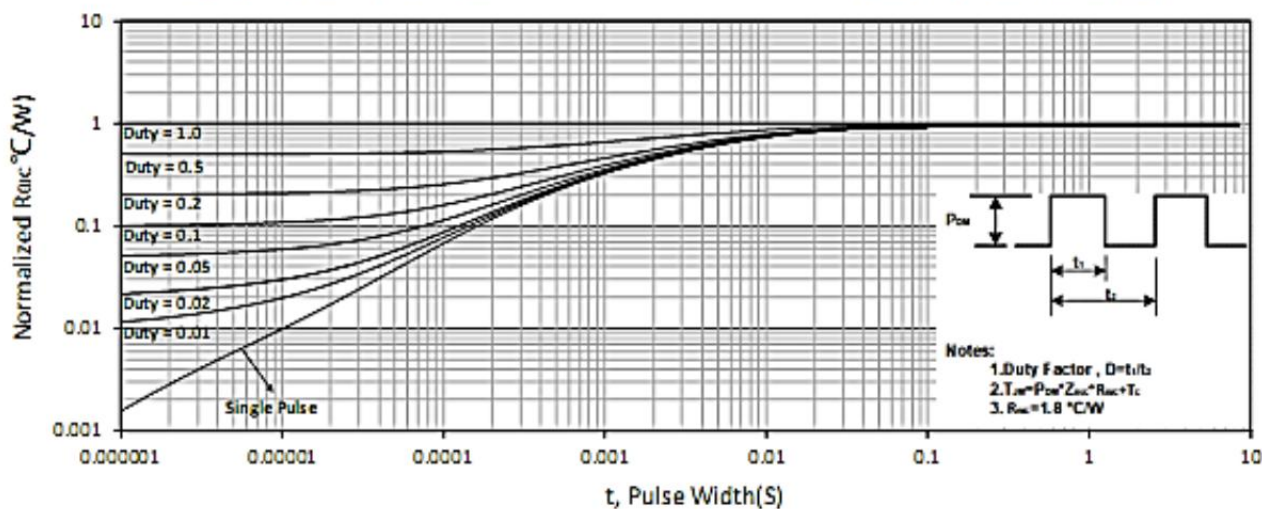
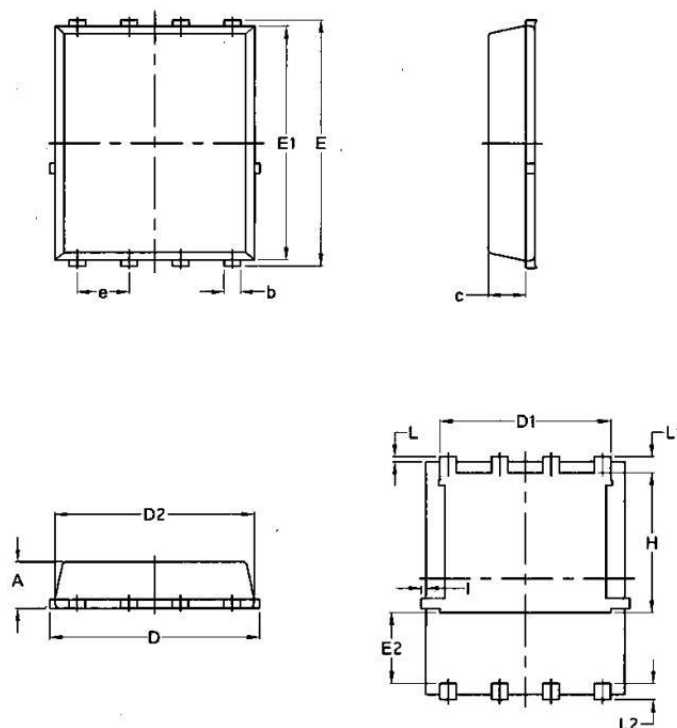


Figure 11. Normalized Maximum Transient Thermal Impedance

Package Mechanical Data- PDFN5*6-8L-JQ Single



Symbol	Common			
	mm		Inch	
	Mim	Max	Min	Max
A	1.03	1.17	0.0406	0.0461
b	0.34	0.48	0.0134	0.0189
c	0.824	0.0970	0.0324	0.082
D	4.80	5.40	0.1890	0.2126
D1	4.11	4.31	0.1618	0.1697
D2	4.80	5.00	0.1890	0.1969
E	5.95	6.15	0.2343	0.2421
E1	5.65	5.85	0.2224	0.2303
E2	1.60	/	0.0630	/
e	1.27 BSC		0.05 BSC	
L	0.05	0.25	0.0020	0.0098
L1	0.38	0.50	0.0150	0.0197
L2	0.38	0.50	0.0150	0.0197
H	3.30	3.50	0.1299	0.1378
I	/	0.18	/	0.0070

Package Marking and Ordering Information

Product ID	Pack	Marking	Qty(PCS)
TAPING	PDFN5*6-8L		5000