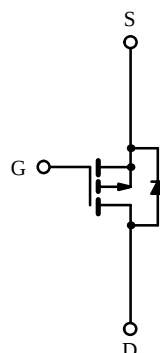
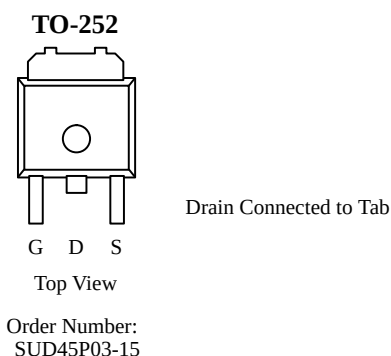


**P-Channel 30-V (D-S), 150°C MOSFET****Product Summary**

V_{DS} (V)	$r_{DS(on)}$ (Ω)	I_D (A) ^a
-30	0.015 @ $V_{GS} = -10$ V	± 13
	0.024 @ $V_{GS} = -4.5$ V	± 8

TrenchFET™
Power MOSFETs

P-Channel MOSFET

Absolute Maximum Ratings ($T_A = 25^\circ\text{C}$ Unless Otherwise Noted)

Parameter	Symbol	Limit	Unit
Drain-Source Voltage	V_{DS}	-30	V
Gate-Source Voltage	V_{GS}	± 20	
Continuous Drain Current ^b	I_D	$T_A = 25^\circ\text{C}$ ± 13	A
		$T_A = 100^\circ\text{C}$ ± 8	
Pulsed Drain Current	I_{DM}	± 100	
Continuous Source Current (Diode Conduction)	I_S	-13	
Maximum Power Dissipation ^b	P_D	$T_C = 25^\circ\text{C}$ 70	W
		$T_A = 25^\circ\text{C}$ 4 ^a	
Operating Junction and Storage Temperature Range	T_J, T_{stg}	-55 to 150	$^\circ\text{C}$

Thermal Resistance Ratings

Parameter	Symbol	Typical	Maximum	Unit
Maximum Junction-to-Ambient ^b	R_{thJA}		30	$^\circ\text{C/W}$
Maximum Junction-to-Case	R_{thJC}		1.8	

Notes

- a. Calculated Rating for $T_A = 25^\circ\text{C}$, for comparison purposes only. This cannot be used as continuous rating (see Absolute Maximum Ratings and Typical Characteristics).
- b. Surface Mounted on FR4 Board, $t \leq 10$ sec.

Updates to this data sheet may be obtained via facsimile by calling Siliconix FaxBack, 1-408-970-5600. Please request FaxBack document #70267.

Specifications ($T_J = 25^\circ\text{C}$ Unless Otherwise Noted)

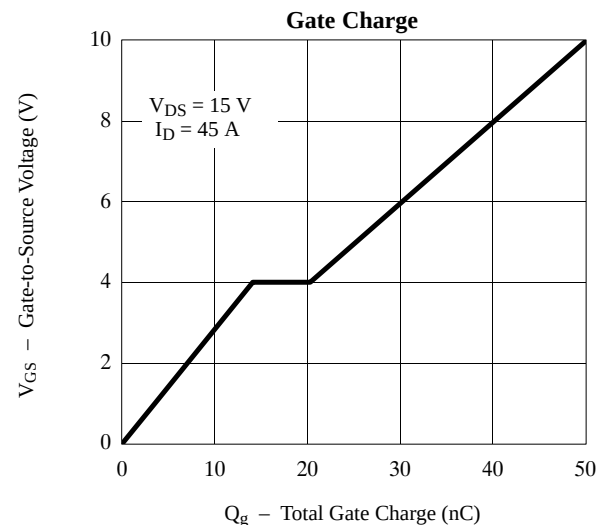
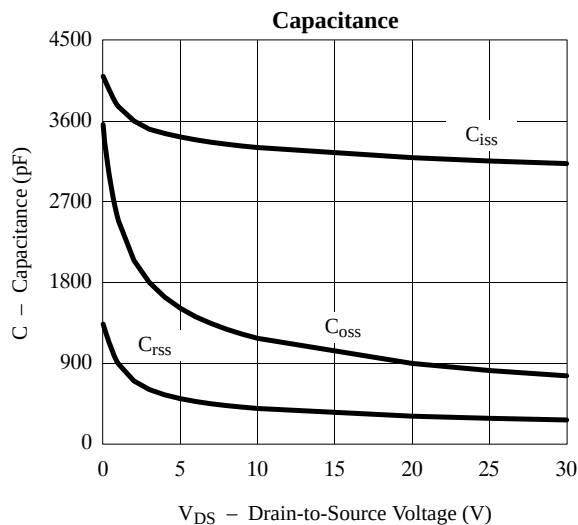
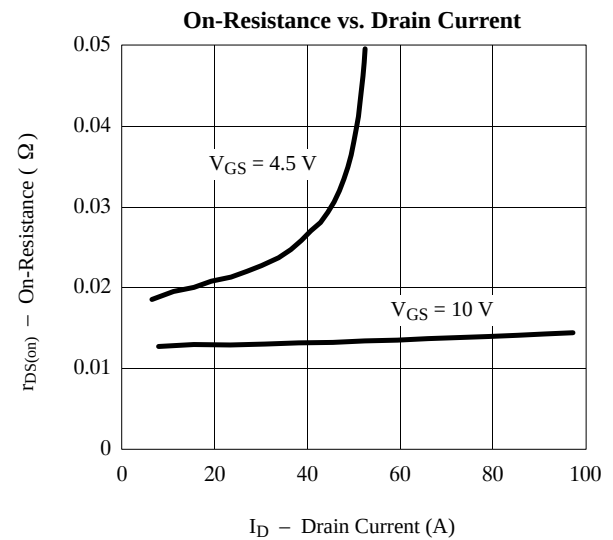
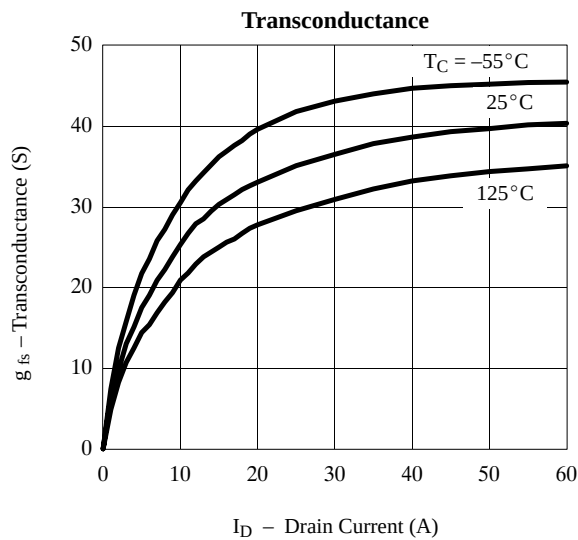
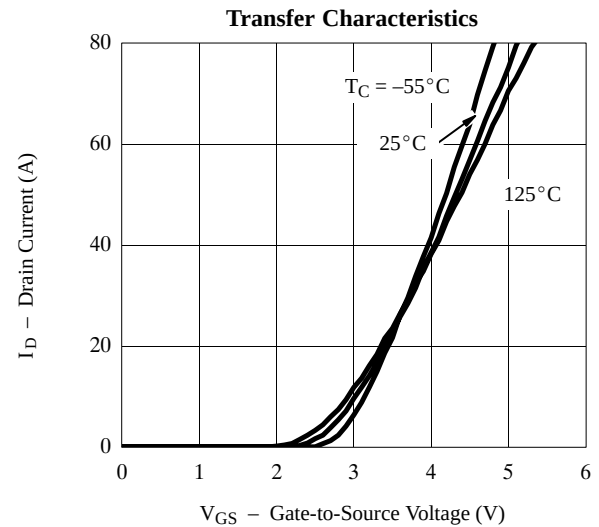
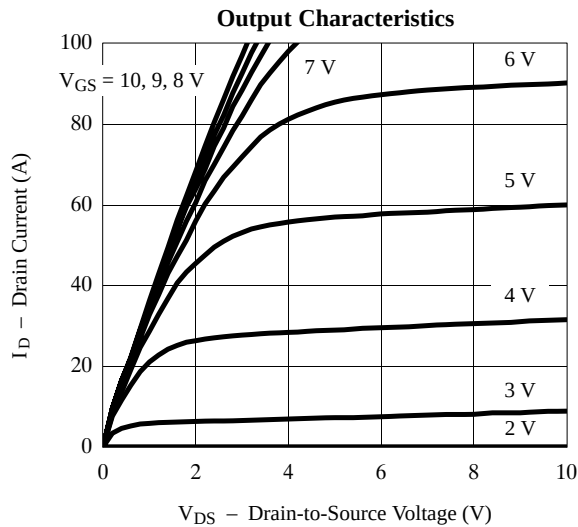
Parameter	Symbol	Test Condition	Min	Typ ^a	Max	Unit
Static						
Drain-Source Breakdown Voltage	V _{(BR)DSS}	V _{GS} = 0 V, I _D = −250 μA	−30			V
Gate Threshold Voltage	V _{GS(th)}	V _{DS} = V _{GS} , I _D = −250 μA	−1.0			
Gate-Body Leakage	I _{GSS}	V _{DS} = 0 V, V _{GS} = ± 20 V			± 100	nA
Zero Gate Voltage Drain Current	I _{DSS}	V _{DS} = −30 V, V _{GS} = 0 V			−1	μA
		V _{DS} = −30 V, V _{GS} = 0 V, T _J = 125°C			−50	
On-State Drain Current ^b	I _{D(on)}	V _{DS} = −5 V, V _{GS} = −10 V	−50			A
		V _{DS} = −5 V, V _{GS} = −4.5 V	−20			
Drain-Source On-State Resistance ^b	r _{DS(on)}	V _{GS} = −10 V, I _D = −13 A		0.012	0.015	Ω
		V _{GS} = −10 V, I _D = −13 A, T _J = 125°C		0.018	0.026	
		V _{GS} = −4.5 V, I _D = −13 A		0.020	0.024	
Forward Transconductance ^b	g _{fs}	V _{DS} = −15 V, I _D = −13 A	20			S
Dynamic ^a						
Input Capacitance	C _{iss}	V _{GS} = 0 V, V _{DS} = −25 V, F = 1 MHz		3200		pF
Output Capacitance	C _{oss}			800		
Reverse Transfer Capacitance	C _{rss}			280		
Total Gate Charge ^c	Q _g	V _{DS} = −15 V, V _{GS} = −10 V, I _D = −45 A		50	125	nC
Gate-Source Charge ^c	Q _{gs}			14		
Gate-Drain Charge ^c	Q _{gd}			6.2		
Turn-On Delay Time ^c	t _{d(on)}	V _{DD} = −15 V, R _L = 0.33 Ω I _D ≅ −45 A, V _{GEN} = −10 V, R _G = 2.4 Ω		13	20	ns
Rise Time ^c	t _r			10	20	
Turn-Off Delay Time ^c	t _{d(off)}			50	100	
Fall Time ^c	t _f			20	40	
Source-Drain Diode Ratings and Characteristic (T _C = 25 °C)						
Pulsed Current	I _{SM}				100	A
Diode Forward Voltage ^b	V _{SD}	I _F = −45 A, V _{GS} = 0 V		1.0	1.5	V
Source-Drain Reverse Recovery Time	t _{rr}	I _F = −45 A, di/dt = 100 A/μs		55	100	ns

Notes

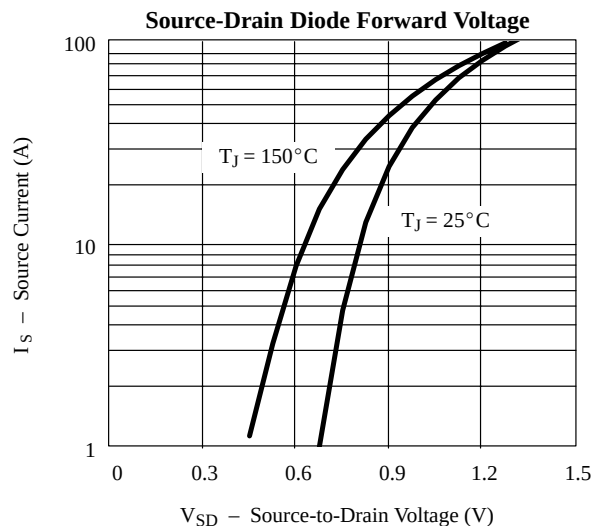
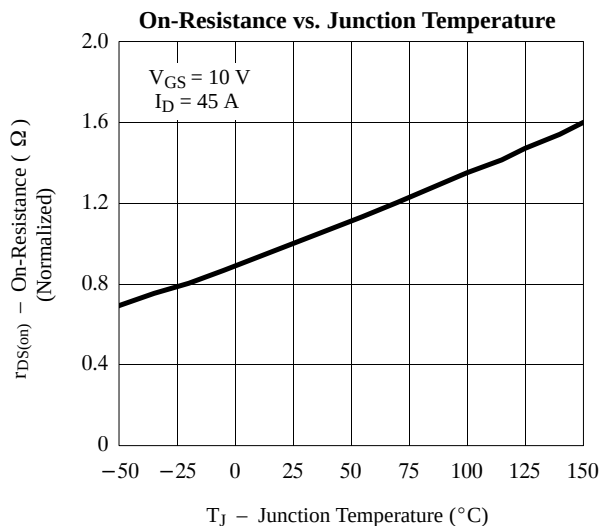
- a. Guaranteed by design, not subject to production testing.
b. Pulse test; pulse width $\leq 300\text{ }\mu\text{s}$, duty cycle $\leq 2\%$.
c. Independent of operating temperature.



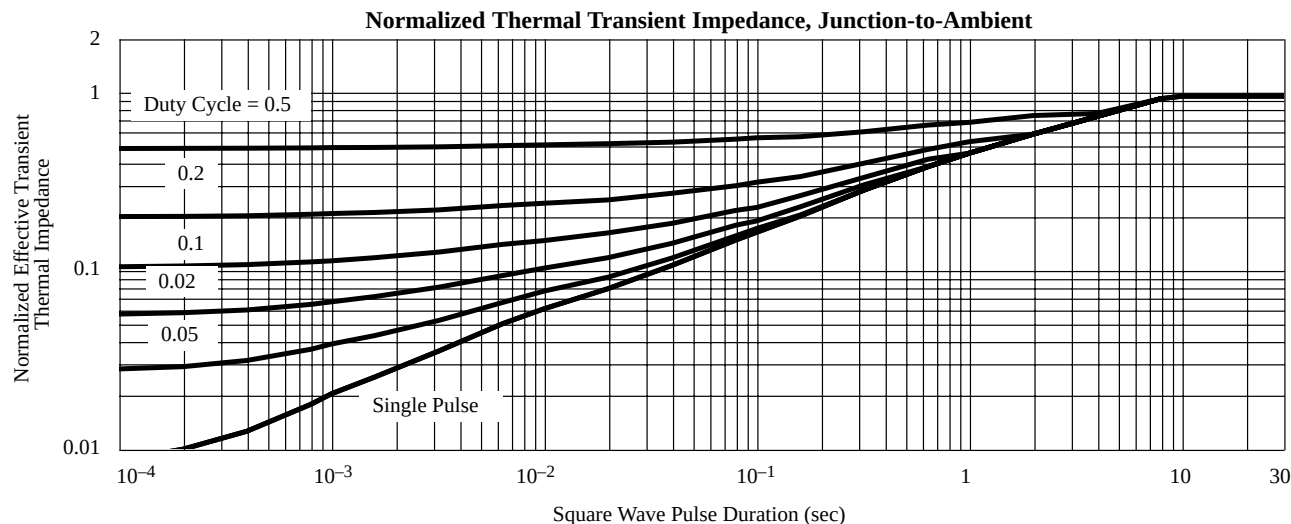
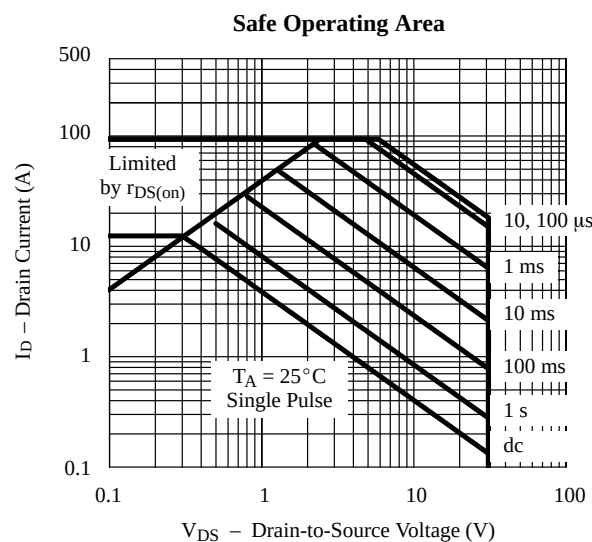
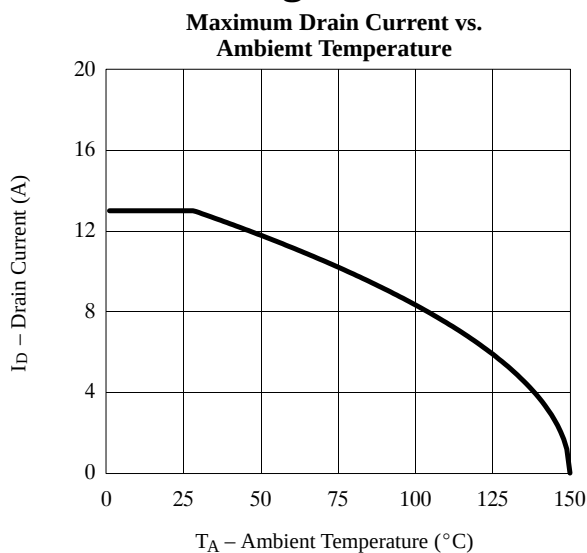
Typical Characteristics (25°C Unless Otherwise Noted)



Typical Characteristics (25°C Unless Otherwise Noted)



Thermal Ratings





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