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SPC-F005.DWG

REVISIONS

DOC. NO. SPC-F005 * Effective: 7/8/02 * DCP No: 1398

DCP #	REV	DESCRIPTION	DRAWN	DATE	CHECKD	DATE	APPRVD	DATE
1885	A	RELEASED	EO	02/03/06	HO	2/6/06	JWM	2/6/06

Description: TO-220 PNP silicon plastic transistor Designed for use in high frequency drivers in audio amplifier applications

Features:

- Collector-Emitter Sustaining Voltage, $V_{CE0} = 120V$
- DC Current Gain Specified to 8 Ampers, $h_{FE} = 40$ Min. @ $I_C = 3A$
 $h_{FE} = 20$ Min. @ $I_C = 4A$

Absolute Maximum Ratings:

- Collector-Base Voltage, $V_{CBO} = 120V$
- Collector-Emitter Voltage, $V_{CEO} = 120V$
- Emitter-Base Voltage, $V_{EBO} = 5V$
- Continuous Collector Current, $I_C = 8A$
- Base Current, $I_B = 2A$
- Total Device Dissipation ($T_C = +25^\circ C$), $P_D = 50W$
Derate above $25^\circ C = 0.4W/^\circ C$
- Operating Junction Temperature Range, $T_J = -65^\circ$ to $+150^\circ C$
- Storage Temperature Range, $T_{stg} = -65^\circ$ to $+150^\circ C$

Electrical Characteristics: ($T_A = +25^\circ C$ unless otherwise specified)

Parameter	Symbol	Test Conditions	Min	Max	Unit
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OFF Characteristics

Collector-Emitter Breakdown Voltage	$V_{(BR)CEO}$	$I_C = 10mA$, $I_B = 0$, (Note 1)	120	—	V
Collector Cut-Off Current	I_{CBO}	$V_{CB} = 120V$, $I_E = 0$	—	10	μA
	I_{CEO}	$V_{CB} = 120V$, $I_B = 0$	—	0.1	mA
Emitter Cut-Off Current	I_{EBO}	$V_{EB} = 5V$, $I_C = 0$	—	10	μA

ON Characteristics

DC Current Gain (Note 1)	h_{FE}	$V_{CE} = 2V$, $I_C = 0.1A$	40	—	—
		$V_{CE} = 2V$, $I_C = 2A$	40	—	—
		$V_{CE} = 2V$, $I_C = 3A$	40	—	—
		$V_{CE} = 2V$, $I_C = 4A$	20	—	—
Collector-Emitter Saturation Voltage	$V_{CE(sat)}$	$I_C = 1A$, $I_B = 0.1A$, (Note 1)	—	0.5	V
Base-Emitter On Voltage	$V_{BE(on)}$	$I_C = 1A$, $V_{CE} = 2V$, (Note 1)	—	1	V

Small-Signal Characteristics (Note 2)

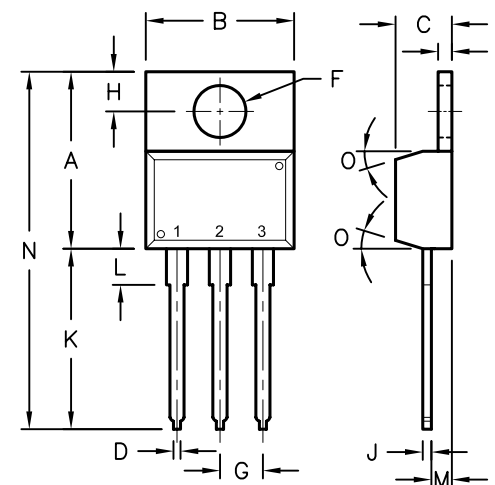
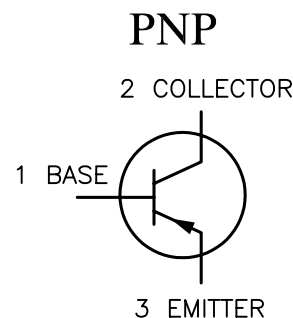
Current Gain-Bandwidth Product	f_T	$V_{CE} = 20V$, $I_C = 20mA$, $f = 100MHz$	30	—	MHz
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Note 1. Pulse Test: Pulse Width $\leq 300\mu s$, Duty Cycle $\leq 2\%$.

Note 2. f_T is defined as the frequency at which $|h_{fe}|$ extrapolates to unity.

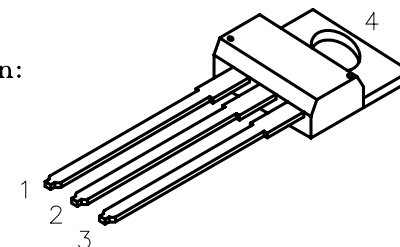


Dimensions	A	B	C	D	E	F	G	H	J	K	L	M	N	O
Min.	14.42	9.63	3.65	—	1.15	3.75	2.29	2.54	—	12.70	2.80	2.03	—	7
Max.	16.51	10.67	4.83	0.90	1.40	3.88	2.79	3.43	0.56	14.73	4.07	2.92	31.24	



Pin Configuration:

1. Base
2. Collector
3. Emitter
4. Collector



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TOLERANCES:

UNLESS OTHERWISE SPECIFIED, DIMENSIONS ARE FOR REFERENCE PURPOSES ONLY.

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DRAWING TITLE:

Transistor, Silicon, TO-220, PNP

SIZE	DWG. NO.	ELECTRONIC FILE	REV
A	MJE15029	01H0841.DWG	A
SCALE: NTS		U.O.M.: Millimeters	SHEET: 1 OF 1