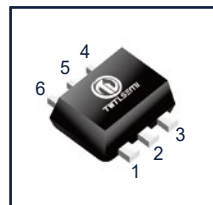
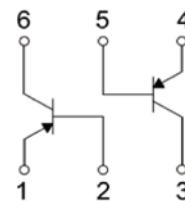


Features

- Epitaxial planar die construction
- Ideal for low power amplification and switching



SOT-563



Equivalent Circuit

Absolute Maximum Ratings($T_A=25^{\circ}\text{C}$)

Symbol	Parameter	Value	Unit
V_{CB0}	Collector-Base Voltage	-40	V
V_{CE0}	Collector-Emitter Voltage	-40	V
V_{EB0}	Emitter-Base Voltage	-5	V
I_C	Collector Current	-0.2	A
P_C	Collector Power Dissipation	150	mW
T_J, T_{stg}	Operation Junction And Storage Temperature Range	-55 ~ +150	$^{\circ}\text{C}$

Electrical Characteristics ($T_A=25^{\circ}\text{C}$ unless otherwise specified)

Symbol	Parameter	Test conditions	Min	Typ	Max	Unit
$V_{(BR)CB0}$	Collector-base breakdown voltage	$I_C=-10\mu\text{A}, I_E=0$	-40			V
$V_{(BR)CE0}$	Collector-emitter breakdown voltage	$I_C=-1\text{mA}, I_B=0$	-40			V
$V_{(BR)EB0}$	Emitter-base breakdown voltage	$I_E=-10\mu\text{A}, I_C=0$	-5			V
I_{CBO}	Collector cut-off current	$V_{CB}=-30\text{V}, I_E=0$			-0.05	μA
I_{EBO}	Emitter cut-off current	$V_{EB}=-5\text{V}, I_C=0$			-0.05	μA
I_{CEX}	Collector cut-off current	$V_{CE}=-30\text{V}, V_{EB(off)}=-3\text{V}$			-0.05	μA
$h_{FE(1)}$	DC current gain	$V_{CE}=-1\text{V}, I_C=-0.1\text{mA}$	60			
$h_{FE(2)}$		$V_{CE}=-1\text{V}, I_C=-1\text{mA}$	80			
$h_{FE(3)}$		$V_{CE}=-1\text{V}, I_C=-10\text{mA}$	100		300	
$V_{CE(sat)1}$	Collector-emitter saturation voltage	$I_C=-10\text{mA}, I_B=-1\text{mA}$			-0.25	V
$V_{CE(sat)2}$		$I_C=-50\text{mA}, I_B=-5\text{mA}$			-0.4	V
$V_{BE(sat)1}$	Base-emitter saturation voltage	$I_C=-10\text{mA}, I_B=-1\text{mA}$	-0.65		-0.85	V
$V_{BE(sat)2}$		$I_C=-50\text{mA}, I_B=-5\text{mA}$			-0.95	V
f_T	Transition frequency	$V_{CE}=-20\text{V}, I_C=-10\text{mA}, f=100\text{MHz}$	250			MHz
C_{ob}	Collector Output capacitance	$V_{CB}=-5\text{V}, I_E=0, f=1\text{MHz}$			4.5	pF
C_{ib}	Collector input capacitance	$V_{EB}=-0.5\text{V}, I_C=0, f=1\text{MHz}$			10	pF
t_d	Delay time	$V_{CC}=-3\text{V}, V_{BE(off)}=-0.5\text{V}$ $I_C=-10\text{mA}, I_{B1}=-I_{B2}=1\text{mA}$			35	nS
t_r	Rise time				35	nS
t_s	Storage time	$V_{CC}=-3\text{V}, I_C=-10\text{mA}$ $I_{B1}=-I_{B2}=1\text{mA}$			225	nS
t_f	Fall time				75	nS

Ordering information

Product ID	Pack	Naming rule	Marking	$h_{FE(1)}$	Qty(PCS)
MMDT3906V	SOT-563	MMDT3906V 产品名称 product name	KAR	100-300	3000

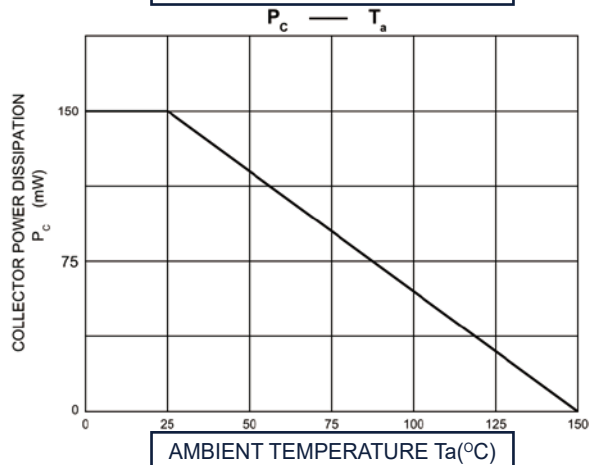
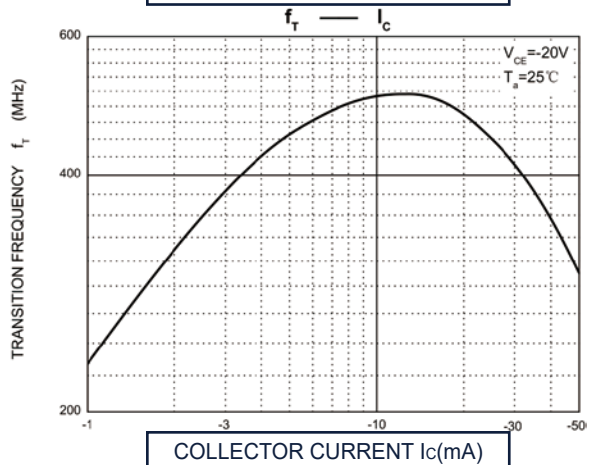
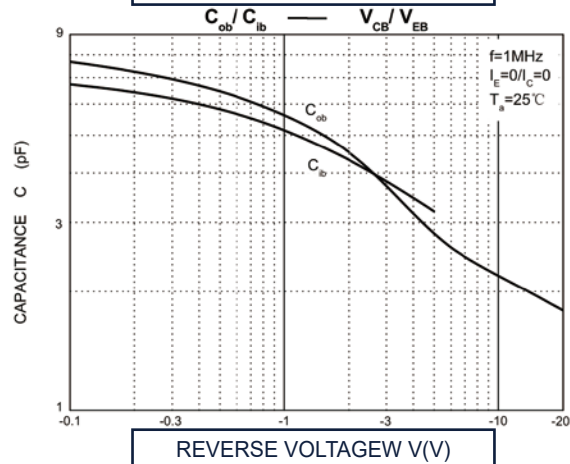
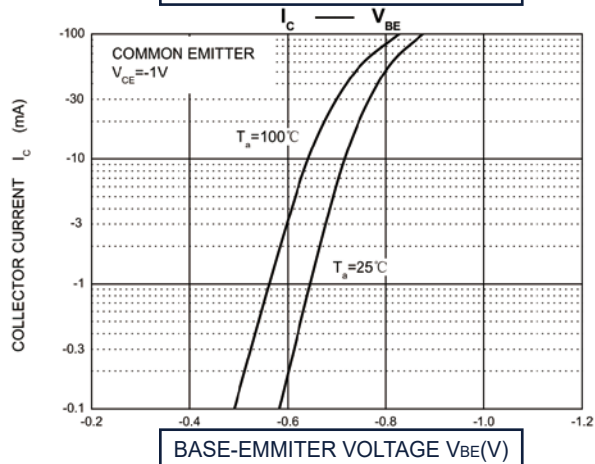
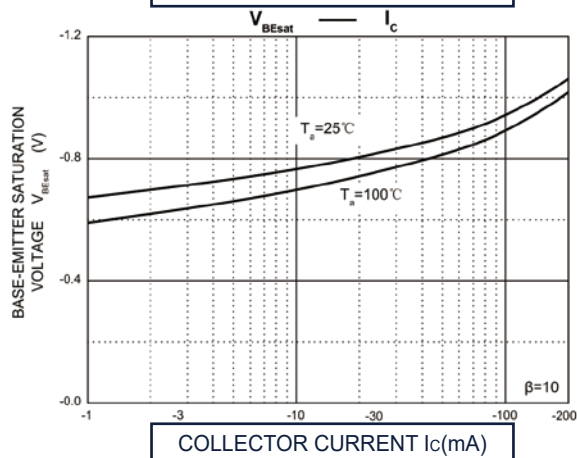
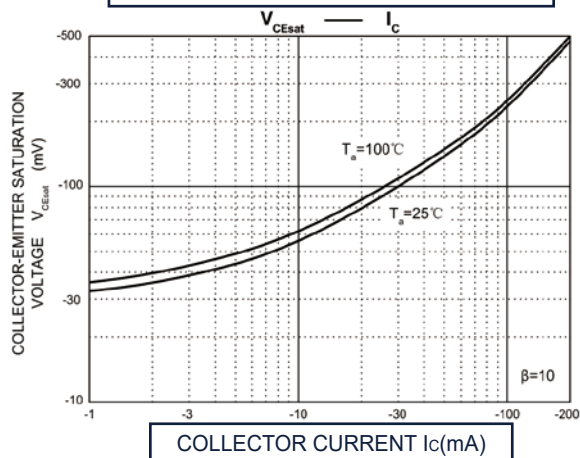
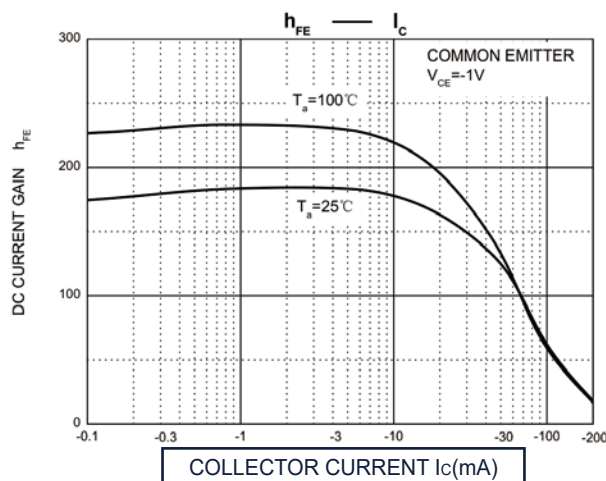
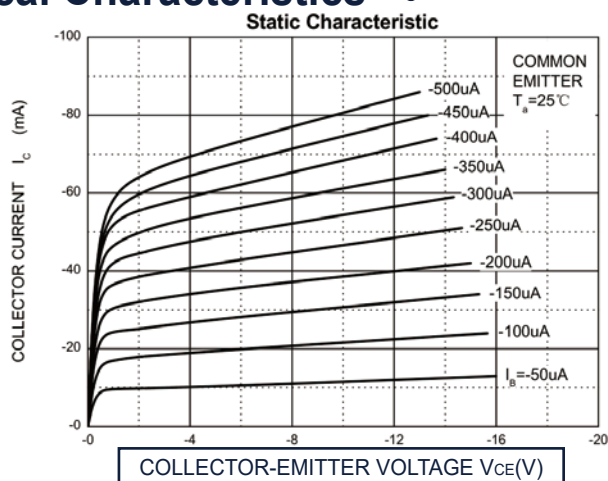


TWTLSEMI

TL-MMDT3906V

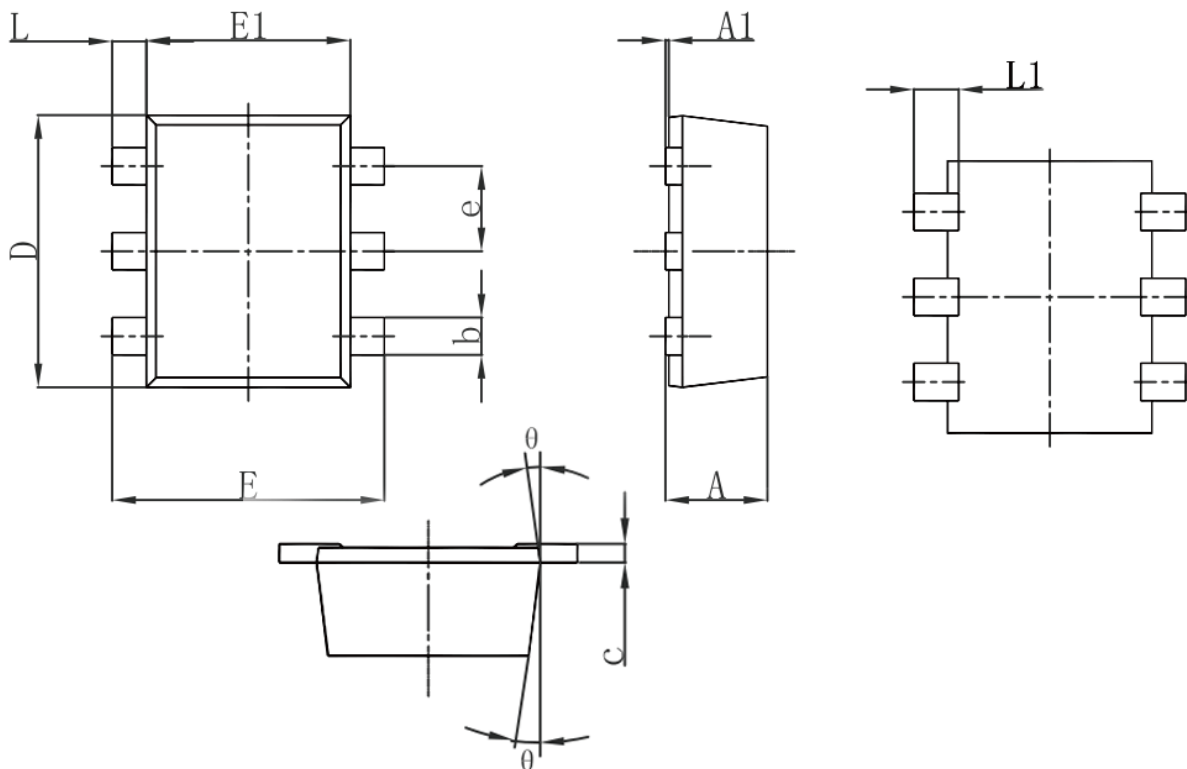
SOT-563 DUAL TRANSISTOR (PNP+PNP)

Typical Characteristics





SOT-563 Package Outline Dimensions



Symbol	Dimensions in Millimeters		Dimensions In Inches	
	Min	Max	Min	Max
A	0.525	0.600	0.021	0.024
A1	0.000	0.050	0.000	0.002
e	0.450	0.550	0.018	0.022
c	0.090	0.160	0.004	0.06
D	1.500	1.700	0.059	0.067
b	0.170	0.270	0.007	0.011
E1	1.100	1.300	0.043	0.051
E	1.500	1.700	0.059	0.067
L	0.100	0.300	0.004	0.012
L1	0.200	0.400	0.008	0.016
θ	10° REF.		10° REF.	