



# **PRODUCT DATA SHEET**



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Datasheet Re

Sample

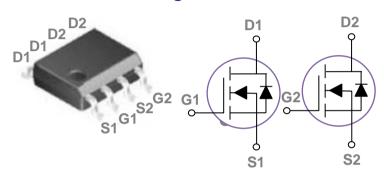
Please note: Please check the JINGAO Semiconductor website to verify the updated device numbers. The most current and up-to-date ordering information can be found at www.jg-semi.cn. Please email any questions regarding the system integration to JINGAO\_questions@jgsemi.com.



## **General Description**

These Dual P-Channel enhancement mode power field effect transistors are using trench DMOS technology. This advanced technology has been especially tailored to minimize on-state resistance, provide superior switching performance, and withstand high energy pulse in the avalanche and commutation mode. These devices are well suited for high efficiency fast switching applications.

<b>Dual</b>	SOP8	Pin	<b>Configuration</b>
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BVDSS	RDSON	ID	
-30V	45m $Ω$	-5.5A	

#### **Features**

- -30V,-5.5A, RDS(ON) =45mΩ@VGS = -10V
- Fast switching
- Green Device Available
- Suit for -4.5V Gate Drive Applications

### **Applications**

- Notebook
- Load Switch
- Battery Protection
- Hand-held Instruments

## **Absolute Maximum Ratings** Tc=25℃ unless otherwise noted

Symbol	Parameter	Rating	Units
V <sub>DS</sub>	Drain-Source Voltage	-30	V
V <sub>GS</sub>	Gate-Source Voltage	±20	V
I <sub>D</sub>	Drain Current – Continuous (T <sub>C</sub> =25°C)	<b>-</b> 5.5	А
	Drain Current – Continuous (T <sub>C</sub> =100°C)	-3.48	А
I <sub>DM</sub>	Drain Current – Pulsed <sup>1</sup>	-22	А
D	Power Dissipation (T <sub>C</sub> =25°C)	2.1	W
$P_D$	Power Dissipation – Derate above 25°C	0.017	W/°C
T <sub>STG</sub>	Storage Temperature Range	-55 to 150	°C
TJ	Operating Junction Temperature Range	-55 to 150	°C

#### **Thermal Characteristics**

Symbol	Symbol Parameter		Max.	Unit
$R_{\theta JA}$	Thermal Resistance Junction to ambient		60	°C/W



## **Electrical Characteristics** (T<sub>J</sub>=25 °C, unless otherwise noted)

#### **Off Characteristics**

Symbol	Parameter	Conditions	Min.	Тур.	Max.	Unit
BV <sub>DSS</sub>	Drain-Source Breakdown Voltage V <sub>GS</sub> =0V , I <sub>D</sub> =-250uA		<b>-</b> 30			V
$\triangle BV_{DSS}/\triangle T_{J}$	BV <sub>DSS</sub> Temperature Coefficient	Reference to 25°C , I <sub>D</sub> =-1mA		-0.03		V/°C
	Drain-Source Leakage Current	V <sub>DS</sub> =-30V , V <sub>GS</sub> =0V , T <sub>J</sub> =25°C			-1	uA
I <sub>DSS</sub>		V <sub>DS</sub> =-24V , V <sub>GS</sub> =0V , T <sub>J</sub> =125°C			-10	uA
I <sub>GSS</sub>	Gate-Source Leakage Current	$V_{GS}=\pm 20V$ , $V_{DS}=0V$			±100	nA

#### **On Characteristics**

D	Static Drain-Source On-Resistance	V <sub>GS</sub> =-10V , I <sub>D</sub> =-3A		45	55	mΩ
R <sub>DS(ON)</sub>		V <sub>GS</sub> = <b>-</b> 4.5V , I <sub>D</sub> = <b>-</b> 2A		65	80	mΩ
$V_{GS(th)}$	Gate Threshold Voltage	V V 1 050 A	-1.0	-1.6	<b>-</b> 2.5	V
$\triangle V_{GS(th)}$	$\triangle V_{GS(th)}$ $V_{GS(th)}$ Temperature Coefficient $V_{GS}=V_{DS}$ , $I_D=-250uA$			4		mV/°C
gfs	Forward Transconductance	V <sub>DS</sub> =-10V , I <sub>D</sub> =-3A		3.5		S

## **Dynamic and switching Characteristics**

Qg	Total Gate Charge <sup>2,3</sup>		 5.1	
Q <sub>gs</sub>	Gate-Source Charge <sup>2, 3</sup>	V <sub>DS</sub> =-15V , V <sub>GS</sub> =-4.5V , I <sub>D</sub> =-3A	 2	 nC
$Q_gd$	Gate-Drain Charge <sup>2,3</sup>		 2.2	
$T_{d(on)}$	Turn-On Delay Time <sup>2, 3</sup>		 3.4	
Tr	Rise Time <sup>2,3</sup>	$V_{DD}$ =-15V , $V_{GS}$ =-10V , $R_G$ =6 $\Omega$	 10.8	 20
$T_{d(off)}$	Turn-Off Delay Time <sup>2, 3</sup>	I <sub>D</sub> =-1A	 26.9	 ns
T <sub>f</sub>	Fall Time <sup>2,3</sup>		 6.9	
C <sub>iss</sub>	Input Capacitance		 560	
Coss	Output Capacitance	V <sub>DS</sub> = <b>-</b> 15V , V <sub>GS</sub> =0V , F=1MHz	 55	 pF
C <sub>rss</sub>	Reverse Transfer Capacitance		 40	

### **Drain-Source Diode Characteristics and Maximum Ratings**

Symbol	ymbol Parameter Conditions		Min.	Тур.	Max.	Unit
Is	Continuous Source Current	V -V -0V Force Current			<b>-</b> 5.5	Α
I <sub>SM</sub>	Pulsed Source Current	$V_G=V_D=0V$ , Force Current			-11	Α
$V_{SD}$	Diode Forward Voltage	V <sub>GS</sub> =0V , I <sub>S</sub> =-1A , T <sub>J</sub> =25°C			-1.2	V

#### Note:

- 1. Repetitive Rating : Pulsed width limited by maximum junction temperature.
- 2. The data tested by pulsed , pulse width  $\leq$  300us , duty cycle  $\leq$  2%.
- 3. Essentially independent of operating temperature.

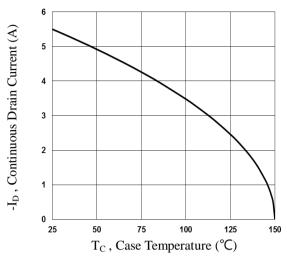


Fig.1 Continuous Drain Current vs. T<sub>c</sub>

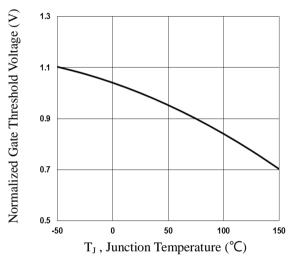


Fig.3 Normalized  $V_{th}$  vs.  $T_J$ 

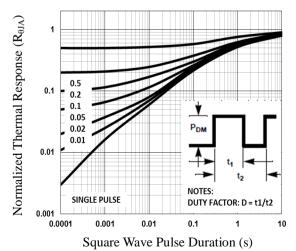


Fig.5 Normalized Transient Impedance

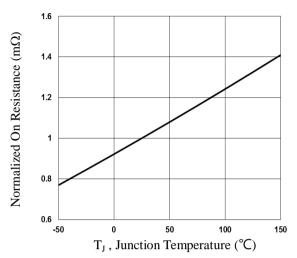


Fig.2 Normalized RDSON vs. T,

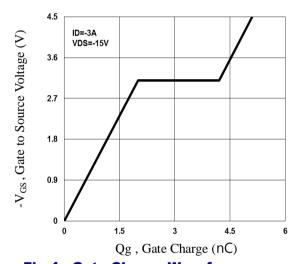


Fig.4 Gate Charge Waveform

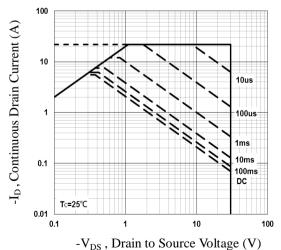
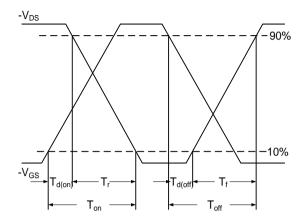


Fig.6 Maximum Safe Operation Area





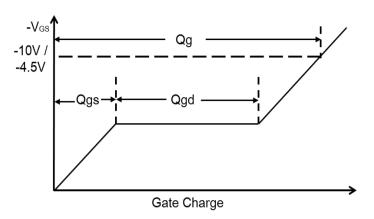
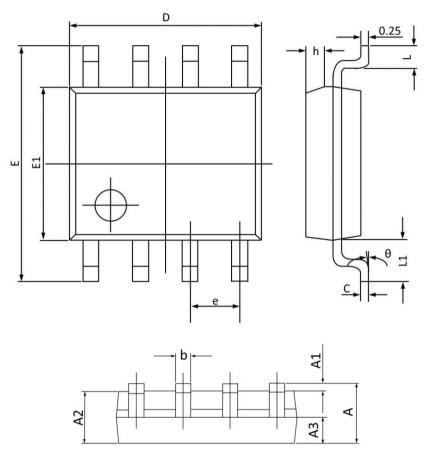


Fig.8 Gate Charge Waveform



# **SOP8 PACKAGE INFORMATION**



Crombal	Dimensions 1	In Millimeters	Dimension	s In Inches
Symbol	Min	Max	Min	Max
A	1.350	1.750	0.053	0.068
A1	0.100	0.250	0.004	0.009
A2	1.300	1.500	0.052	0.059
<b>A3</b>	0.600	0.700	0.024	0.027
b	0.390	0.480	0.016	0.018
c	0.210	0.260	0.009	0.010
D	4.700	5.100	0.186	0.200
E	5.800	6.200	0.229	0.244
<b>E</b> 1	3.700	4.100	0.146	0.161
e	1.270	O(BSC)	0.050	(BSC)
h	0.250	0.500	0.010	0.019
L	0.500	0.800	0.019	0.031
L1	1.050	O(BSC)	0.041(BSC)	
$\theta$	0°	8°	0°	8°



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