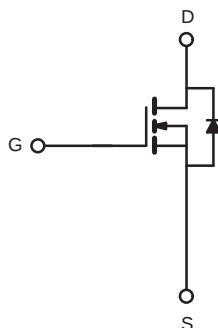
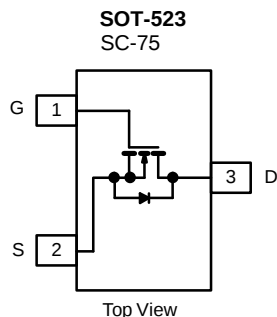


SSM3K7002BFS-VB Datasheet

N-Channel 60V (D-S) MOSFET

PRODUCT SUMMARY

V_{DS} (V)	$R_{DS(on)}$ (Ω)	I_D (mA)
60	1.2 at $V_{GS} = 10$ V	330



FEATURES

- Halogen-free According to IEC 61249-2-21 Definition
- Low On-Resistance: 2 Ω
- Low Threshold: 2 V (typ.)
- Low Input Capacitance: 25 pF
- Fast Switching Speed: 25 ns
- Low Input and Output Leakage
- Trench Power MOSFET
- Compliant to RoHS Directive 2002/95/EC



RoHS
COMPLIANT

BENEFITS

- Low Offset Voltage
- Low-Voltage Operation
- Easily Driven Without Buffer
- High-Speed Circuits
- Low Error Voltage

APPLICATIONS

- Direct Logic-Level Interface: TTL/CMOS
- Drivers: Relays, Solenoids, Lamps, Hammers, Display, Memories, Transistors, etc.
- Battery Operated Systems
- Solid-State Relays

ABSOLUTE MAXIMUM RATINGS $T_A = 25$ °C, unless otherwise noted

Parameter	Symbol	Limit	Unit
Drain-Source Voltage	V_{DS}	60	V
Gate-Source Voltage	V_{GS}	± 20	
Continuous Drain Current ($T_J = 150$ °C) ^b	$T_A = 25$ °C	I_D	mA
	$T_A = 100$ °C	330	
Pulsed Drain Current ^a	I_{DM}	800	
Power Dissipation ^b	$T_A = 25$ °C	P_D	W
	$T_A = 100$ °C	0.35	
Maximum Junction-to-Ambient ^b	R_{thJA}	350	°C/W
Operating Junction and Storage Temperature Range	T_J, T_{stg}	- 55 to 150	°C

Notes:

a. Pulse width limited by maximum junction temperature.

b. Surface Mounted on FR4 board.

SPECIFICATIONS T _A = 25 °C, unless otherwise noted						
Parameter	Symbol	Test Conditions	Limits			Unit
			Min.	Typ. ^a	Max.	
Static						
Drain-Source Breakdown Voltage	V _{DS}	V _{GS} = 0 V, I _D = 10 μA	60			V
Gate-Threshold Voltage	V _{GS(th)}	V _{DS} = V _{GS} , I _D = 250 μA	1		2.5	
Gate-Body Leakage	I _{GSS}	V _{DS} = 0 V, V _{GS} = ± 20 V			± 10	μA
		V _{DS} = 0 V, V _{GS} = ± 15 V			1	
		V _{DS} = 0 V, V _{GS} = ± 10 V			± 150	nA
		V _{DS} = 0 V, V _{GS} = ± 10 V, T _J = 85 °C			± 1000	
		V _{DS} = 0 V, V _{GS} = ± 5 V			± 100	
Zero Gate Voltage Drain Current	I _{DSS}	V _{DS} = 60 V, V _{GS} = 0 V			1	μA
		V _{DS} = 60 V, V _{GS} = 0 V , T _J = 125 °C			500	
On-State Drain Current ^a	I _{D(on)}	V _{GS} = 10 V, V _{DS} = 7.5 V	800			mA
		V _{GS} = 4.5 V, V _{DS} = 10 V	500			
Drain-Source On-Resistance ^a	R _{DS(on)}	V _{GS} = 10 V, I _D = 500 mA		1.2		Ω
		V _{GS} = 4.5 V, I _D = 200 mA		2		
Forward Transconductance ^a	g _{fs}	V _{DS} = 10 V, I _D = 200 mA	100			mS
Diode Forward Voltage	V _{SD}	I _S = 200 mA, V _{GS} = 0 V			1.3	V
Dynamic ^a						
Total Gate Charge	Q _g	V _{DS} = 10 V, V _{GS} = 4.5 V I _D ≅ 250 mA		0.4	0.6	nC
Input Capacitance	C _{iss}	V _{DS} = 25 V, V _{GS} = 0 V f = 1 MHz		30		pF
Output Capacitance	C _{oss}			6		
Reverse Transfer Capacitance	C _{rss}			2.5		
Switching ^{a, b, c}						
Turn-On Time	t _{d(on)}	V _{DD} = 30 V, R _L = 150 Ω			25	ns
Turn-Off Time	t _{d(off)}	I _D ≅ 200 mA, V _{GEN} = 10 V, R _G = 10 Ω			35	

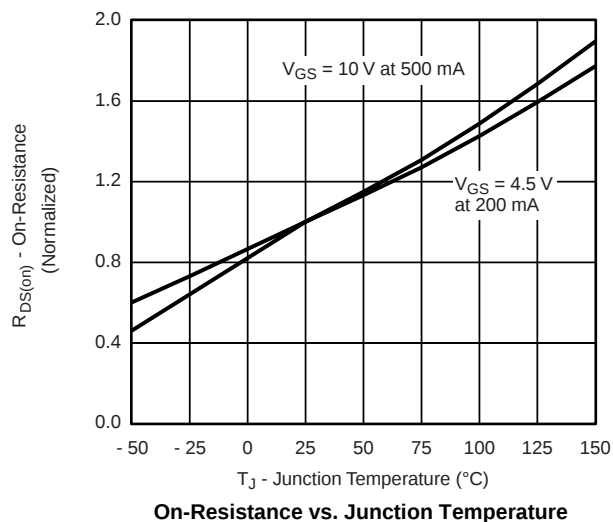
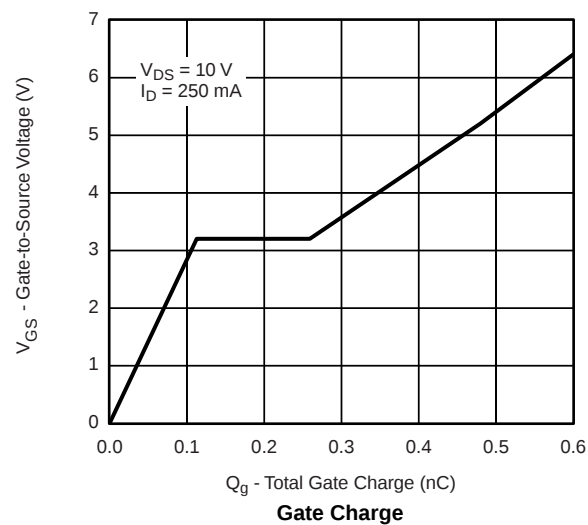
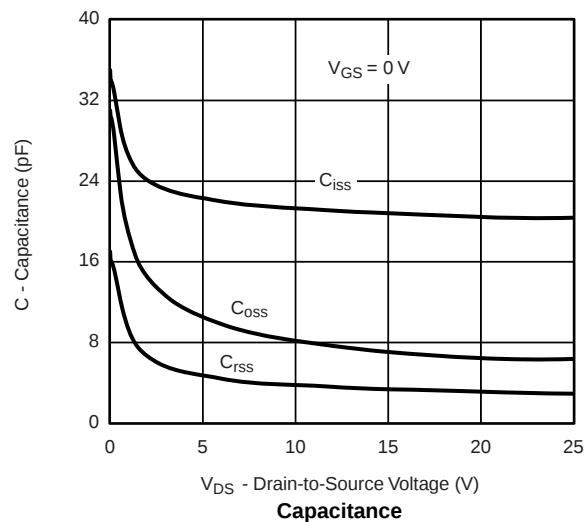
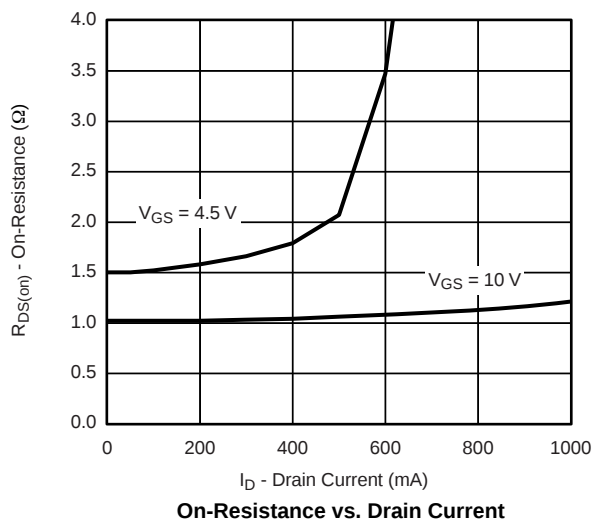
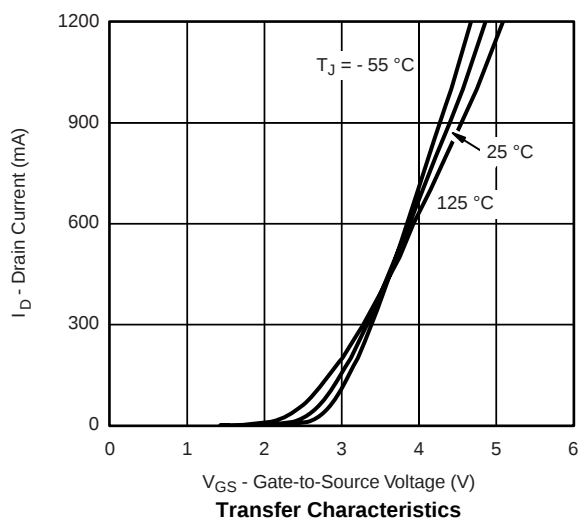
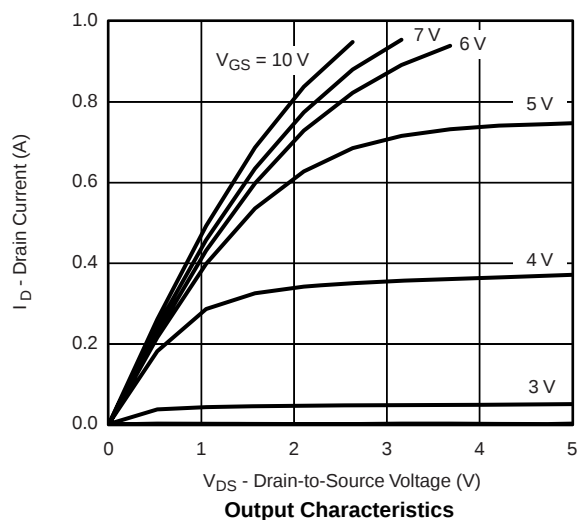
Notes:

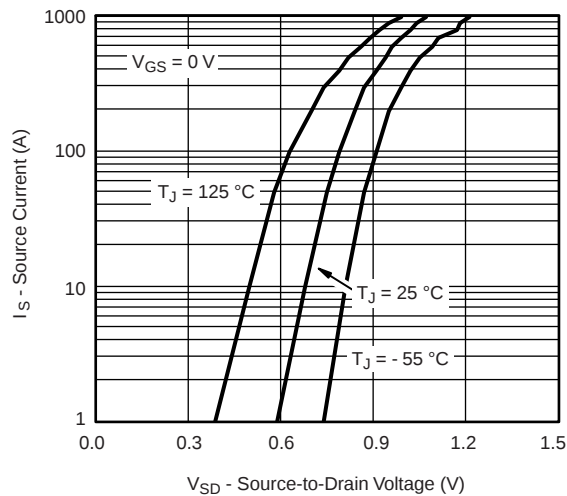
a. For DESIGN AID ONLY, not subject to production testing.

b. Pulse test: $PW \leq 300\text{ }\mu\text{s}$ duty cycle $\leq 2\%$.

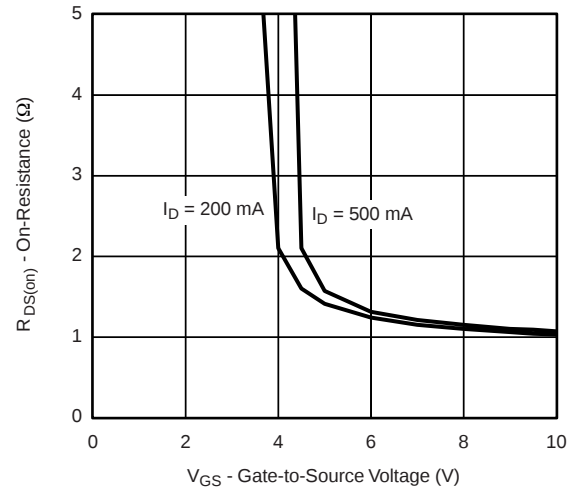
c. Switching time is essentially independent of operating temperature.

Stresses beyond those listed under "Absolute Maximum Ratings" may cause permanent damage to the device. These are stress ratings only, and functional operation of the device at these or any other conditions beyond those indicated in the operational sections of the specifications is not implied. Exposure to absolute maximum rating conditions for extended periods may affect device reliability.

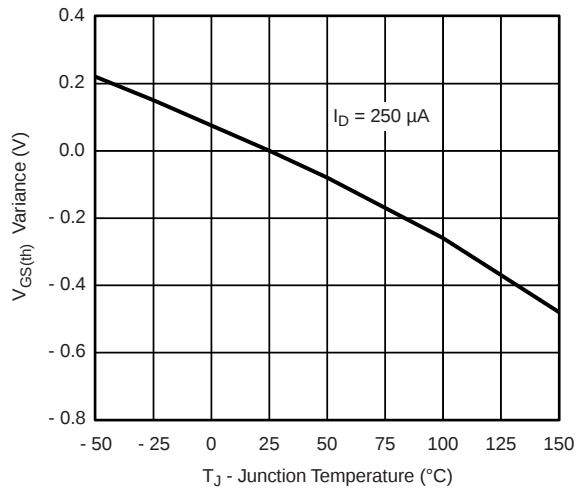
TYPICAL CHARACTERISTICS 25 °C, unless otherwise noted

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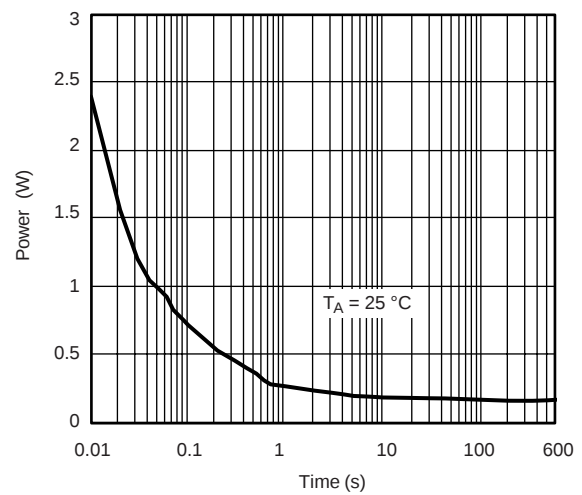
Source-Drain Diode Forward Voltage



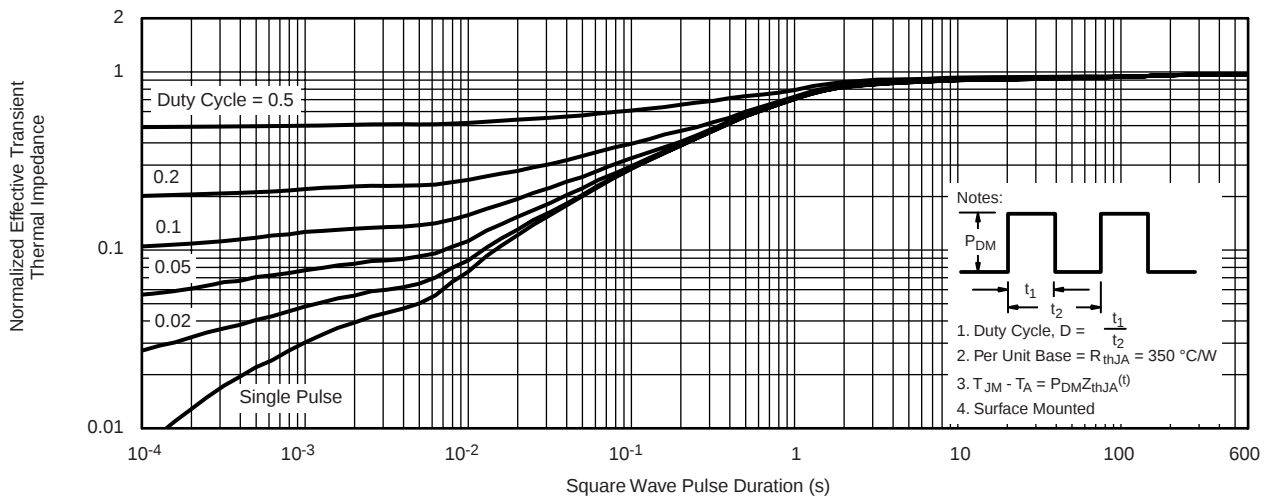
On-Resistance vs. Gate-Source Voltage



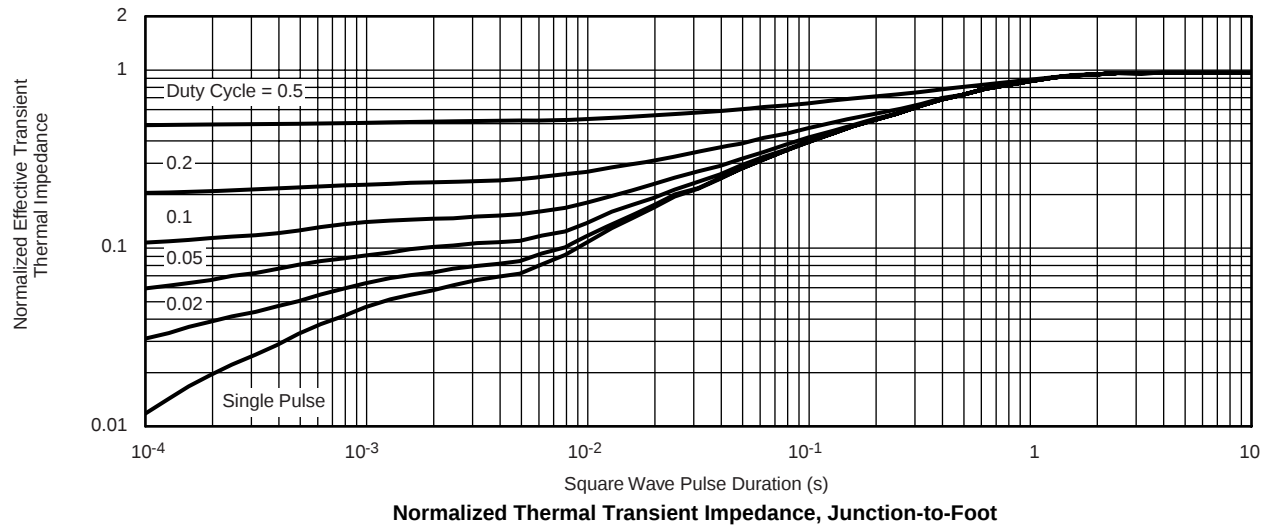
Threshold Voltage Variance Over Temperature



Single Pulse Power, Junction-to-Ambient

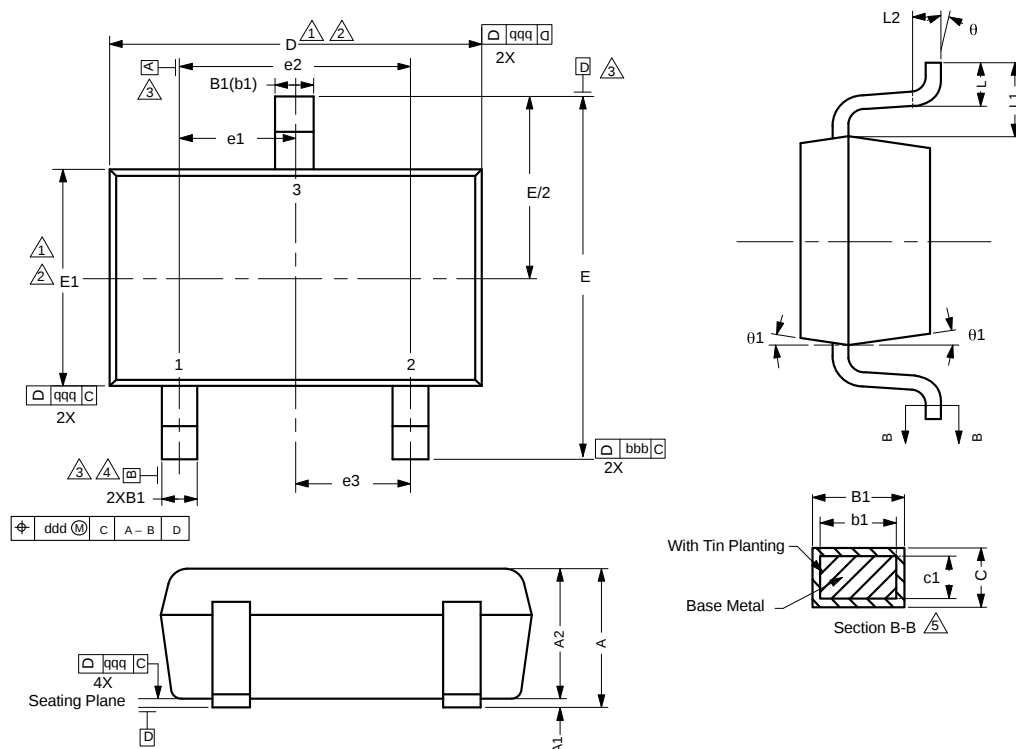


Normalized Thermal Transient Impedance, Junction-to-Ambient

THERMAL RATINGS ($T_A = 25\text{ }^{\circ}\text{C}$, unless otherwise noted)

Note

- The characteristics shown in the two graphs
 - Normalized Transient Thermal Impedance Junction-to-Ambient ($25\text{ }^{\circ}\text{C}$)
 - Normalized Transient Thermal Impedance Junction-to-Foot ($25\text{ }^{\circ}\text{C}$)
- are given for general guidelines only to enable the user to get a "ball park" indication of part capabilities. The data are extracted from single pulse transient thermal impedance characteristics which are developed from empirical measurements. The latter is valid for the part mounted on printed circuit board - FR4, size 1" x 1" x 0.062", double sided with 2 oz. copper, 100 % on both sides. The part capabilities can widely vary depending on actual application parameters and operating conditions.

SC-75A: 3 Leads



Notes

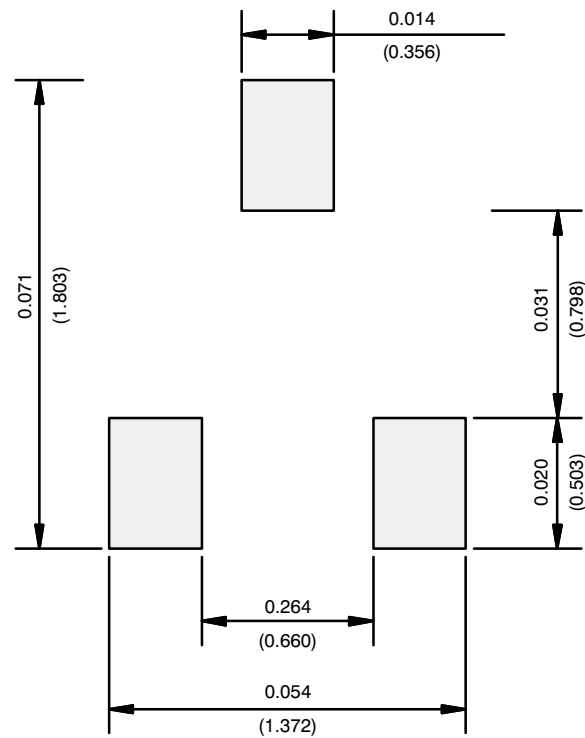
Dimensions in millimeters will govern.

1. Dimension D does not include mold flash, protrusions or gate burrs. Mold flash protrusions or gate burrs shall not exceed 0.10 mm per end. Dimension E1 does not include interlead flash or protrusion. Interlead flash or protrusion shall not exceed 0.10 mm per side.
2. Dimensions D and E1 are determined at the outmost extremes of the plastic body exclusive of mold flash, tie bar burrs, gate burrs and interlead flash, but including any mismatch between the top and bottom of the plastic body.
3. Datums A, B and D to be determined 0.10 mm from the lead tip.
4. Terminal positions are shown for reference only.
5. These dimensions apply to the flat section of the lead between 0.08 mm and 0.15 mm from the lead tip.

DIMENSIONS	TOLERANCES
aaa	0.10
bbb	0.10
ccc	0.10
ddd	0.10

DIM.	MILLIMETERS			NOTE
	MIN.	NOM.	MAX.	
A	-	-	0.80	
A ₁	0.00	-	0.10	
A ₂	0.65	0.70	0.80	
B ₁	0.19	-	0.24	5
b ₁	0.17	-	0.21	
c	0.13	-	0.15	5
c ₁	0.10	-	0.12	5
D	1.48	1.575	1.68	1, 2
E	1.50	1.60	1.70	
E ₁	0.66	0.76	0.86	1, 2
e ₁	0.50 BSC			
e ₂	1.00 BSC			
e ₃	0.50 BSC			
L	0.15	0.205	0.30	
L ₁	0.40 ref.			
L ₂	0.15 BSC			
θ	0°	-	8°	
θ ₁	4°	-	10°	

RECOMMENDED MINIMUM PADS FOR SC-75A: 3-Lead



Recommended Minimum Pads
Dimensions in Inches/(mm)

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