MSKSEMI 美森科













ESD

TVS

TSS

MOV

GDT

PIFD

AO3415A

Product specification





Features

- -20V,-4.5A, RDS(ON)=40mΩ@VGS=-4.5V
- Improved dv/dt capability
- Fast switching
- Green Device Available

Applications

- Notebook
- Load Switch
- Hend-Held Instruments

BVDSS	RDSON	ID
-20V	40mΩ	-4.5A

Reference News

PACKAGE OUTLINE	PIN Configuration	Marking
D	G	XF ** ặ
SOT-23-3L		

Absolute Maximum Ratings Tc=25℃ unless otherwise noted

Symbol	Parameter	Rating	Units
V _{DS}	Drain-Source Voltage	-20	V
V _G s	Gate-Source Voltage	±12	V
	Drain Current – Continuous (Tc=25℃)	-4.5	Α
lD	Drain Current – Continuous (T _C =100℃)	-2.7	Α
I _{DM}	Drain Current – Pulsed ¹	-18	Α
D	Power Dissipation (Tc=25℃)	1.5	W
P _D	Power Dissipation – Derate above 25℃	0.012	W/℃
T _{STG}	Storage Temperature Range	-55 to 150	$^{\circ}$
TJ	Operating Junction Temperature Range	-55 to 150	${\mathbb C}$

Thermal Characteristics

Symbol	Parameter	Тур.	Max.	Unit
Reja	Thermal Resistance Junction to ambient		80	°C/W



0Electrical Characteristics (TJ=25 °C, unless otherwise noted)

Off Characteristics

Symbol	Parameter	Conditions	Min.	Тур.	Max.	Unit
BV _{DSS}	Drain-Source Breakdown Voltage	V _{GS} =0V , I _D =-250uA	-20			V
△BV _{DSS} /△T _J	BV _{DSS} Temperature Coefficient	Reference to 25°C , I _D =-1mA		-0.02		V/°C
		V _{DS} =-20V , V _{GS} =0V , T _J =25℃			-1	uA
I _{DSS}	Drain-Source Leakage Current	V _{DS} =-16V , V _{GS} =0V , T _J =125℃			-10	uA
lgss	Gate-Source Leakage Current	V _{GS} = ±12V , V _{DS} =0V	1	<u></u>	±100	nA

On Characteristics

R _{DS(ON)}	Static Duain Course On Desistance	V _{GS} =-4.5V , I _D =-3A		40	52	mΩ
TVDS(ON)	Static Drain-Source On-Resistance	V _{GS} =-2.5V , I _D =-2A		47	65	
V _{GS(th)}	Gate Threshold Voltage					v
	Cate Threeheld Voltage		-0.3	-0.65	-1.0	
$^{\triangle}V_{GS(th)}$	V _{GS(th)} Temperature Coefficient	$V_{GS}=V_{DS}$, I_D =-250uA		2		mV/°C
gfs	Forward Transconductance	V _{DS} =-10V , I _S =-3A		7		S

Dynamic and switching Characteristics

Qg	Total Gate Charge ^{2, 3}		 9.6		
Qgs	Gate-Source Charge ^{2, 3}	V_{DS} =-10V , V_{GS} =-4.5V , I_{D} =-3A	 1.6		nC
Q _{gd}	Gate-Drain Charge ^{2, 3}		 2		
T _{d(on)}	Turn-On Delay Time ^{2,3}		 6	I	
Tr	Rise Time ^{2, 3}		 21.6		20
T _{d(off)}	Turn-Off Delay Time ^{2,3}	V _{DD} =-10V , V _{GS} =-4.5V ,	 51		nS
T _f	Fall Time ^{2,3}	R_G =25Ω I_D =-1A	 13.8		
Ciss	Input Capacitance		 850		
Coss	Output Capacitance	V _{DS} =-10V , V _{GS} =0V , F=1MHz	 70		pF
Crss	Reverse Transfer Capacitance		 55		

Drain-Source Diode Characteristics and Maximum Ratings

Symbol	Parameter	Conditions		Тур.	Max.	Unit
ls	Continuous Source Current	V _G =V _D =0V,Force Current			-4.5	Α
Ism	Pulsed Source Current	To the or the seasons and the			-9.0	Α
VsD	Diode Forward Voltage	V _{GS} =0V , I _S =-1A , T _J =25℃	ļ		-1.2	V

Note:

- 1. Repetitive Rating : Pulsed width limited by maximum junction temperature.
- 2. The data tested by pulsed, pulse width \leq 300us, duty cycle \leq 2%.
- 3. Essentially independent of operating temperature.

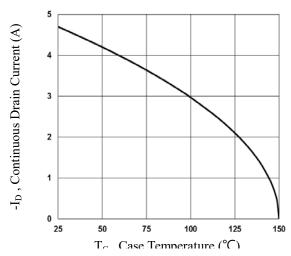


Fig.1 Continuous Drain Current vs. T_c

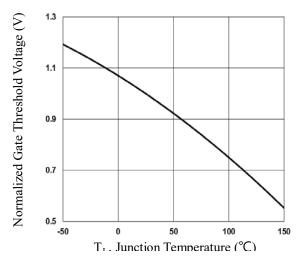


Fig.3 Normalized V_{th} vs. T_J

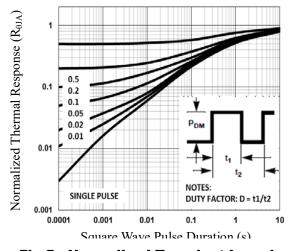


Fig.5 Normalized Transient Impedance

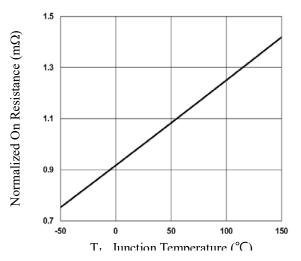


Fig.2 Normalized RDSON vs. T,

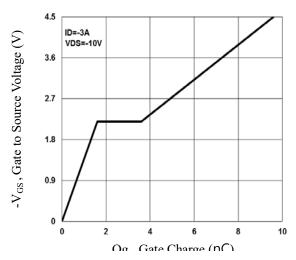


Fig.4 Gate Charge Waveform

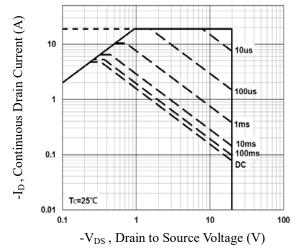
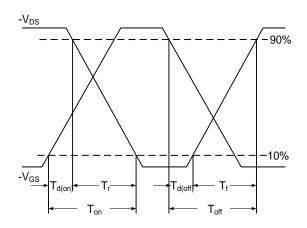


Fig.6 Maximum Safe Operation Area





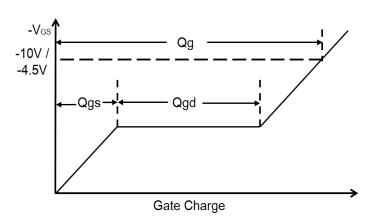
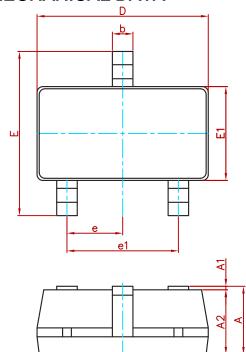
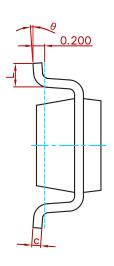


Fig.8 Gate Charge Waveform



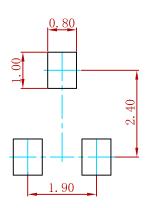
PACKAGE MECHANICAL DATA





Symbol	Dimensions In	Dimensions In Millimeters		s In Inches
Symbol	Min.	Max.	Min.	Max.
Α	1.050	1.250	0.041	0.049
A1	0.000	0.100	0.000	0.004
A2	1.050	1.150	0.041	0.045
b	0.300	0.500	0.012	0.020
С	0.100	0.200	0.004	0.008
D	2.820	3.020	0.111	0.119
E1	1.500	1.700	0.059	0.067
E	2.650	2.950	0.104	0.116
е	0.950(BSC)		0.037	(BSC)
e1	1.800	2.000	0.071	0.079
L	0.300	0.600	0.012	0.024
θ	0°	8°	0°	8°

Suugested Pad Layout



Note:

- 1.Controlling dimension:in millimeters.
- 2.General tolerance:± 0.05mm.
- 3. The pad layout is for reference purposes only.

REELSPECIFICATION

P/N	PKG	QTY
AO3415A	SOT-23-3L	3000



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