

IPP80P04P4L-04-VB Datasheet

P-Channel 40-V (D-S) MOSFET

PRODUCT SUMMARY

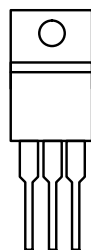
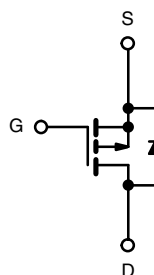
V_{DS} (V)	$r_{DS(on)}$ (Ω)	I_D (A) ^a	Q_g (Typ.)
- 40	0.0041 at $V_{GS} = - 10$ V	- 110	185 nC

FEATURES

- Trench Power MOSFET


RoHS
 COMPLIANT

TO-220AB


 G D S
 Top View


P-Channel MOSFET

ABSOLUTE MAXIMUM RATINGS $T_A = 25$ °C, unless otherwise noted

Parameter		Symbol	Limit	Unit
Drain-Source Voltage		V_{DS}	- 40	V
Gate-Source Voltage		V_{GS}	± 20	
Continuous Drain Current ($T_J = 175$ °C)	$T_C = 25$ °C	I_D	- 110 ^a	A
	$T_C = 70$ °C		- 110 ^a	
	$T_A = 25$ °C		39 ^{b, c}	
	$T_A = 70$ °C		33 ^{b, c}	
Pulsed Drain Current		I_{DM}	240	
Continuous Source-Drain Diode Current	$T_C = 25$ °C	I_S	110	
	$T_A = 25$ °C		10 ^{b, c}	
Avalanche Current	L = 0.1 mH	I_{AS}	75	mJ
Single-Pulse Avalanche Energy		E_{AS}	281	
Maximum Power Dissipation	$T_C = 25$ °C	P_D	375	W
	$T_C = 70$ °C		262	
	$T_A = 25$ °C		15 ^{b, c}	
	$T_A = 70$ °C		10.5 ^{b, c}	
Operating Junction and Storage Temperature Range		T_J, T_{stg}	- 55 to 175	°C
Soldering Recommendations (Peak Temperature) ^{d, e}			260	

THERMAL RESISTANCE RATINGS

Parameter		Symbol	Typical	Maximum	Unit
Maximum Junction-to-Ambient ^{b, d}	$t \leq 10$ s	R_{thJA}	8	10	°C/W
Maximum Junction-to-Case (Drain)	Steady State	R_{thJC}	0.33	0.4	

Notes:

a. Package limited.

b. Surface Mounted on 1" x 1" FR4 board.

c. $t = 10$ s.

d. Maximum under Steady State conditions is 40 °C/W.

SPECIFICATIONS T _J = 25 °C, unless otherwise noted						
Parameter	Symbol	Test Conditions	Min.	Typ.	Max.	Unit
Static						
Drain-Source Breakdown Voltage	V _{DS}	V _{GS} = 0 V, I _D = - 250 μA	- 40			V
V _{DS} Temperature Coefficient	ΔV _{DS} /T _J	I _D = - 250 μA		- 40		mV/°C
V _{GS(th)} Temperature Coefficient	ΔV _{GS(th)} /T _J			- 5.5		
Gate-Source Threshold Voltage	V _{GS(th)}	V _{DS} = V _{GS} , I _D = - 250 μA	- 2	- 3	- 4	V
Gate-Source Leakage	I _{GSS}	V _{DS} = 0 V, V _{GS} = ± 20 V			± 100	nA
Zero Gate Voltage Drain Current	I _{DSS}	V _{DS} = - 40 V, V _{GS} = 0 V			- 1	μA
		V _{DS} = - 40 V, V _{GS} = 0 V, T _J = 55 °C			- 10	
On-State Drain Current ^a	I _{D(on)}	V _{DS} ≥ 5 V, V _{GS} = - 10 V	- 120			A
Drain-Source On-State Resistance ^a	r _{DS(on)}	V _{GS} = - 10 V, I _D = - 20 A		0.0041		Ω
Forward Transconductance ^a	g _{fs}	V _{DS} = - 15 V, I _D = - 20 A		75		S
Dynamic ^b						
Input Capacitance	C _{iss}	V _{DS} = - 25 V, V _{GS} = 0 V, f = 1 MHz		11300		pF
Output Capacitance	C _{oss}			1510		
Reverse Transfer Capacitance	C _{rss}			1000		
Total Gate Charge	Q _g	V _{DS} = - 20 V, V _{GS} = - 10 V, I _D = - 110 A		185	280	nC
Gate-Source Charge	Q _{gs}			48		
Gate-Drain Charge	Q _{gd}			42		
Gate Resistance	R _g	f = 1 MHz		4.0		Ω
Turn-On Delay Time	t _{d(on)}	V _{DD} = - 20 V, R _L = 0.18 Ω I _D ≅ - 110 A, V _{GEN} = - 10 V, R _g = 1 Ω		25	40	ns
Rise Time	t _r			290	440	
Turn-Off Delay Time	t _{d(off)}			110	165	
Fall Time	t _f			35	55	
Drain-Source Body Diode Characteristics						
Continuous Source-Drain Diode Current	I _S	T _C = 25 °C			- 110	A
Pulse Diode Forward Current ^a	I _{SM}				- 240	
Body Diode Voltage	V _{SD}	I _S = - 20 A		- 0.8	- 1.5	V
Body Diode Reverse Recovery Time	t _{rr}	I _F = - 20 A, di/dt = 100 A/μs, T _J = 25 °C		70	105	ns
Body Diode Reverse Recovery Charge	Q _{rr}			130	200	nC
Reverse Recovery Fall Time	t _a			37		ns
Reverse Recovery Rise Time	t _b			33		

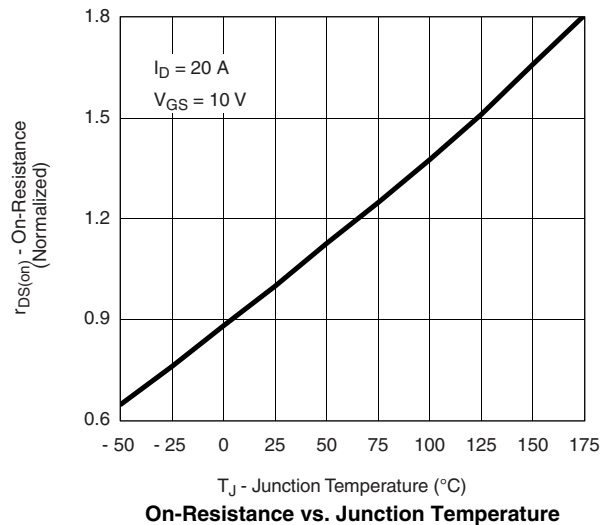
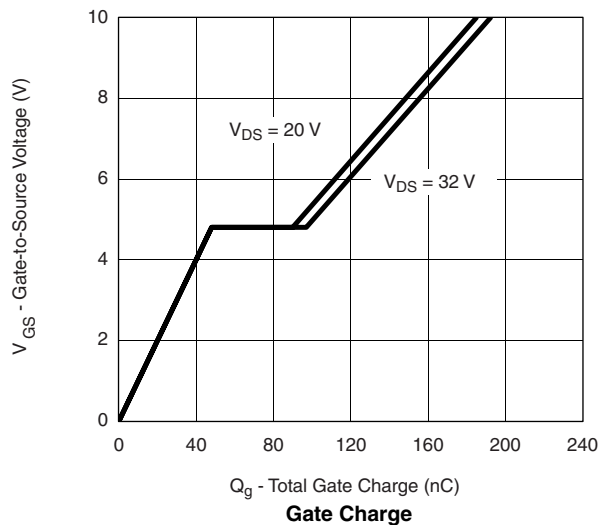
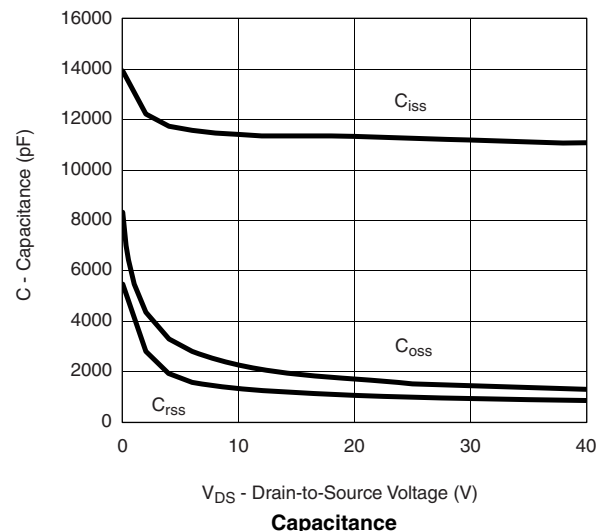
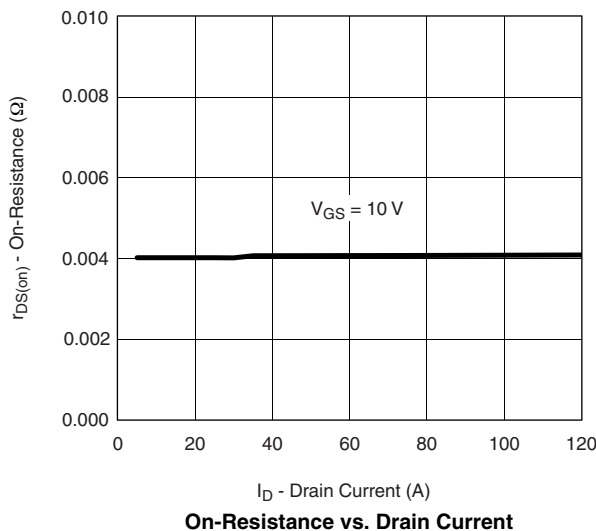
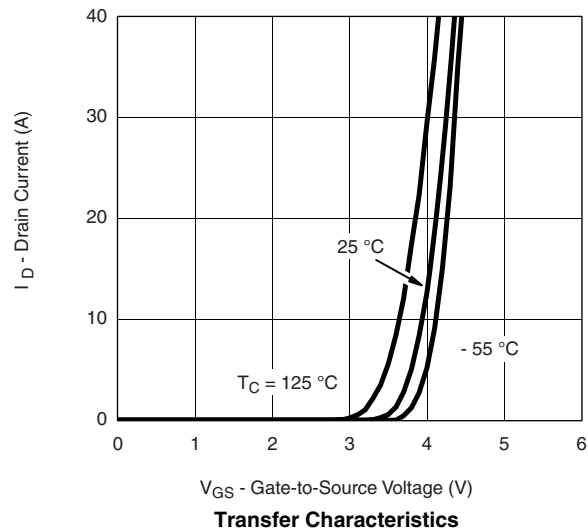
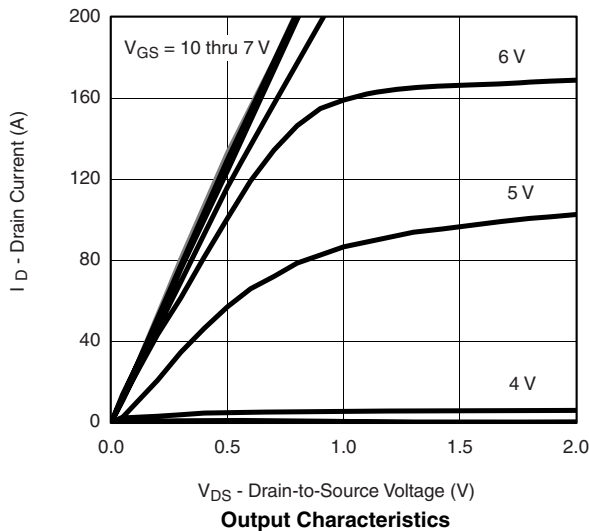
Notes:

a. Pulse test; pulse width $\leq 300\text{ }\mu\text{s}$, duty cycle $\leq 2\%$.

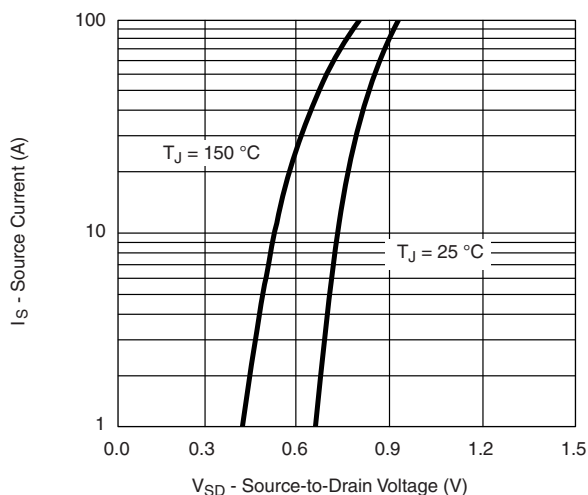
b. Guaranteed by design, not subject to production testing.

Stresses beyond those listed under "Absolute Maximum Ratings" may cause permanent damage to the device. These are stress ratings only, and functional operation of the device at these or any other conditions beyond those indicated in the operational sections of the specifications is not implied. Exposure to absolute maximum rating conditions for extended periods may affect device reliability.

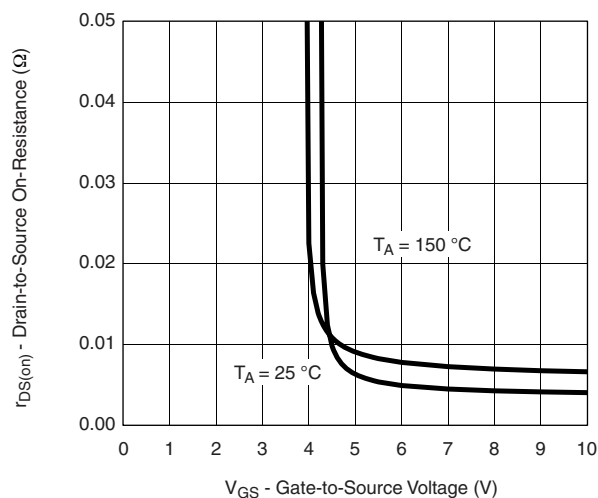
TYPICAL CHARACTERISTICS 25 °C, unless otherwise noted



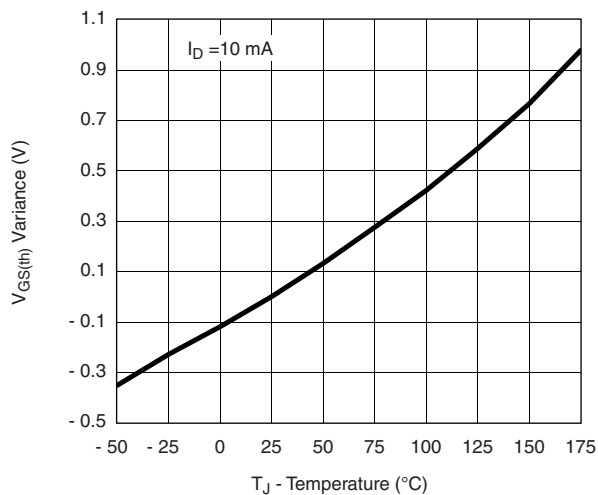
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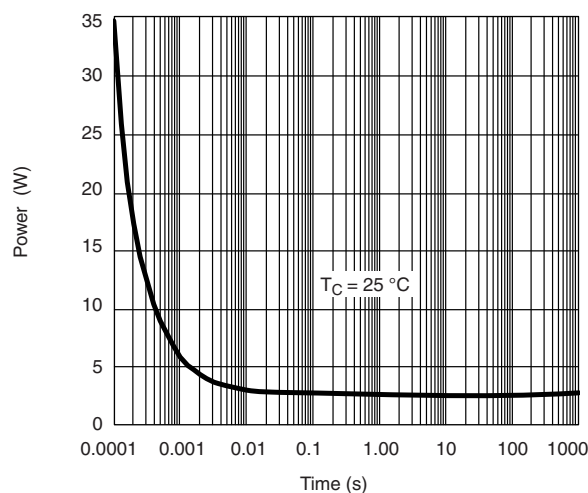
Source-Drain Diode Forward Voltage



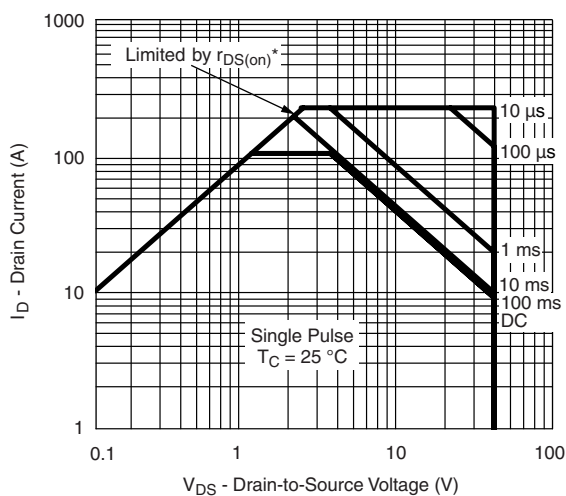
On-Resistance vs. Gate-to-Source Voltage



Threshold Voltage



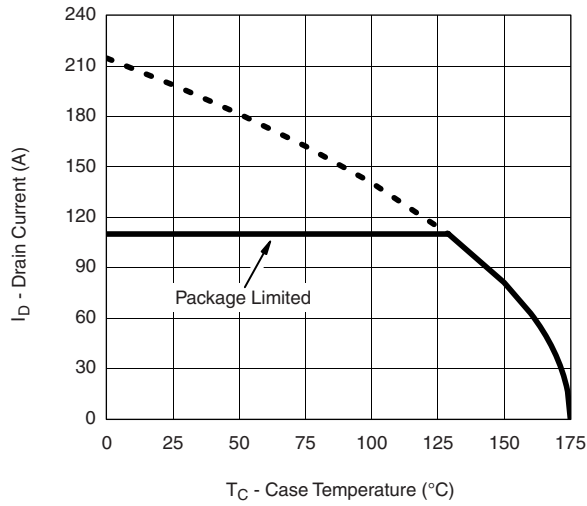
Single Pulse Power, Junction-to-Ambient



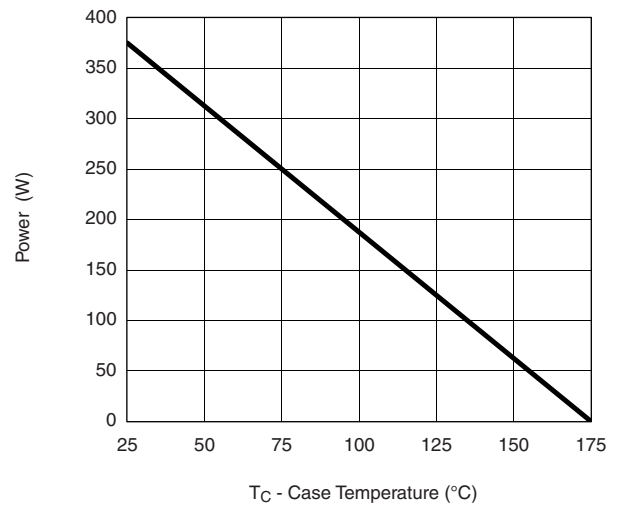
* $V_{GS} >$ minimum V_{GS} at which $r_{DS(on)}$ is specified

Safe Operating Area, Junction-to-Case

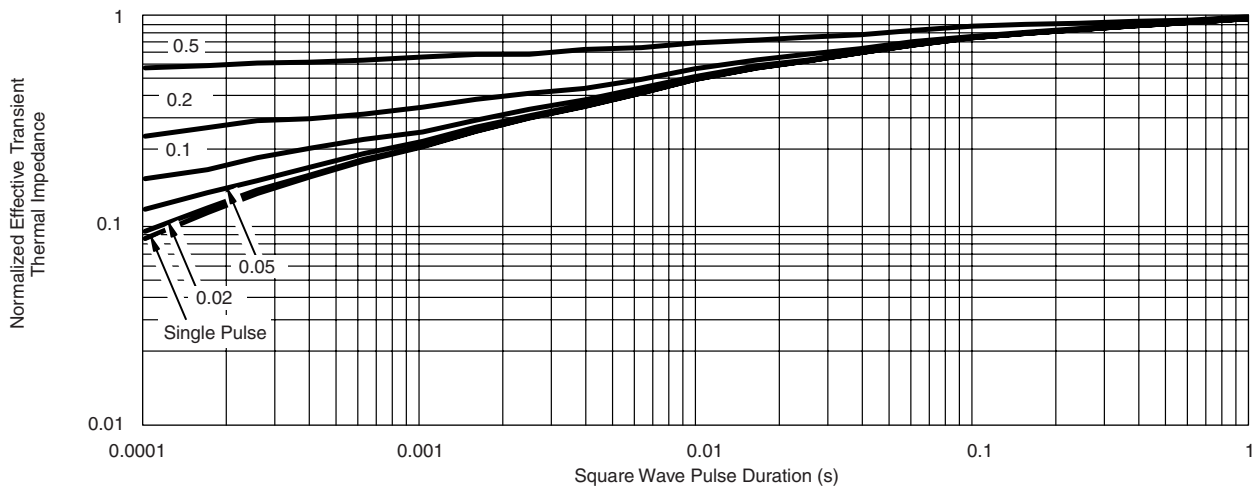
TYPICAL CHARACTERISTICS 25 °C, unless otherwise noted



Max. Avalanche and Drain Current vs. Case Temperature*



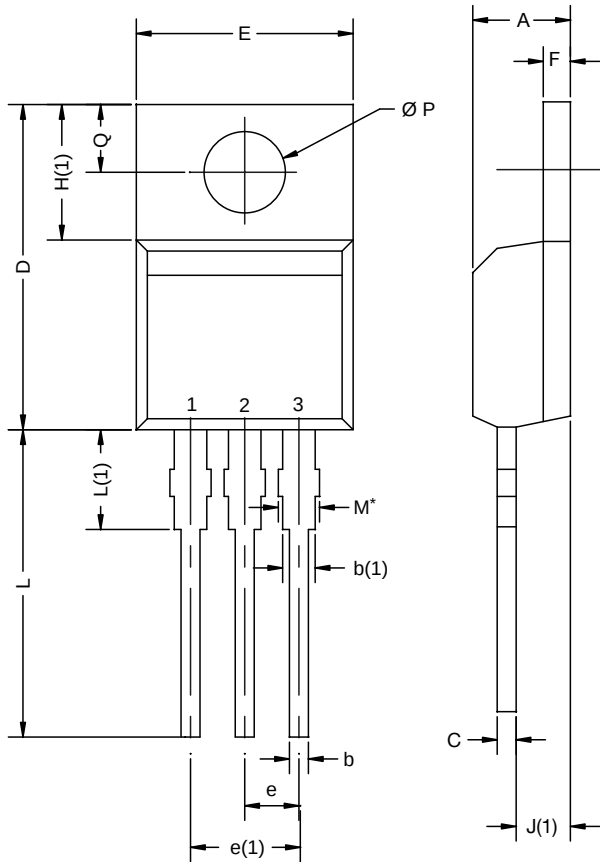
Power Derating, Junction-to-Case



Normalized Thermal Transient Impedance, Junction-to-Case

* The power dissipation P_D is based on $T_{J(max)} = 175$ °C, using junction-to-case thermal resistance, and is more useful in settling the upper dissipation limit for cases where additional heatsinking is used. It is used to determine the current rating, when this rating falls below the package limit.

TO-220AB



DIM.	MILLIMETERS		INCHES	
	MIN.	MAX.	MIN.	MAX.
A	4.25	4.65	0.167	0.183
b	0.69	1.01	0.027	0.040
b(1)	1.20	1.73	0.047	0.068
c	0.36	0.61	0.014	0.024
D	14.85	15.49	0.585	0.610
E	10.04	10.51	0.395	0.414
e	2.41	2.67	0.095	0.105
e(1)	4.88	5.28	0.192	0.208
F	1.14	1.40	0.045	0.055
H(1)	6.09	6.48	0.240	0.255
J(1)	2.41	2.92	0.095	0.115
L	13.35	14.02	0.526	0.552
L(1)	3.32	3.82	0.131	0.150
Ø P	3.54	3.94	0.139	0.155
Q	2.60	3.00	0.102	0.118

ECN: X12-0208-Rev. N, 08-Oct-12
DWG: 5471

Notes

* M = 1.32 mm to 1.62 mm (dimension including protrusion)
Heatsink hole for HVM

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