

Multilayer Ceramic Chip Capacitors

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Search by Part No.

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Tech Note

Technical Support Tool

FAQ

CLLE1AX7R1A104M050AC

?



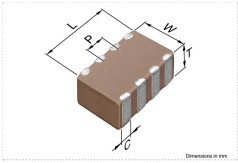
TDK item description ? CLLE1AX7R1A104MT****

Applications Commercial Grade

Feature Ultra Low Inductance

Series CLLE1A(2012) [EIA 0805]

Status Production (Not Recommended for New Design)



Images are for reference only and show exemplary products.

PDF file of this page

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Documents

- Catalog
- Specification
- RoHS Certificate
- SVHC/REACH Certificate
- Promotion Video
- Sample Kits

Size

Length(L)	2.00mm ±0.15mm
Width(W)	1.25mm ±0.15mm
Thickness(T)	0.50mm +0.05,-0.10mm
Terminal Width(C)	0.25mm Nom.
Terminal Spacing(P)	0.50mm Nom.
Recommended Land Pattern (PA)	0.50mm to 0.80mm
Recommended Land Pattern (PB)	0.30mm to 0.60mm
Recommended Land Pattern (PC)	0.30mm Nom.
Recommended Land Pattern (PP)	0.50mm Nom.

Electrical Characteristics

Capacitance	100nF ±20%
Rated Voltage	10VDC
Temperature Characteristic ?	X7R(±15%)
Dissipation Factor (Max.)	10%
Insulation Resistance (Min.)	1000MΩ

Other

Soldering Method	Reflow
AEC-Q200	No
Packing	Blister (Plastic)Taping [180mm Reel]
Package Quantity	4000pcs

Characteristic Graph (This is reference data, and does not guarantee the products characteristics.)

Impedance

no data available

Change settings

ESR

no data available

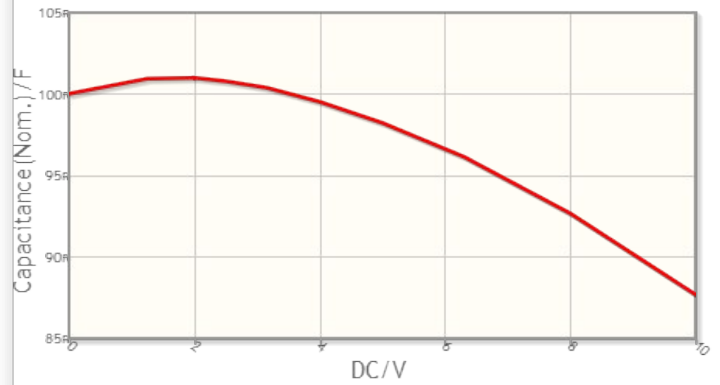
Change settings

Capacitance

no data available

Change settings

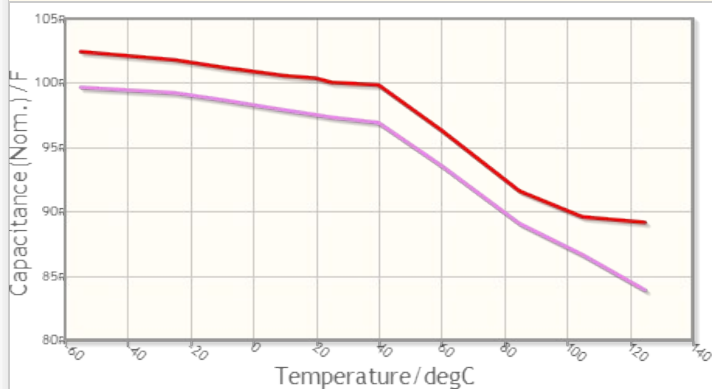
DC Bias Characteristic



CLLE1AX7R1A104M050AC

Change settings

Temperature Characteristic



CLLE1AX7R1A104M050AC(
No Bias)

CLLE1AX7R1A104M050AC(DC Bi
as = 5V)

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Ripple Temperature Rising

no data available

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| » RF / SAW Components and Modules | » Flash Storages |
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