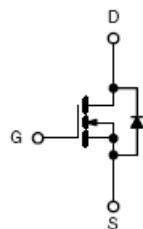


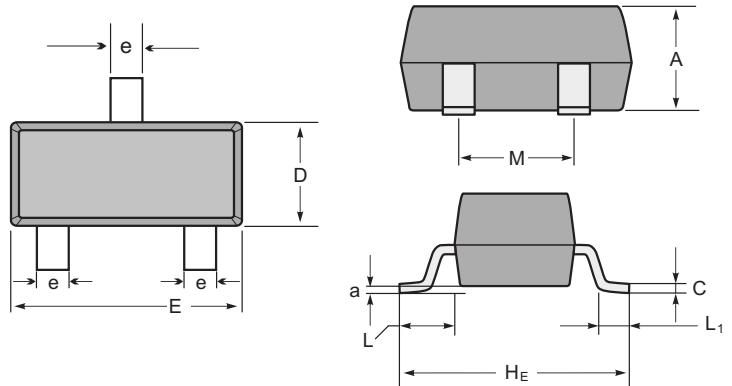
FEATURE

- High density cell design for low $R_{DS(ON)}$
- Voltage controlled small signal switch
- Rugged and reliable
- High saturation current capability

SOT-23

Equivalent Circuit

Marking

Type number	Marking code
SI2300	2300


SOT-23 mechanical data

	UNIT	A	C	D	E	H _E	e	M	L	L ₁	a
mm	max	1.1	0.15	1.4	3.0	2.6	0.5	1.95	0.55 (ref)	0.36 (ref)	0.0
	min	0.9	0.08	1.2	2.8	2.2	0.3	1.7			0.15
mil	max	43	6	55	118	102	20	77	22 (ref)	14 (ref)	0.0
	min	35	3	47	110	87	12	67			6

Maximum ratings ($T_a=25^\circ\text{C}$ unless otherwise noted)

Parameter	Symbol	Value		Unit
Drain-Source Voltage	V_{DS}	20	± 12	V
Gate-Source Voltage	V_{GS}			
Continuous Drain Current	I_D	6	25	A
Pulsed Drain Current	I_{DM}			
Maximum Body-Diode Continuous Current	I_S	2	1.25	W
Maximum Power Dissipation	P_D			
Thermal Resistance from Junction to Ambient	$R_{\theta JA}$	357	150	$^\circ\text{C}/\text{W}$
Junction Temperature	T_J			
Storage Temperature	T_{stg}			

SI2300

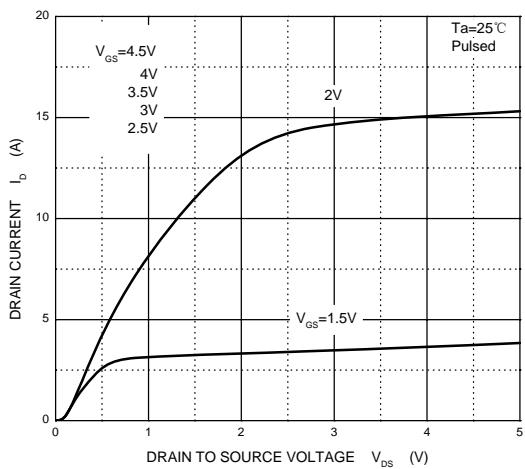
$T_a=25^\circ\text{C}$ unless otherwise specified

Parameter	Symbol	Test Condition	Min	Typ	Max	Unit
STATIC PARAMETERS						
Drain-source breakdown voltage	$V_{(\text{BR})\text{DSS}}$	$V_{\text{GS}} = 0\text{V}, I_D = 250\mu\text{A}$	20			V
Gate-source leakage current	I_{GSS}	$V_{\text{DS}} = 0\text{V}, V_{\text{GS}} = \pm 12\text{V}$			± 100	nA
Zero gate voltage drain current	I_{DSS}	$V_{\text{DS}} = 16\text{V}, V_{\text{GS}} = 0\text{V}$			1.0	μA
Gate threshold voltage	$V_{\text{GS}(\text{th})}$	$V_{\text{DS}} = V_{\text{GS}}, I_D = 250\mu\text{A}$	0.5	0.7	1.0	V
Drain-source on-state resistance	$R_{\text{DS}(\text{on})}$	$V_{\text{GS}} = 4.5\text{V}, I_D = 5.0\text{A}$		22	27	$\text{m}\Omega$
		$V_{\text{GS}} = 2.5\text{V}, I_D = 4.0\text{A}$		35	42	
		$V_{\text{GS}} = 1.8\text{V}, I_D = 2.0\text{A}$			73	
Diode forward voltage	V_{SD}	$V_{\text{GS}} = 0\text{V}, I_S = 1\text{A}$		0.75	1	V
Forward transconductance	g_{fs}	$V_{\text{DS}} = 5\text{V}, I_D = 3.8\text{A}$	4			S
DYNAMIC PARAMETERS*						
Input capacitance	C_{iss}	$V_{\text{DS}} = 10\text{V}, V_{\text{GS}} = 0\text{V}, f = 1\text{MHz}$		630		pF
Output capacitance	C_{oss}			164		
Reverse transfer capacitance	C_{rss}			137		
Gate resistance	R_g	$V_{\text{DS}} = 0\text{V}, V_{\text{GS}} = 0\text{V}, f = 1\text{MHz}$		1.5		Ω
SWITCHING PARAMETERS*						
Turn-on delay time	$t_{\text{d}(\text{on})}$	$V_{\text{GS}} = 5\text{V}, V_{\text{DS}} = 10\text{V}, R_L = 1.7\Omega, R_{\text{GEN}} = 6\Omega$		5.5		ns
Rise time	t_r			14		
Turn-off delay time	$t_{\text{d}(\text{off})}$			29		
Fall time	t_f			10.2		

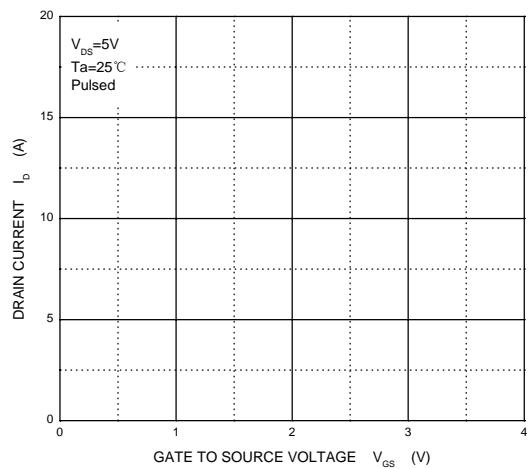
*These parameters have no way to verify.

RATING AND CHARACTERISTIC CURVES (SI2300)

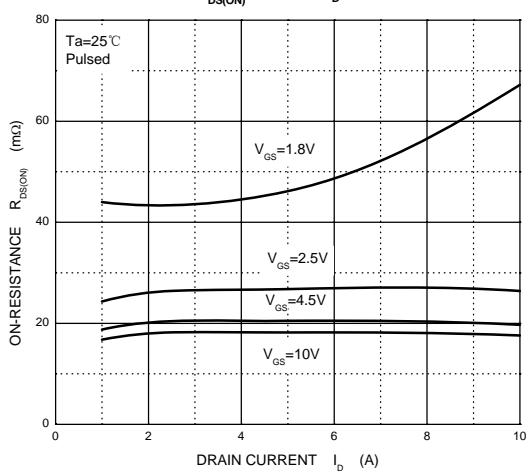
Output Characteristics



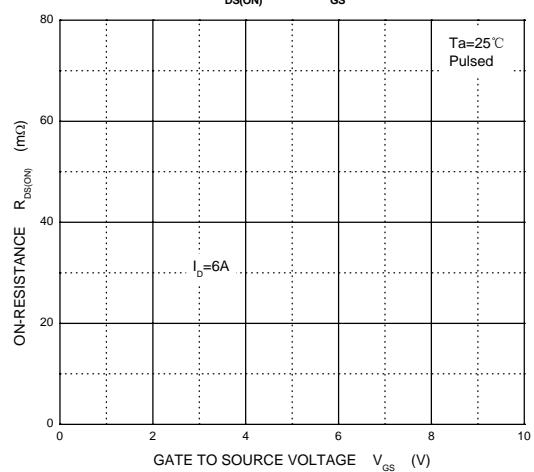
Transfer Characteristics



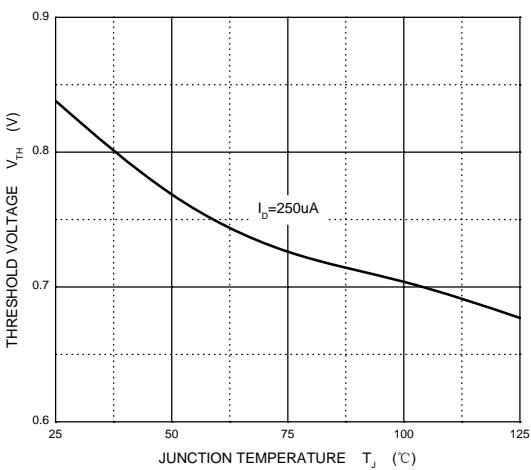
$R_{DS(ON)}$ — I_D



$R_{DS(ON)}$ — V_{GS}



Threshold Voltage



I_S — V_{SD}

