

JLSW400B170R62E7DN

62 mm module with TRENCHSTOP IGBT7 and emitter controlled 7 diode

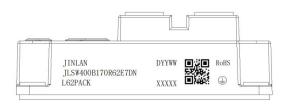
Features

- VCE(sat) with positive temperature coefficient
- Low inductance case
- Fast & soft reverse recovery anti-parallel FWD
- 10µs short circuit capability
- Low switching losses
- Isolated copper baseplate using DBC technology
- Standard housing



62 Pack

MARKING DIAGRAM



Typical Applications

- · Switching mode power supply
- · Inductive heating
- · Electronic welder

JINLAN

= Company Name

JLSW400B170R62E7DN

= Specific Device Code

YYWW

= Year and Work Week Code

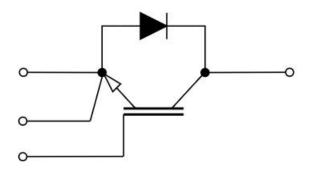
XXXXX

= Serial Number

QR code

= Custom Assembly Information

Description





Package Insulation coordination

Parameter	Symbol	Note or test condition	Values	Unit
Isolation test voltage	V _{ISOL}	RMS,f=50Hz,t=60s	4.0	kV
Internal isolation		basic insulation(class 1,IEC 61140)	Al ₂ O ₃	
Creepage distance	d _{creep}	terminal to heatsink	20	mm
Clearance	d _{clear}	terminal to heatsink	11	mm
Comparative tracking index (electrical)	CTI		≥200	
RTI Elec.	RTI	housing	≥175	$^{\circ}$

Package Characteristic values

Parameter	Symbol Note or test condition		Values			Unit
		Note or test condition	Min.	Тур.	Max.	
Storage Temperature Range	T _{STG}		-40		150	$^{\circ}$
Module lead resistance, terminals - chip	R _{CC*EE} '	Tc=25°C,per switch		0.50	-	mΩ
Mounting torque for module mounting		Terminal Connection Torque, Screw M4	1.1	-	2.0	
	М	Terminal Connection Torque, Screw M6	2.5		5.0	Nm
		Mounting Torque, Screw M6	3.0		5.0	
Weight	G			300		g



IGBT

Absolute Maximum Ratings (Tc = 25°C unless otherwise noted)

Symbol	Description	Value	Unit
V _{CES}	Collector-Emitter Voltage	1700	V
V _{GES}	Gate-Emitter Voltage ±30		V
Icdc	Continuous Collector Current @ Tc = 80°C (TJMAX = 175°C)	400	Α
I _{CM}	Pulsed Collector Current, t _p limited by T _{vj max} 800		Α
Tjmax	Maximum Junction Temperature 175		$^{\circ}$ C

Characteristics (Tc = 25°C unless otherwise noted)

Symbol	Parameter	Test Condition	Min	Тур	Max	Unit	
	Collector Freitter Cotunation Valtage	V _{GE} = 15 V, IC = 400 A, T _{vj} = 25°C		1.7	1.9	.,	
$V_{CE(sat)}$	Collector-Emitter Saturation Voltage	V _{GE} = 15 V, IC = 400 A, T _{vj} = 150°C		1.9		V	
$V_{\text{GE(TH)}}$	Gate-Emitter Threshold Voltage	$V_{GE} = V_{CE}$, $I_{C} = 18.0 \text{ mA}$, $T_{vj} = 25^{\circ}\text{C}$	5.0	5.75	6.5	٧	
I _{CES}	Collector-Emitter Cutoff Current	V _{GE} = 0 V, V _{CE} =V _{CES} ,T _{vj} = 25°C			4.0	mA	
I _{GES}	Gate-Emitter Leakage Current	$V_{GE} = V_{GES}$, $V_{CE} = 0$ V, $T_{vj} = 25$ °C	-		400	nA	
R _{Gint}	Internal Gate Resistance	f=1MHz		1.0		Ω	
Cies	Input Capacitance			8.4		nF	
Coes	Output Capacitance	$V_{CE}=25V,f=1MHz,$		6.2		nF	
C _{res}	Reverse Transfer	V _{GE} =0 V		1.4		nF	
Q _G	Gate Charge	V _{CC} = 900 V, V _{GE} = 15 V, I _C =400 A		4		μC	
$t_{d(on)}$	Turn-On Delay Time			0.36			
t _r	Rise Time			0.12		ns	
$t_{\text{d(off)}}$	Turn−off Delay Time	$V_{CE} = 900 \text{ V, } I_{C} = 400 \text{ A}$ $R_{G(on)} = 5 \Omega, R_{G(off)} = 5 \Omega$		0.70		113	
t_f	Fall Time	$V_{GE} = \pm 15V$	-	0.20			
Eon	Turn-On Switching Loss per Pulse	Inductive Load T _{vj} = 25°C		95			
E _{off}	Turn Off Switching Loss per Pulse			77		mJ	
Ets	Total Switching Loss			172			
$t_{d(on)}$	Turn-On Delay Time		-	TBD			
t _r	Rise Time		-	TBD			
t _{d(off)}	Turn-off Delay Time			TBD		ns	
tf	Fall Time	$V_{CE} = 900 \text{ V, } I_{C} = 400 \text{A}$ $R_{G(on)} = 5 \Omega, R_{G(off)} = 5 \Omega$		TBD			
Eon	Turn-on Switching Loss per Pulse	V _{GE} =±15V Inductive Load		TBD			
E _{off}	Turn Off Switching Loss per Pulse	T _{vj} = 150 °C	1	TBD		mJ	
Ets	Total Switching Loss		-	TBD			
I _{SC}	SC Data	t _{SC} ≤10 μs,V _{GE} =15V, T _{vj} =150℃,V _{CC} =100V		1800		Α	
R _{thJC}	Thermal resistance	Junction-to-Case (per IGBT)		TBD		K/W	
T _{vj op}		Temperature under switching conditions	-40		175 ¹⁾	$^{\circ}$	

 $^{^{1)}}T_{\nu j\,op}$ > 150 $^{\circ}{\rm C}$ is only allowed for operation at overload conditions.



Diodes

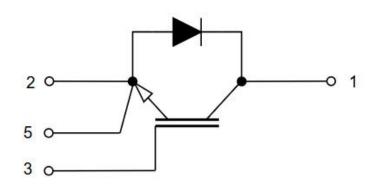
Absolute Maximum Ratings (Tc = 25°C unless otherwise noted)

Symbol	Description	Value	Unit
V _{RRM}	Repetitive Peak Reverse Voltage	1700	V
I _F	Diode Continuous Forward Current @ T _C =80 ℃ 40		Α
I _{FRM}	Diode Maximum Forward Current t _p =1ms 800		Α

Symbol	Parameter	Test Condition	Min	Тур	Max	Unit
		I _F =400 A, V _{GE} =0 V, T _{vj} = 25°C		2.90	3.60	
VF	Diode Forward Voltage	I _F = 400 A, V _{GE} =0V, T _{vj} = 150°C		2.80		V
Qr	Recovered Charge	V _{CC} = 900 V, I _C = 400 A	1	28.8		μC
IRM	Peak Reverse Recovery Current	$R_{Gon} = 5 \Omega, R_{Goff} = 5 \Omega$ $V_{GF} = \pm 15 V$	1	300		Α
E _{rec}	Reverse Recovery Energy	Inductive Load T _{vj} = 25°C		10.4		mJ
Qr	Recovered Charge	V _{CC} = 900 V, I _C = 400 A		TBD		μC
IRM	Peak Reverse Recovery Current	$R_{Gon} = 5\Omega, R_{Goff} = 5\Omega$ $V_{GF} = \pm 15V$	-	TBD		Α
E _{rec}	Reverse Recovery Energy	Inductive Load T _v j = 150°C	-	TBD		mJ
R _{thJC}	Thermal resistance	Junction-to-Case (per diode)	-	TBD		K/W
T _{vj op}		Temperature under switching conditions	-40		175 ²⁾	$^{\circ}$

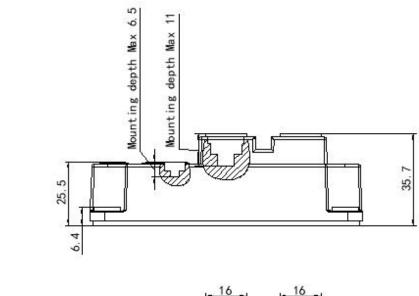
 $^{^{2)}\!}T_{\nu j\,op}\!>\!150\,^{\circ}\!\!\mathrm{C}$ is only allowed for operation at overload conditions.

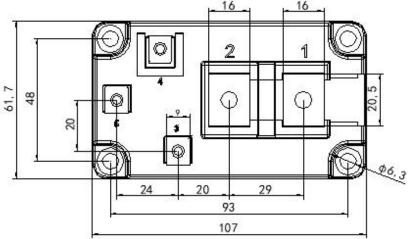
CIRCUIT DIAGRAM





PACKAGE DIMENSION





REVISION HISTORY

Document version	Date of release	Description of changes
Rev.00	2025-06-06	Preview



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