



# 180405275 Blue Gecko SoC CSP (EFR32BG1) Errata Update

**Bulletin Issue Date:** 4/5/2018

**Effective Date:** 4/5/2018

## Description of Change

Silicon Labs is pleased to announce Errata Rev 0.3 for the WLCSP package of EFR32BG1 Blue Gecko Bluetooth® Low Energy SoC. Revision 0.2 of the Errata created on July 21st, 2017 was not released publicly. This revision added the following errata items:

- \* ADC\_E224 – ADC Warm-Up Ready Can Cause ACMP to Not Function
- \* ADC\_E226 – SCANSTOP Does Not Immediately Stop the Ongoing Sample
- \* ADC\_E227 – New Conversion Triggers Cause Jitter to the Ongoing Conversions
- \* DBG\_E204 – Debug Recovery with JTAG Does Not Work
- \* DCDC\_E205 – Delay Required after Enabling the DC-DC Before Entering EM2/3/4H
- \* DCDC\_E206 – Reset During Radio Operation With DC-DC Results in DVDD Brown Out
- \* EMU\_E215 – Device May Brown Out After Energy Mode Transition
- \* EMU\_E216 – EM4H I/O Retention Cannot Be Disabled
- \* RMU\_E202 – External Debug Access Not Available After Watchdog or Lockup Full Reset
- \* RTCC\_E203 – Potential Stability Issue with RTCC Registers
- \* USART\_E202 – Incorrect 8-bit Timer Operation in Asynchronous Mode

Revision 0.3 does not add any new errata, it includes workaround descriptions for the following errata items:

- \* EMU\_E210 – Potential Power-Down When Entering EM2
- \* RMU\_E202 – External Debug Access Not Available After Watchdog or Lockup Full Reset

## Reason for Change

Announce EFR32BG1 CSP Errata.

## Product Identification

EFR32BG1P332F256GJ43-C0  
EFR32BG1B232F256GJ43-C0  
EFR32BG1V132F256GJ43-C0

This change is considered a minor change which does not affect form, fit, function, quality, or reliability. The information is being provided as a customer courtesy.

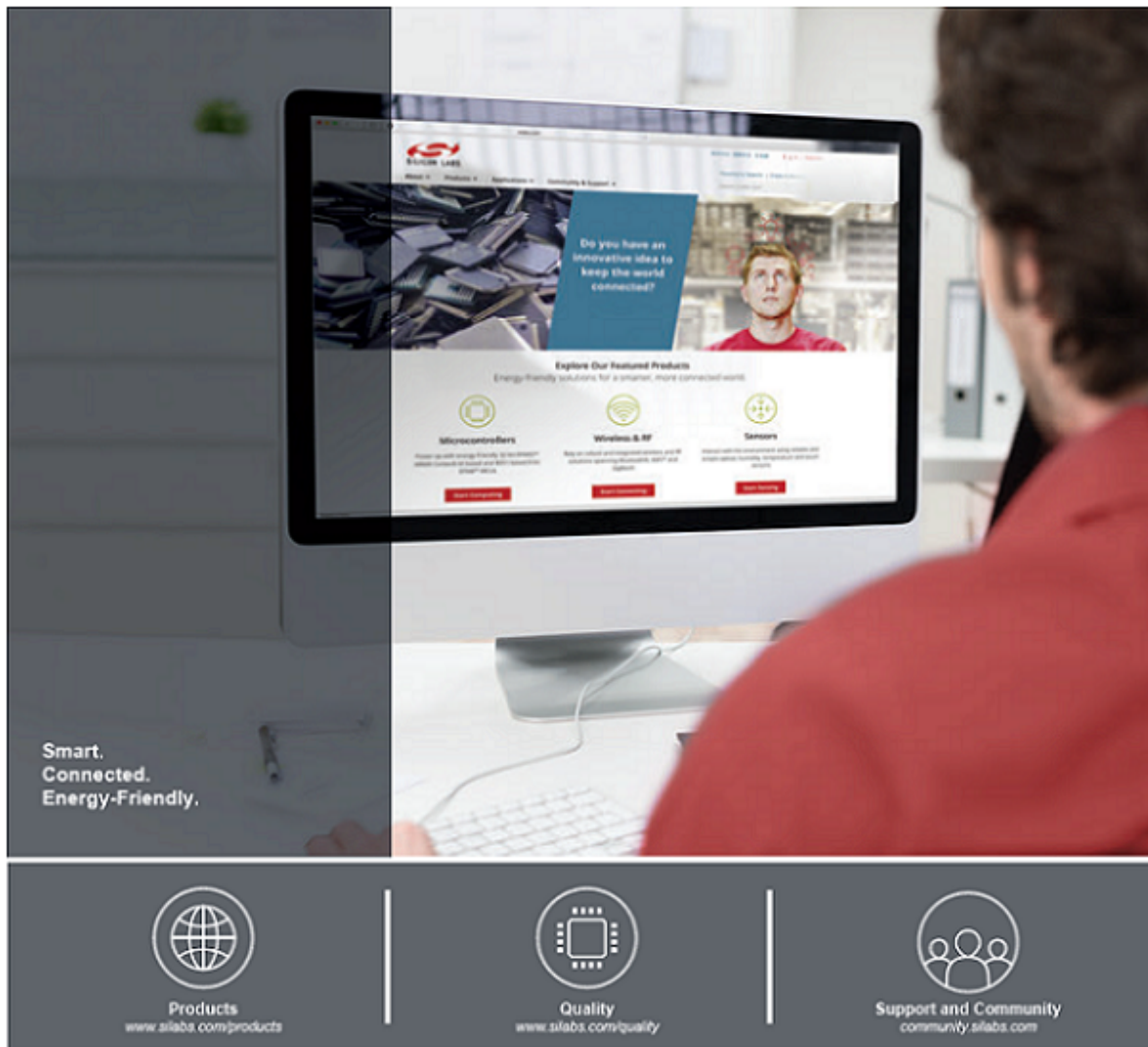
Please contact your local Silicon Labs sales representative with any questions about this notification. A list of Silicon Labs sales representatives may be found at <http://www.silabs.com>.

## Customer Actions Needed:

Review Blue Gecko SoC CSP (EFR32BG1) Errata Rev 0.3.

## User Registration

Register today to create your account on Silabs.com. Your personalized profile allows you to receive technical document updates, new product announcements, “how-to” and design documents, product change notices (PCN) and other valuable content available only to registered users. <http://www.silabs.com/profile>



#### Disclaimer

Silicon Labs intends to provide customers with the latest, accurate, and in-depth documentation of all peripherals and modules available for system and software implementers using or intending to use the Silicon Labs products. Characterization data, available modules and peripherals, memory sizes and memory addresses refer to each specific device, and "Typical" parameters provided can and do vary in different applications. Application examples described herein are for illustrative purposes only. Silicon Labs reserves the right to make changes without further notice and limitation to product information, specifications, and descriptions herein, and does not give warranties as to the accuracy or completeness of the included information. Silicon Labs shall have no liability for the consequences of use of the information supplied herein. This document does not imply or express copyright licenses granted hereunder to design or fabricate any integrated circuits. The products are not designed or authorized to be used within any Life Support System without the specific written consent of Silicon Labs. A "Life Support System" is any product or system intended to support or sustain life and/or health, which, if it fails, can be reasonably expected to result in significant personal injury or death. Silicon Labs products are not designed or authorized for military applications. Silicon Labs products shall under no circumstances be used in weapons of mass destruction including (but not limited to) nuclear, biological or chemical weapons, or missiles capable of delivering such weapons.

#### Trademark Information

Silicon Laboratories Inc.®, Silicon Laboratories®, Silicon Labs®, SiLabs® and the Silicon Labs logo®, Bluegiga®, Bluegiga Logo®, Clockbuilder®, CMEMS®, DSPLL®, EFM®, EFM32®, EFR, Ember®, Energy Micro, Energy Micro logo and combinations thereof, "the world's most energy friendly microcontrollers", Ember®, EZLink®, EZRadio®, EZRadioPRO®, Gecko®, ISOModem®, Micrium, Precision32®, ProSLIC®, Simplicity Studio®, SiPHY®, Telegesis, the Telegesis Logo®, USBXpress®, Zentri and others are trademarks or registered trademarks of Silicon Labs. ARM, CORTEX, Cortex-M3 and THUMB are trademarks or registered trademarks of ARM Holdings. Keil is a registered trademark of ARM Limited. All other products or brand names mentioned herein are trademarks of their respective holders.



Silicon Laboratories Inc.  
400 West Cesar Chavez  
Austin, TX 78701

<http://www.silabs.com>