

LTCC 低通滤波器 (Low Pass Filter)

HT-LFCN-1325+

Features

- Low insertion loss.
- Good rejection.
- LTCC Construction.
- temperature stable.
- Small size.

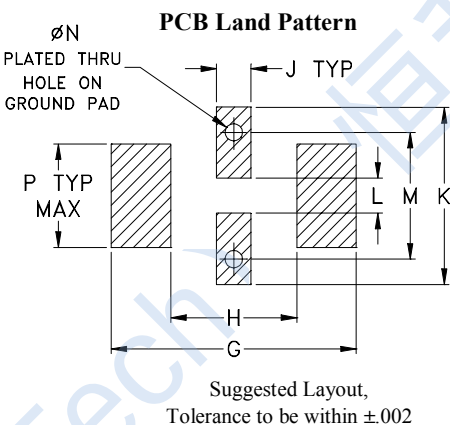
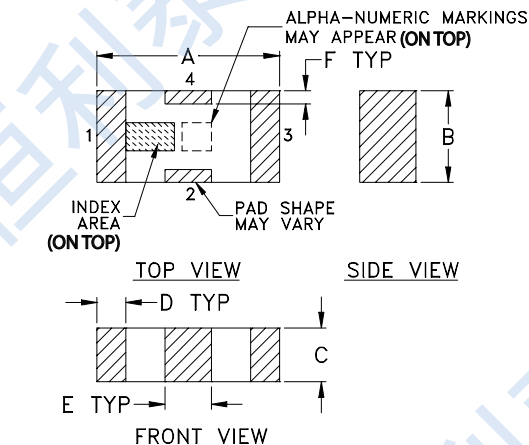
Applications

- Harmonic rejection.
- VHF/UHF transmitters/receivers.
- Lab use.

Pad Connections

RF IN	1
RF OUT	3
GROUND	2,4

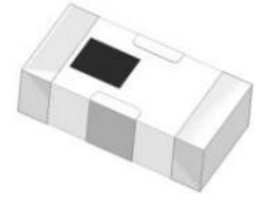
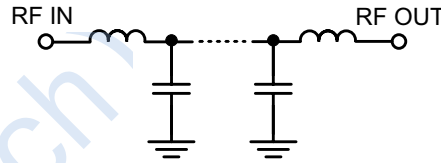
Outline Drawing



Outline Dimensions : inch mm

A	B	C	D	E	F	G	H	J	K	L	M	N	P	wt
.126	.063	.037	.020	.032	.009	.169	.087	.024	.122	.024	.087	.012	.071	grams
3.20	1.60	0.94	0.51	0.81	0.23	4.29	2.21	0.61	3.10	0.61	2.21	0.30	1.80	.020

Functional Schematic



50 Ω
DC to 1325MHz

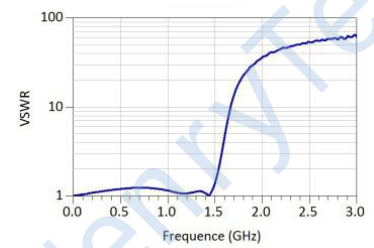
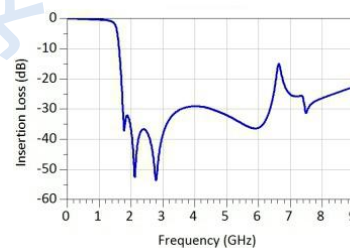
Electrical Specifications(1) at 25°C

Parameter		Frequency (MHz)	Min.	Typ.	Max.	Unit
Pass Band	Insertion Loss	DC-1325	-	0.8	1.0	dB
	Freq. Cut-Off	1590	-	3.0	-	dB
	VSWR	DC-1325	-	1.2	1.5	:1
Stop Band	Rejection Loss	2100	25	32	-	dB
		2200	30	35	-	dB
		4250	25	28	-	dB
	VSWR	2100-4250	-	20	-	:1

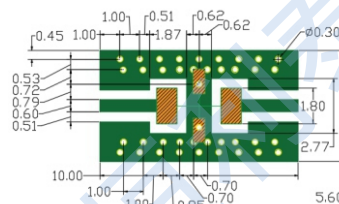
1.In Application where DC voltage is present at either input or output ports, coupling capacitors are required.

Typical Performance Data at 25°C

Frequency (MHz)	Insertion Loss (dB)	VSWR (:1)
50	0.06	1.02
500	0.24	1.20
750	0.35	1.23
1200	0.62	1.06
1320	0.83	1.12
1500	2.00	1.39
2000	36.73	36.07
2270	38.40	46.71
2570	39.40	55.64
3024	37.64	60.94
4042	29.20	71.07
5000	32.09	68.75
6000	36.27	53.14
7000	25.21	46.76
8000	26.39	48.58



Suggested PCB Layout



Maximum Ratings

Operating Temperature	-55°C to +100°C
Storage Temperature	-55°C to +100°C
RF Power Input*	9W max at 25°C

*Passband rating, derate linearly to 3.5W at 100°C ambient. Permanent damage may occur if any of these limits are exceeded.

- NOTES: 1. COPLANAR WAVEGUIDE PARAMETERS ARE SHOWN FOR ROGERS RO4350 WITH THICKNESS .508" $\pm$.0015". COPPER: 1/2 OZ. EACH SIDE. FOR OTHER MATERIALS TRACE WIDTH & GAP MAY NEED TO BE MODIFIED.
2. BOTTOM SIDE OF THE PCB IS CONTINUOUS GROUND PLANE.
- DENOTES PCB COPPER LAYOUT WITH SMOBC (SOLDER MASK OVER BARE COPPER)
- DENOTES COPPER LAND PATTERN FREE OF SOLDER MASK

Tel: +86-28-85589664

E-mail: mr_li@henglitaitech.com

Web: www.hltchips.com

Room 1401, 14th Floor, Building 5, Huaxi Science and Technology Innovation Center, No. 50, Shuixi Road, Jinniu District, Chengdu City