RoHS



# JL3T550V120RE3E7SN

#### LE3 Pack Module with NCE Gen.7 Trench/Fieldstop IGBT and Emitter Controlled diode and NTC

#### **Features**

- Neutral Point Clamped Three-Level Inverter Module
- Low Inductive Layout
- Solderable Pins

#### **Benefits**

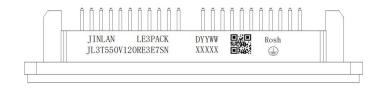
- Higher System Efficiency
- Reduced Cooling Requirements
- Low Conduction Losses Over Temperature

LE3 Pack

#### MARKING DIAGRAM

## **Typical Applications**

- Solar Inverters
- 3-level-applications Converters
- UPS Systems



JINLAN = Company Name

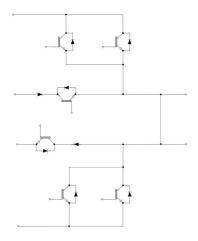
JL3T550V120RE3E7SN = Specific Device Code

YYWW = Year and Work Week Code

XXXXX = Serial Number

QR code = Custom Assembly Information

## **Description**



# Package Insulation coordination

Parameter	Symbol	Note or test condition	Values	Unit
Isolation test voltage	V <sub>ISOL</sub>	RMS,f=50Hz,t=60s	2.5	kV
Creepage distance	d <sub>creep</sub>	terminal to heatsink	>12	mm
Clearance	d <sub>clear</sub>	terminal to heatsink	>11	mm
Comparative tracking index (electrical)	СТІ		≥500	

# **Package Characteristic values**

Parameter	Symbol	Note or test condition			Values		Unit
Parameter	Symbol	Note of test condition		Min.	Тур.	Max.	Oilit
Stray Inductance	Lce			-	15	ı	nH
Mounting torque for module mounting	М	-Mounting according to valid application note	M5, Screw	3		5	Nm
Storage Temperature Range	Тѕтс			-40		125	°C
Flatness of base plate						0.3	mm

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# IGBT(Q1,Q4)

## Absolute Maximum Ratings (Tc = 25°C unless otherwise noted)

Symbol	Description	Value	Unit
V <sub>CES</sub>	Collector-Emitter Voltage	1200	V
$V_{\text{GES}}$	Gate-Emitter Voltage	±20	V
Ic	Continuous Collector Current @ T <sub>J</sub> = 150°C	550	Α
I <sub>CP</sub>	Pulsed Collector Current @ T <sub>J</sub> = 150°C	1100	Α

#### Characteristics (Tc = 25°C unless otherwise noted)

Symbol	Parameter	Test Condi	tion	Min	Тур	Max	Unit
	Callantes Freittes Catumatics Voltage	\/ - 15\/   -550 A	T <sub>J</sub> = 25°C		1.80		.,
V <sub>CE(sat)</sub>	Collector-Emitter Saturation Voltage	V <sub>GE</sub> = 15 V, I <sub>C</sub> =550 A	T <sub>J</sub> = 150°C		2.10		V
$V_{\text{GE(TH)}}$	Gate-Emitter Threshold Voltage	V <sub>GE</sub> = V <sub>CE</sub> , I <sub>C</sub>	=9 mA		5.25	-	V
I <sub>CES</sub>	Collector-Emitter Cutoff Current	V <sub>GE</sub> = 0 V, V <sub>CE</sub> :	= 1200 V			150	μA
$I_{GES}$	Gate Leakage Current	V <sub>GE</sub> = 20 V, V <sub>0</sub>	<sub>CE</sub> = 0 V		-	100	nA
$Q_g$	Total Gate Charge	V <sub>CE</sub> = 600 V, V <sub>G</sub>	<sub>E</sub> = ± 15 V		2.21		μC
Ciss	Input Capacitance	.,	/ f 400 H-		64.2	-	nF
C <sub>rss</sub>	Reverse Transfer Capacitance	$V_{CE} = 30 \text{ V}, V_{GE} = 0 \text{ V}$	V, I = 100 KHZ		0.32		nF
t <sub>d(on)</sub>	Turn-On Delay Time (inductive load)				97		
tr	Rise Time (inductive load)	T. <sub>1</sub> = 25°	C		22		
t <sub>d(off)</sub>	Turn-off Delay Time (inductive load)	V <sub>CE</sub> =600V, I <sub>C</sub>	=300A		135		ns
t <sub>f</sub>	Fall Time (inductive load)	$V_{GE}$ = -7 V to +15 V, $R_{Gon}$ = $2\Omega$ , $R_{Goff}$ =2 $\Omega$			20		
E <sub>on</sub>	Turn-On Switching Loss per Pulse				9.76		
E <sub>off</sub>	Turn Off Switching Loss per Pulse				13.17		m.
t <sub>d(on)</sub>	Turn-On Delay Time				107		
t <sub>r</sub>	Rise Time				35		
t <sub>d(off)</sub>	Turn-off Delay Time	$T_J = 25^{\circ}$ $V_{CE} = 600V, I_{CE}$			165		ns
t <sub>f</sub>	Fall Time	$V_{GE} = -7 \text{ V to } +15 \text{ V}$ $R_{Goff} = 29$	$R_{Gon} = 2\Omega$		47		
Eon	Turn-on Switching Loss per Pulse	T\Goff-23	.2		19.5		
E <sub>off</sub>	Turn Off Switching Loss per Pulse				21.3		m.
Ciss	Input Capacitance	V <sub>CE</sub> = 30 V, V <sub>GE</sub> = 0 V, f = 100 kHz			64.2		_
C <sub>rss</sub>	Reverse Transfer Capacitance				0.32		nF
R <sub>thJC</sub>	Thermal Resistance - Chip-to-Case	Junction-to-Case (per diode)			0.053		°C/\
T <sub>vj op</sub>	Temperature under switching conditions			-40		175 <sup>1)</sup>	°C

 $<sup>^{1)}</sup>T_{vj\,op}$  > 150  $^{\circ}$ C is only allowed for operation at overload conditions. For detailed specifications please refer to AN 2018-14.



## Diode (D1, D4)

#### Absolute Maximum Ratings (Tc = 25°C unless otherwise noted)

Symbol	Description	Value	Unit
V <sub>RRM</sub>	Peak Repetitive Reverse Voltage	1200	V
l <sub>F</sub>	Continuous Forward Current @T <sub>J</sub> = 150°C	400	Α
I <sub>FRM</sub>	Repetitive Peak Forward Current @T <sub>J</sub> = 150°C	800	Α

## Characteristics (Tc=25℃ unless otherwise noted)

Symbol	Parameter	Test Cor	ndition	Min	Тур	Max	Unit
	Diode Forward Voltage	1 400 A	T <sub>J</sub> = 25°C		1.50		.,
V <sub>F</sub>		I <sub>F</sub> = 400 A	T <sub>J</sub> = 150°C		1.40		V
Qrr	Reverse Recovery Charge	T <sub>J</sub> = 2			1.9		μC
I <sub>RRM</sub>	Peak Reverse Recovery Current	$V_{CE}$ =600V, I <sub>C</sub> =300A $V_{GE}$ = -7 V to +15 V, R <sub>Gon</sub> = 2 $\Omega$ , $R_{Goff}$ =2 $\Omega$			24.775		Α
E <sub>REC</sub>	Reverse Recovery Energy				1.755		mJ
$Q_{rr}$	Reverse Recovery Charge	$\begin{array}{c} T_{J}\!=\!150^{\circ}\text{C} \\ V_{CE}\!=\!600\text{V, }I_{C}\!=\!300\text{A} \\ V_{GE}\!=\!-\!7\text{ V to }\!+\!15\text{ V, }R_{Gon}\!=\!2\Omega, \\ R_{Goff}\!=\!2\Omega \end{array}$			5.91		μC
I <sub>RRM</sub>	Peak Reverse Recovery Current				35.45		Α
E <sub>REC</sub>	Reverse Recovery Energy				4.345		mJ
$R_{thJC}$	Thermal Resistance - Chip-to-Case	Junction-to-Case (per diode)			0.083	ı	°C/W
$T_{vj \ op}$	Temperature under switching conditions			-40		175 <sup>2</sup>	°C

<sup>&</sup>lt;sup>2)</sup>T<sub>vj op</sub> > 150 ℃ is only allowed for operation at overload conditions. For detailed specifications please refer to AN 2018-14.



# IGBT(Q2,Q3)

## Absolute Maximum Ratings (Tc = 25°C unless otherwise noted)

Symbol	Description	Value	Unit
V <sub>CES</sub>	Collector-Emitter Voltage	750	V
V <sub>GES</sub>	Gate-Emitter Voltage	±20	V
Ic	Continuous Collector Current @ T <sub>J</sub> = 150°C	400	Α
I <sub>CP</sub>	Pulsed Collector Current @ T <sub>J</sub> = 150°C	800	Α

#### Characteristics (Tc = 25°C unless otherwise noted)

Symbol	Parameter	Test Condition		Min	Тур	Max	Unit
.,	Callantan Freitten Octoration Vallana	V 45 V 1 400 A	T <sub>J</sub> = 25°C		1.55	2.00	.,
$V_{CE(sat)}$	Collector-Emitter Saturation Voltage	V <sub>GE</sub> = 15 V, I <sub>C</sub> =400 A	T <sub>J</sub> = 150°C		1.85		V
V <sub>GE(TH)</sub>	Gate-Emitter Threshold Voltage	V <sub>GE</sub> = V <sub>CE</sub> , I	=5 mA		5.25		V
I <sub>CES</sub>	Collector-Emitter Cutoff Current	V <sub>GE</sub> = 0 V, V <sub>CI</sub>	= 750 V			100	μA
I <sub>GES</sub>	Gate Leakage Current	V <sub>GE</sub> = 20 V, V	<sub>CE</sub> = 0 V			100	nA
$Q_g$	Total Gate Charge	V <sub>CE</sub> = 600 V, V	<sub>GE</sub> = ± 15 V		0.775		μC
Ciss	Input Capacitance				23.65		
Coss	Output Capacitance	$V_{CE} = 30 \text{ V}, V_{GE} = 0$	V, f = 100 kHz		0.785		nF
Crss	Reverse Transfer Capacitance				0.145		
$t_{d(on)}$	Turn-On Delay Time	$V_{CE}$ =400V, $I_{C}$ =300A $V_{GE}$ = -7 V to +15 V, $R_{Gon}$ = 2 $\Omega$ , $R_{Goff}$ =2 $\Omega$			62		
t <sub>r</sub>	Rise Time				25		ns
$t_{d(off)}$	Turn-off Delay Time				250		115
t <sub>f</sub>	Fall Time	T <sub>J</sub> = 25	°C		38		
Eon	Turn-On Switching Loss per Pulse				2.50		
$E_{off}$	Turn Off Switching Loss per Pulse				1.12		mJ
$t_{d(on)}$	Turn-On Delay Time				TBD		
t <sub>r</sub>	Rise Time	V <sub>CE</sub> =400V, I	c =300A		TBD		no
$t_{\text{d(off)}}$	Turn-off Delay Time	V <sub>GE</sub> = -7 V to +15 \	$/$ , $R_{Gon} = 2\Omega$ ,		TBD		ns
$\mathbf{t}_{f}$	Fall Time	R <sub>Goff</sub> =2Ω T <sub>J</sub> = 150°C			TBD		
Eon	Turn-on Switching Loss per Pulse				3.37		1
E <sub>off</sub>	Turn Off Switching Loss per Pulse				1.75		mJ
$R_{thJC}$	Thermal Resistance - Chip-to-Case	Junction-to-Case (per diode)			0.099		°C/W
T <sub>vj op</sub>	Temperature under switching conditions			-40		175 <sup>3</sup>	°C

 $<sup>^{3)}</sup>T_{vj\,op}$  > 150  $^{\circ}$ C is only allowed for operation at overload conditions. For detailed specifications please refer to AN 2018-14.



## Diode (D2, D3)

# Absolute Maximum Ratings (Tc = 25°C unless otherwise noted)

Symbol	Description	Value	Unit
$V_{RRM}$	Peak Repetitive Reverse Voltage	750	V
l <sub>F</sub>	Continuous Forward Current @T <sub>J</sub> = 150°C	100	Α
I <sub>FRM</sub>	Repetitive Peak Forward Current @T <sub>J</sub> = 150°C	200	Α

#### Characteristics (Tc=25℃ unless otherwise noted)

Symbol	Parameter	Test Condition		Min	Тур	Max	Unit
Diada Famuand Valtana	1 100 1	T <sub>J</sub> = 25°C		1.45		.,	
V <sub>F</sub>	Diode Forward Voltage	I <sub>F</sub> = 100 A	T <sub>J</sub> = 150°C		1.35		V
R <sub>thJC</sub>	Thermal Resistance - Chip-to-Case	Junction-to-Case (per diode)			0.357		°C/W
T <sub>vj op</sub>	Temperature under switching conditions			-40		175 <sup>4</sup>	°C

<sup>&</sup>lt;sup>4)</sup>T<sub>vj op</sub> > 150 ℃ is only allowed for operation at overload conditions. For detailed specifications please refer to AN 2018-14



## **Diode** (D5, D6)

#### Absolute Maximum Ratings (Tc = 25°C unless otherwise noted)

Symbol	Description	Value	Unit
$V_{RRM}$	Peak Repetitive Reverse Voltage	750	V
l <sub>F</sub>	Continuous Forward Current @ T <sub>c</sub> = 75°C (T <sub>J</sub> = 150°C)	500	Α
I <sub>FRM</sub>	Repetitive Peak Forward Current@ T <sub>J</sub> = 150°C	1000	Α

#### Characteristics (Tc=25℃ unless otherwise noted)

Symbol	Parameter	Test Condi	Min	Тур	Max	Unit	
.,			T <sub>J</sub> = 25°C		1.50	-	.,
V <sub>F</sub>	Diode Forward Voltage	$I_F = 500 \text{ A}, V_{GE} = 0 \text{ V}$	T <sub>J</sub> = 150°C		1.40	-	V
Q <sub>rr</sub>	Reverse Recovery Charge	$V_{CE}$ =400 V, $I_{C}$ =300A $V_{GE}$ = -7 V to +15 V, $R_{Gon}$ = 2 $\Omega$ , $R_{Goff}$ =0.5 $\Omega$ , $T_{J}$ = 25°C			6.07	ı	μC
I <sub>RRM</sub>	Peak Reverse Recovery Current				130		Α
E <sub>REC</sub>	Reverse Recovery Energy				1.62	1	mJ
Qrr	Reverse Recovery Charge	V <sub>CE</sub> =400 V, Io	: =300A		13.87	-	μC
I <sub>RRM</sub>	Peak Reverse Recovery Current	$V_{GE} = -7 \text{ V to } +15 \text{ V}$	$R_{Gon} = 2\Omega$		169	1	Α
E <sub>REC</sub>	Reverse Recovery Energy	$R_{Goff}$ =0.5 $\Omega$ , $T_{J}$ = 175°C			4.42	-	mJ
RthJC	Thermal Resistance - Chip-to-Case	Junction-to-Case (per diode)			0.1		°C/W
T <sub>vj op</sub>	Temperature under switching conditions			-40		175 <sup>4</sup>	°C

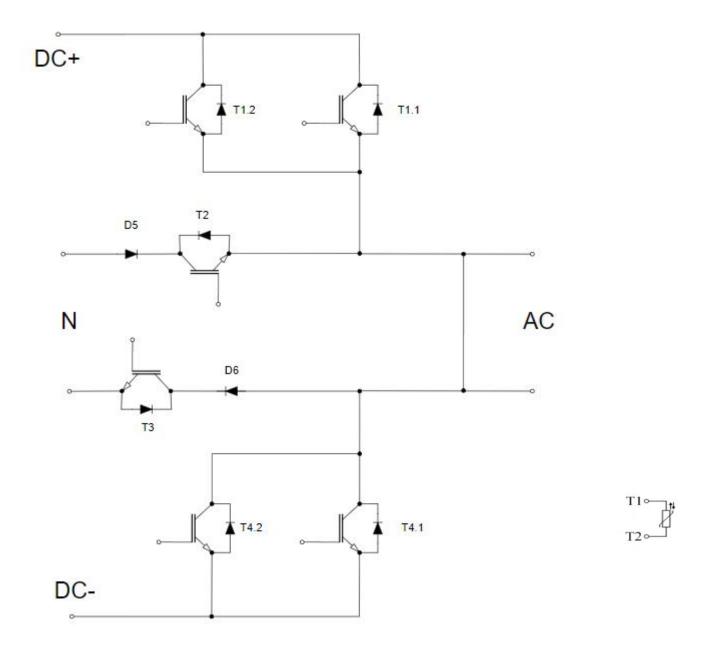
<sup>&</sup>lt;sup>5)</sup>T<sub>vjop</sub> > 150 °C is only allowed for operation at overload conditions. For detailed specifications please refer to AN 2018-14

## NTC Characteristics (Tc = 25°C unless otherwise noted)

Symbol	Parameter	Test Condition	Min	Тур	Max	Unit
R <sub>25</sub>	Rated Resistance	T <sub>C</sub> = 25°C		5		kQ
ΔR/R	Deviation of R100	T <sub>C</sub> =100 °C,R <sub>100</sub> =493Ω	-5		5	%
B <sub>25/50</sub>	B-value	$R_2=R_{25} \exp[B_{25/50}(1/T_2-1/(298.15K))]$		3375		K
B <sub>25/100</sub>	B-value	R <sub>2</sub> =R <sub>25</sub> exp[B <sub>25/100</sub> (1/T <sub>2</sub> - 1/(298.15K))]		3433		K

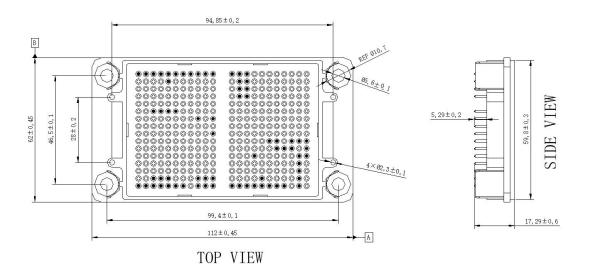


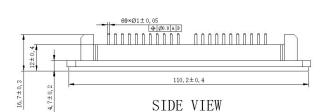
#### **CIRCUIT DIAGRAM**

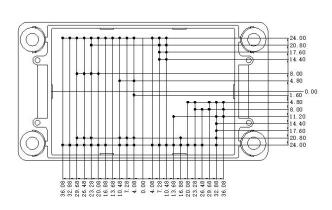


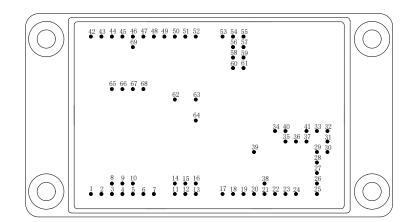


## **PACKAGE DIMENSIONS**









PIN	PIN Definition	
1~10	DC+	
11~24	N	
25~37	DC-	
38	G3	
39	C3	
40	G4.2	
41	G4.1	
42~61	AC	
62	C2	
63	T1	
64	T2	
65	E1.2	
66	G1.2	
67	G1.1	
68	E1.1	
69	G2	



# Jinlan Power Semiconductor(Wuxi).co.,LTD

# **REVISION HISTORY**

Document version	Date of release	Description of changes
Rev.00	2024-9-03	Preview



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