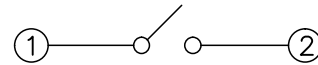
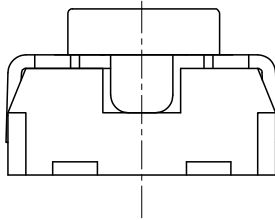
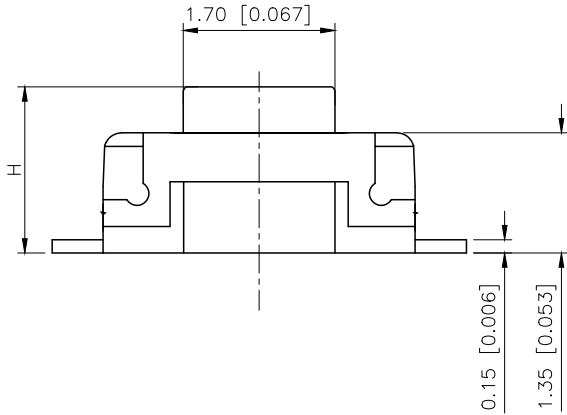
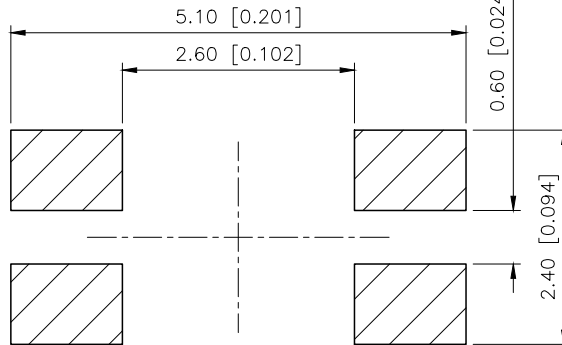
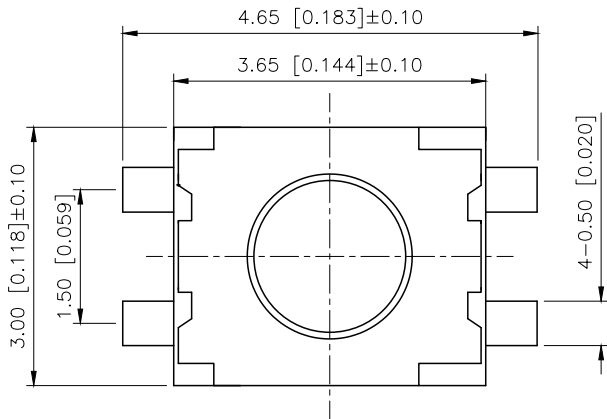


REVISIONS					
REV.	ECN NO.	LOCATION	DESCRIPTION	DATE	DESIGN
A0	N/A		INITIAL RELEASE	/	/
A1	N/A		图面升级整理图面	2025.06.05	阿树

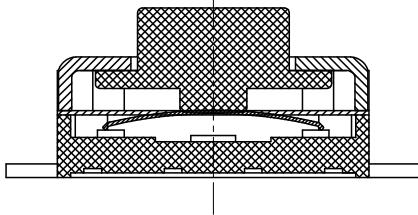


Circuit Diagram

SPECIFICATION
Contact Rating: 50mA,12V DC
Contact Resistance: 100mΩ max
Insulation Resistance: 100MΩ min.500V DC
Dielectric Strength: 250V AC/1minute
Travel: 0.13mm
Operating Temp: -30℃~+85℃
Storage Temp: -30℃~+80℃



PCB LAYOUT



PAPT NO	TS-1185EC-X-X-X
Operating Force	A:100gf B:160gf C:260gf
Height	H:A:1.8 B:1.85 C:1.92 D:2.0 E:2.5
Fixed column	B:black W:white

⑤	Terminal	4	Brass (0.30t)	Plating silvering
④	Contact	1	F.ag/SUS	Natural
③	Base	1	LCP	
②	Cover	1	Brass	Plating Nickel
①	Keystake	1	LCP	
ITEM	PAPT NAMF	QTY	MATERIAL	FINISHING

AuGLAR	±5°
X.	±0.40
X.x	±0.30
X.xx	±0.20
X.xxx	±0.15
UNSPECIFIED TOLERAUCES	



DESIGN	阿树	VIEW:		MODEL TYPE:	TACT SWITCH
CHECKED		UNIT:	mm/in	PART NO.:	TS-1185EC-X-X-X
APPROVED		SIZE:	A4	DWG NO.:	